

Cuaga Force

ICSP

To load code to the board.

- MISO - PB2
- MOSI - PB1
- SCK - PC1
- RESRT - PC3

I2C

For master slave communication with mega.

- SDA - PB1
- SDL - PC1
- HANDSHAKE - PB3

DIRECTION

- PA1
- PA2

ENCODERS

- INT1 - PB0
- INT2 - PC2

DRIVER

- PWM - PA5
- ENABLE - PA3
- CS - PA4
- INA - PA6

- INB - PA7

LEDS

- LED1 - PA0
- LED2 - PC0

PINOUT

PIN	USE
PA0	LED1
PA1	DIRECTION1
PA2	DIRECTION2
PA3	ENABLE
PA4	CS
PA5	PWM
PA6	INA
PA7	INB
PB0	INT1
PB1	MOSI && SDA
PB2	MISO
PB3	HANDSHAKE
PC0	LED2
PC1	SCL && SCK
PC2	INT2
PC3	RESET
PC4	CLOCK
PC5	CLOCK