Архітектура комп'ютера

Лабораторна робота №3

Програмне формування затримки КР1816ВЕ48

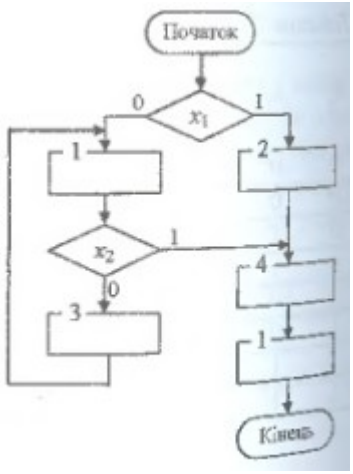
Виконав : Ромас А гр. ІО-01

```
            Sel    Rb0

            Mov    A, #0
            Anld   P5, A

            Movd   A, P4
            Jb1    Top2

; top1

Top1:

            Mov    A, #7       ; 5
            Orld   P5, A       ; 5

            Nop
            Nop

            Mov    A, #5
            Anld   P5, A

; y2 delay finished

; delay for y1 222

            Mov    A, #FEH     ; 5
            Mov    T, A        ; 2.5
            Strt   T           ; 2.5
; begin 160
Lbl1:
            Jtf    End1
            Jmp    Lbl1

End1:


; end 160

            Nop

; begin 30
            Mov    R7, #3      ; 5
Lbl2:       Djnz   R7, Lbl2
;end 30

            Mov    A, #4
            Anld   P5, A

; delay for y1 ended

            Mov    A, #FBH
            Mov    T, A
            Strt   T

Lbl4:

            Jtf    End2
            Jmp    Lbl4

End2:

            Mov    R7, #13

Lbl22:      Djnz   R7, Lbl22

            Nop

            Mov    A, #0
            Anld   P5, A

; delay for y3 ended
```

h_4	h_5	h_1	Часові затримки: Управляючи сигнали y_1	y_2	y_3	y_4	y_5
0	0	0	720	25	400	10	800
0	0	1	560	12	700	330	280
0	1	0	100	725	15	22	45
0	1	1	40	240	100	50	150
1	0	0	18	280	80	720	60
1	0	1	400	35	800	180	12
1	1	1	222	15	720	500	560

h_2	h_3	Управляючі сигнали: Номер операторної вершини 1	2	3	4
0	0	y_1y_2	y_1y_3	y_4y_5	y_3
1	0	$y_1y_2y_3$	y_2	y_4y_1	y_4

n_2	n_4	Порт
0	0	*BUS*
0	1	*P1*
1	1	*P4, P5*



```
            Movd    A, P4
            Jb2     Top4

; top3

Top3:

            Mov     A, #9
            Orld    P5, A

            Mov     A, #FEH
            Mov     T, A
            Strt    T

Somelbl0:   Jtf     Somelbl3
            Jmp     Somelbl0

Somelbl3:

            Mov     R7, #6
Somelbl1:   Djnz    R7, Somelbl1

;somelbl2:
            Nop
            Mov     A, #8
            Anld    P5, A

; delay for y1 ended

            Mov     A, #FDH
            Mov     T, A
            Strt    T

Somelbl2:   Jtf     Somelbl4
            Jmp     Somelbl2

Somelbl4:

            Nop
            Nop
            Nop
            Nop
            Nop

            Mov     A, #0
            Anld    P5, A

; delay for y4 ended

            Jmp     Top1

; top2

Top2:

            Mov     A, #2
            Orld    P5, A

            Nop
            Nop

            Mov     A, #0
            Anld    P5, A

; delay for y2 ended

; top4

Top4:

            Mov     A, #4
            Orld    P5, A

            Mov     A, #FBH
            Mov     T, A
            Strt    T

Somelbl5:   Jtf     Somelbl6
            Jmp     Somelbl5
```

```
Somelbl6:

          Mov    R7, #14
Somelbl7: Djnz   R7, Somelbl7

; delay for y4 ended


          Mov    A, #0
          Anld   P5, A

; top11

Top11:

          Mov    A, #7      ; 5
          Orld   P5, A      ; 5

          Nop
          Nop

          Mov    A, #5
          Anld   P5, A

; y2 delay finished

; delay for y1 222

          Mov    A, #FEH    ; 5
          Mov    T, A       ; 2.5
          Strt   T          ; 2.5
; begin 160
Ll1:
          Jtf    En1
          Jmp    Ll1

En1:


; end 160

          Nop

; begin 30
          Mov    R7, #3     ; 5
Ll2:      Djnz   R7, Ll2
;end 30

          Mov    A, #4
          Anld   P5, A

; delay for y1 ended

          Mov    A, #FBH
          Mov    T, A
          Strt   T

Ll4:

          Jtf    En2
          Jmp    Ll4

En2:

          Mov    R7, #13

Ll22:     Djnz   R7, Ll22

          Nop

          Mov    A, #0
          Anld   P5, A

; delay for y3 ended

          End
```