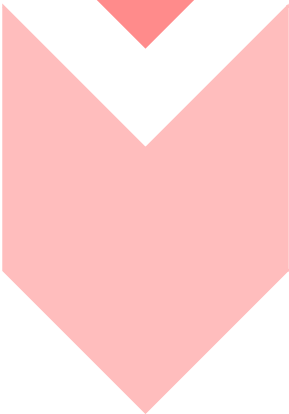- External Reference
- Inverter Chain PLL

- Interlinked PLLs
- RF Capable FPGA

- Build/Test Network
- Analysis of Results