Sai Emmani

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PROFESSIONAL SUMMARY

Passionate engineer aspiring to be an enthusiastic solution developer and participate in development of Cisco's intelligent systems.

SKILLS

- C,C++
- SQL
- Python
- Crystal Reports
- Agile PLM
- Linux
- PyTests
- Matlab

EXPERIENCE

SOFTWARE ENGINEER

11/2015 to 08/2017

Tech Mahindra | Hyderabad, Telagana

Responsibilities

- Evaluated user end problems and issues with the software performance.
- Established program modifications through testing each phase.
- Implemented and updated bug tracking procedures.
- Ensured compliance with testing standards.
- Involved in User requirements, System requirements, and Design Specification reviews of the project.

EDUCATION

Masters of Engineering | Electrical And Computer Engineering *2017-2019*
Concordia University, Montréal, QC

Bachelors of Technology | Electrical and Electronics Engineering 2011-2015
K L University, Vijayawada,INDIA

INTERNSHIP

Bharat Heavy Electricals Limited (BHEL) 2014

- Studied different Generators and motors on how they are tested nad designed.
- Got to learn more about Constructional features of turbine .

PROJECTS

1.GSK (Glaxo Smith Kline)

- Worked closely with real-time business workflows and requirements of the project
- Created changes and managed the artwork through Agile PLM (Product Life Cycle Management) and AMS (Artwork Management System) in ITIL
- Tested developed work in a validated environment through user-acceptance testing and tracking defects
- Followed through customization of integrations in Oracle Agile PLM.
- Conducted regression testing, analyzed results and submitted observations

2.Mini MIPS

- Designed a pipelined 32-bit Mini-Mips processor based on RISC architecture.
- Implemented the whole design in RTL code as five blocks which are instruction fetch, instruction decode, execution, memory fetch and write back stages in Verilog.
- Implemented multiple test cases in testbench to verify functional coverage of whole hardware design at block level and system level for easy verification.
- Designed a separate control unit in Verilog code at register transfer level to control data path in all five stages by generating various control signals to respective stages according to dependency checking.

3.Fault Identification System for Transmission lines using Artificial Neural Network

Transmission lines among the other electrical power system component suffer from unexpected failure due to various causes. A fault occurs on transmission line when two or more conductors come in contact with each other or ground. Ground faults are considered as one of the main problems in power systems and account for more than 80% of all faults.

- In this system output of the Transmission line is used to train an artificial neural network to detect the transmission line faults.
- Fault detection has been achieved by using artificial neural network through a protective relaying pattern classifier system and a new scheme is proposed for fast and reliable fault detection.
- An ANN-based system is proposed in designed for fault detection. An improved performance is experienced once the neural network is trained sufficiently and suitably in MATLAB.