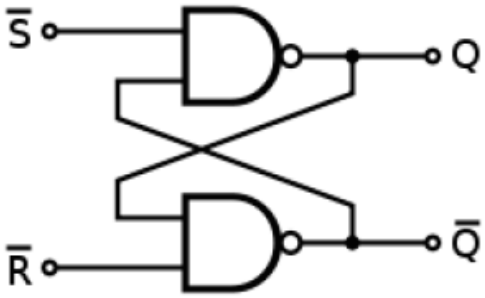
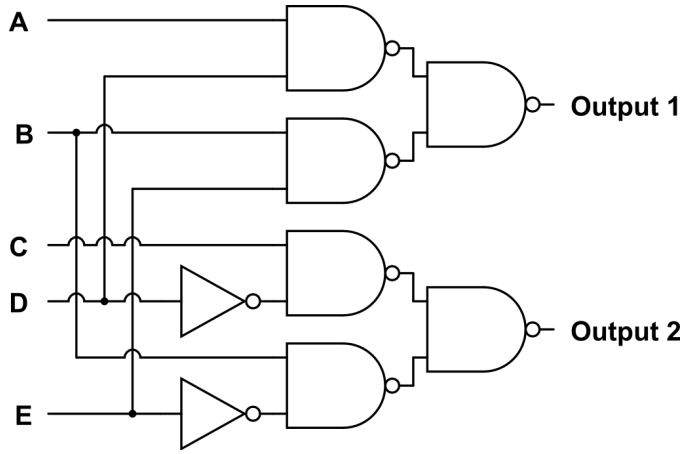
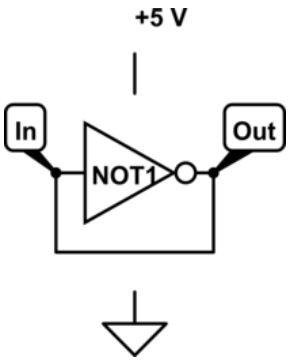
>>>> Reference Page – you may separate this page from the other pages <<<<

S'R' Latch Characteristic Table

S'	R'	Q(t)	Q(t+1)
1	1	X	Q(t)
0	1	X	1 (set)
1	0	X	0 (reset)
0	0	X	Not allowed



Circuit A:



Circuit B: