

Yices 2.6

Bruno Dutertre and Dejan Jovanović
SRI International

SMTCOMP 2017
Heidelberg, Germany

Yices 2.6 in SMTCOMP 2017

Status

- No major change since last year
- Supports linear and non-linear arithmetic, arrays, UF, bitvectors
- Limited quantifier reasoning: $\exists\forall$ fragments for bitvector, LRA
- Includes two types of solvers: classic DPPL(T) + MC-SAT

Entered in all the divisions that Yices supports

- **Main track:** Quantifier-free logics including linear and nonlinear arithmetic, bitvectors, and combination with UF and Arrays.
- **Application track:** Same logics, except that the MC-SAT solver is not incremental yet.

What's New

Yices2



New Licence

- Yices 2 is now GPL

Distributions

- Prebuilt binaries + source tarfile at <http://yices.csl.sri.com>
- Git repository on Github <https://github.com/SRI-CSL/yices2>
- Ubuntu/Debian package
- Homebrew package for MacOS X

What's Next

MC-SAT Extensions

- Add support for incremental solving
- Extends MC-SAT to bitvector problems

CDCL Solver

- New sat solver in progress

Miscellaneous

- Complete support for SMT-LIB 2.6
- Fix some API issues