



`http://www.veriT-solver.org`

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What is new (not yet in the SMT-COMP version):

- ▶ cleaning, efficiency improvements
- ▶ λ -free Higher-order
- ▶ improved quantifier handling (ML, instantiation, superposition)
- ▶ better proofs

Goals:

- ▶ clean, small SMT for UF(N|L)IRA with quantifiers and proofs
- ▶ for verification platforms B, TLA+