

Yices 2.6 in SMTCOMP 2018

Yices 2

- Supports linear and non-linear arithmetic, arrays, UF, bitvectors
- Includes two types of solvers: classic DPPL(T) + MC-SAT
- <https://github.com/SRI-CSL/yices2>

New in 2018

- Unsat cores
- Incremental MC-SAT

Entered in all the divisions that Yices supports

- **Main/application track:** Quantifier-free logics including linear and nonlinear arithmetic, bitvectors, and combination with UF and Arrays.
- **Unsat core track:** Same logics, except that unsat cores are not yet supported by MC-SAT (i.e., nonlinear arithmetic)

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