



<http://www.veriT-solver.org>

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What is new:

- ▶ cleaning, efficiency improvements, e.g. UF (space for improvement)
- ▶ (much) improved quantifier handling
- ▶ Other w.i.p.: (N|L)RA (Redlog), quantifier handling, proofs

Goals:

- ▶ clean, small SMT for UF(N|L)IRA with quantifiers and proofs
- ▶ for verification platforms B, TLA+