

CVC4 at the SMT Competition 2018

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Divisions

This year's configuration of CVC4 enters **all divisions** in **all tracks**.

New Features / Improvements

- **New:** Floating-Point Solver
- **New:** Novel approach for Quantified Bit-Vectors
- **New:** Experimental division QF_SLIA (strings)
- Eager Bit-Blasting Solver with CaDiCaL as back end
- Heuristic Approaches for Non-Linear Arithmetic with CaDiCaL as back end
- Improvement of quantifier instantiation

Experimental Configuration CVC4-experimental-idl-2

- non-competitive
- specialized IDL solver, entered division QF_IDL of the main track