

The Impact of Quantum Computing on RSA

Stephen Campbell

sac170630@utdallas.edu

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Abstract—

I. INTRODUCTION

EXPERTISE

II. RSA

Public key cryptosystems are the basis of secure communication on the internet. The Rivest-Shamir-Adleman (RSA) [1] public key cryptosystem is prominently used on the modern internet. In fact, it is currently being used in the browser I am writing this in! Understanding how RSA functions and how Quantum Computing "breaks" it provides tantamount evidence of the application of the computational power leveraged by quantum computers.

A. Purpose

In a public key cryptosystem, a participant generates a pair of keys : the public key and the private key. The public key is openly distributed and allows anyone to encrypt information. Ideally, this encrypted information can only be decrypted with the private key. This ideal cryptosystem does not exist. The goal of RSA and of modern public key cryptosystems, is to create a practical methodology to generate a public and private key such that it is effectively impossible to decrypt encrypted data without the private key.

B. Mechanics

There are 3 major computational components of the RSA cryptosystem: key generation, encryption, and decryption. As a note on general terminology, an encrypted message is referred to as a ciphertext. The setup of RSA is that a public key, in the form a tuple of numbers (e, n) , is generated and distributed. The private key is also a kept as tuple of numbers (d, n) ;however, this key is not distributed . A concise explanation of the ;cryptosystem follows.

1) *Encryption*: The encryption step of RSA is one fairly straightforward calculation. The ciphertext c is generated from the message m by the below equation.

$$c := m^e \bmod n$$

2) *Decryption*: The decryption step of RSA is also a straightforward calculation. The ciphertext is decrypted by the below equation.

$$0 \leq m < n$$

$$(m^e)^d \equiv m \pmod{n}$$

3) *Key generation*: The steps of encryption and decryption feel magical yet rely on very unique properties that exist between the modulus, the encryption key, and the decryption key. These special properties will be developed shortly. First, there are important theorems that are utilized.

Euler's Totient Function

$$n = p_1^{k_1} p_2^{k_2} \dots p_r^{k_r}$$

$$\varphi(n) = p_1^{k_1-1}(p_1 - 1)p_2^{k_2-1}(p_2 - 1) \dots p_r^{k_r-1}(p_r - 1)$$

Euler's Theorem

$$\gcd(n, a) = 1 \implies a^{\varphi(n)} \equiv 1 \pmod{n}$$

The major idea is shown below.

$m :=$ Message

Choose

$$n : 0 \leq m < n$$

Then

$$m = m \left(m^{\varphi(n)} \right) \bmod n \text{ Assume } m, n \text{ coprime}$$

$$m = m \left(m^{\varphi(n)} \right)^h \bmod n$$

$$m = m^{h\varphi(n)+1} \bmod n$$

$$m^{h\varphi(n)+1} = m^{ed} \bmod n$$

$$h\varphi(n) + 1 = ed, \quad h \in \mathbb{Z}$$

$$ed = 1 \bmod \varphi(n)$$

Choose

$$e : 0 < e < \varphi(n), \quad \gcd(e, \varphi(n)) = 1$$

Find Modular Inverse of e

After the encryption key e is found, the decryption d key can be easily determined by computing the modular inverse.

C. What makes this hard to crack?

As seen from the key generation step, the decryption key can be easily recovered from the encryption key and the Euler totient function $\varphi(n)$ of the modulus. The modulus and the encryption key are both distributed as the public key . The Euler totient function is extremely easily to calculate for a prime number, it is simply $p-1$;however, however for a composite the totient function requires the complete prime factorization of the composite . This provides the connection between cracking RSA and integer factorization. Furthermore, to make factoring problem harder and the cryptosystem more secure, 2 extremely large prime numbers are chosen. These

numbers have roughly half the bits of the modulus. So for a 2048 bit key, these prime numbers are 1024 bits or on the order of 10^{308} .

D. A Curious Aside

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E. Classical Cracking

One method of cracking RSA would be to factor the publicly distributed modulus. With the prime factorization of modulus in hand, one could calculate the Euler totient of the modulus, take inverse the encryption key mod the totient of the modulus and recover the decryption key. Thus the major problem lies in factoring the modulus. The goto algorithms for factoring large integers are the general number field sieve and the quadratic sieve. These general number field sieve considered slightly better for integers $> 10^{100}$ has a asymptotic running time of

$$\exp\left(\left(\sqrt[3]{\frac{64}{9}} + o(1)\right)(\ln n)^{1/3}(\ln \ln n)^{2/3}\right)$$

What does this all mean? The state of the art [2] in classical factorization is when a number compliant to RSA-250 (on the order of 10^{250}) was factored. The calculation was performed on an array of Intel Xeon Gold 6130 CPUs and the combined duration was roughly 2700 “core” years.

III. QUANTUM COMPUTING

In an effort to achieve insights into applications of quantum computing, a hands on approach to learning about the topic was taken. The Qiskit provided textbook and associated labs of <https://qiskit.org/textbook/preface.html> proved enormously helpful in shedding light on this topic in this project’s limited timeframe. Qiskit provides a python module for quantum circuit construction and simulation. This python module was utilized to create a python implementation of Shor’s algorithm. Conveyed below are key takeaways from this resource.

A. Fundamentals

Definitions

- A qubit is any two state quantum mechanical system.
- A quantum gate is a unitary transformation on the state of a set of qubits.

- A quantum computer refers to a construction of connections between qubits and quantum gates.

Many quantum gates are useful in constructing high level circuits. These include: Controlled Not, Toffoli, Pauli X,Y,Z, Hadamard, Controlled Phase, and Fredkin. The action of these gates is easily seen when visualized on the Bloch sphere. The Qiskit visualization library provides convenience methods that plot the state of the qubits in a quantum circuit. An example is depicted below. Many quantum gates are useful in constructing high level circuits. These include: Controlled Not, Toffoli, Pauli X,Y,Z, Hadamard, Controlled Phase, and Fredkin. The action of these gates is easily seen when visualized on the Bloch sphere. The Qiskit visualization library provides convenience methods that plot the state of the qubits in a quantum circuit. An example is depicted below in Fig. 1.

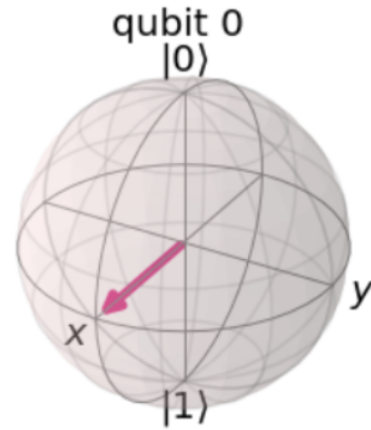


Fig. 1. A Qubit in the $|+\rangle$ state represented on the Bloch Sphere

B. Building Blocks

1) *Quantum Fourier Transform*: From a practical viewpoint, the quantum Fourier transform is a unitary transformation that maps a set of qubits encoded in binary to a set of qubits encoded in a “Fourier” basis. Essentially, this Fourier basis encodes the binary number as a phase around the z axis. A phase of 0 points along the axis. An example from the Qiskit website is shown below in Fig 2.

$$\begin{aligned} |q_3 q_2 q_1 q_0\rangle &= |0101\rangle \\ \text{QFT } |0101\rangle &= |q'_3 q'_2 q'_1 q'_0\rangle \\ q'_0 &\rightarrow \frac{1}{\sqrt{2}} \left(|0\rangle + \exp\left(2\pi i \cdot \frac{5}{2^4}\right) |1\rangle \right) \\ q'_1 &\rightarrow \frac{1}{\sqrt{2}} \left(|0\rangle + \exp\left(2\pi i \cdot \frac{5}{2^3}\right) |1\rangle \right) \\ q'_2 &\rightarrow \frac{1}{\sqrt{2}} \left(|0\rangle + \exp\left(2\pi i \cdot \frac{5}{2^2}\right) |1\rangle \right) \\ q'_3 &\rightarrow \frac{1}{\sqrt{2}} \left(|0\rangle + \exp\left(2\pi i \cdot \frac{5}{2^1}\right) |1\rangle \right) \end{aligned}$$

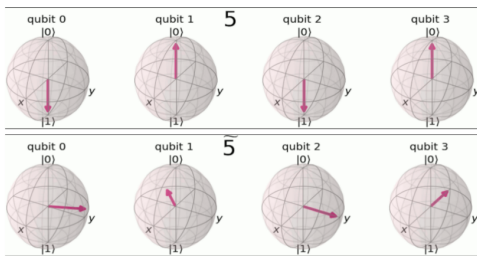


Fig. 2. The number 5 encoded in the binary basis and the Fourier basis

Constructing the quantum Fourier transform circuit relies only on 2 gates: the Hadamard and controlled phase gate. A 3 Bit QFT circuit is shown below.

2) *Quantum Phase Estimation:* The purpose of a quantum phase estimation circuit is to approximate the eigenvalue of a unitary operator's eigenstate. Given the eigenstate can be prepared and the unitary operator can be applied, the circuit allows for arbitrary precision approximation by increasing the number of approximating qubits. As to be seen, the problem of integer factorization can be reduced to estimating the phase of a unitary operator. In Shor's algorithm, Shor recommends twice the number of qubits as the number we are trying to factor for approximation.

3) *Quantum Modular Exponentiation*: As Shor notes in his praised paper [3], "The bottleneck in the quantum factoring algorithm; i.e., the piece of the factoring algorithm that consumes the most time and space, is modular exponentiation." Interestingly, the modular exponentiation algorithm he presents is simply a quantum, and thus reversible, version of a classical circuit. Implementing this modular exponentiation circuit with Qiskit proved similar to be of the acclaimed complexity. The design I implemented was heavily inspired from Vedral's exposition [4] on arithmetic operations on quantum computers. The procedure of the design was the following:

Carry and Sum Blocks

All addition and subtraction operation begin by adding the left operand's bit, the right operand's bit, and a potential "carry" bit from the previous operation. In Quantum Circuits, these blocks can be realized with toffoli and cnot gates.

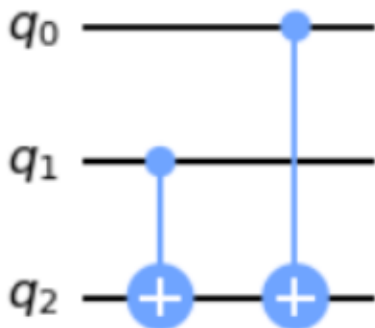


Fig. 3. A Classic Sum Block Implemented with Quantum Gates

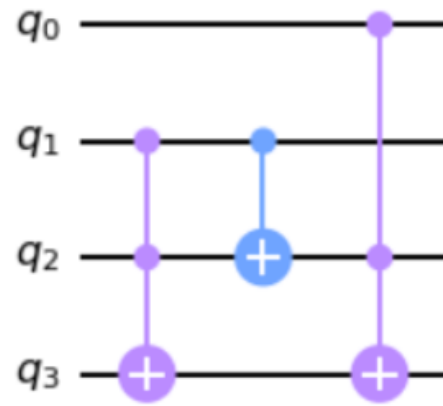


Fig. 4. A Classic Carry Block Implemented with Quantum Gates

N Qubit Adder

A n bit adder can be constructed with a cascade of sum and carry blocks. Because the circuit must be reversible, the circuit requires the use of "ancilla" bits where intermediate values are computed and "uncomputed".

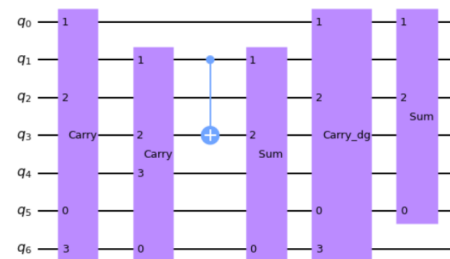


Fig. 5. A 2 Bit Adder Block Implemented with Carry and Sum Blocks

N Qubit Modular Adder

The Adder's inverse noted in the example as adder dg has the effect of subtracting the left operand from the right. This fact is utilized in the modular adder. The process of the circuit is as follows:

- 1) The modulus is placed in qubits 7 and 8, the left operand is in qubits 0-1, the right operand in 2-4, 5-6 are ancilla, qubit 9 is also ancilla
- 2) The left and right operand are added
- 3) The modulus is then subtracted from that addition
- 4) The sign bit is "copied" to the ancillary qubit 9 and the modulus is added back if necessary the sign is negative
- 5) The value of the sign bit is uncomputed to make the gate reversible

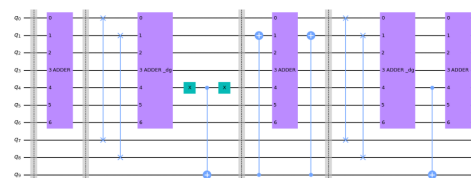


Fig. 6. A 2 Bit mod2 Adder Block Implemented with Adder Blocks

Controlled N Qubit Modular Multiplier

By "Classically Controlling" the position of the gates based on the multiplier and utilizing the properties of modular multiplication, the quantum controlled modular multiplication circuit can be realized.

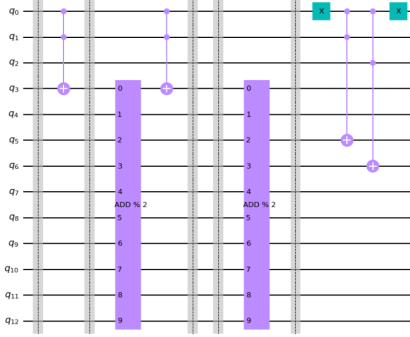


Fig. 7. A 2 Bit Mod 2 Multiply by 3 Controlled Modular Multiplier Block Implemented with Modular Adder Blocks

Controlled N Qubit Modular Exponentiation

The modular exponentiation circuit can be realized by cascading controlled modular multiplications and uncomputing ancillas when necessary.

C. Shor's Algorithm

The goal of Shor's algorithm is to find the period, r , of a number, $b \bmod n$ and with r factor n . Using a quantum computer, the period of a number $\bmod n$ can be found by using phase estimation on the unitary operator $b \cdot x \bmod N$ on a state $|x\rangle$. The eigenstate chosen to find the period is simply $|1\rangle$. The phase estimated will be some fraction whose denominator has a high probability of being the period, r , of a number, b , $\bmod n$. On a quantum computer, this algorithm shatters the asymptotic complexity:

$$O((\log n)^2(\log \log n)(\log \log \log n))$$

IV. CONCLUSION AND KEY TAKEAWAYS

- Accessibility of resources about Quantum Computing and its simulation
- Quantum Computing Topics usually have large overlap with ideas from classical digital logic systems

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