

Revision History

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Rev 5.0.1 — Remove PG8 control of U4; swap pins of Bat1 footprint Rev 5.0.2 — Add physical connection to unused pins of U5 Rev 5.0.4 — Add connection from AXP209 (pin 48) to P1—8 (IRQ to NEO) Rev 5.0.5 — Add C16 (noise filter for IRQ) Rev 5.0.6 — Add time delay from AXP209 to FET Switch; add test points Rev 5.0.7 — Add R12 pull down on PG11 to set pedigree Rev 5.08 — Mounting holes went missing in 5.0.7, added back in 5.0.8 Rev 5.0.9 — Change P4 to TH mount, SMD electrical; changes layout only
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Orders

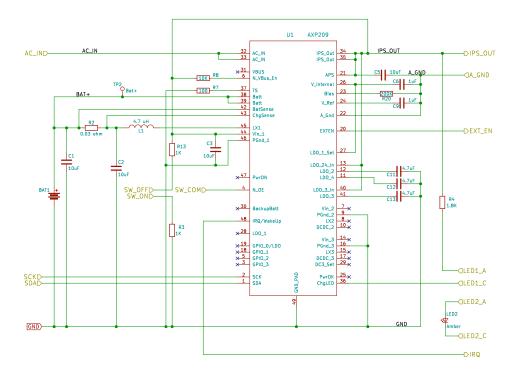
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5.0.0 - Boards ordered from PCBWay -
11/03/18
5.0.2 - Boards ordered from PCBWay -
12/06/18
5.0.4 - Order boards from PCBWay
for 100 unit build

TO DETERMINE BOARD PEDIGREE (from cli.py):
- Is PA6 HIGH?
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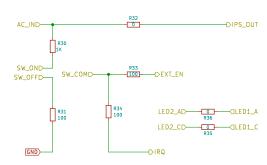
> Sheet: /Notes/ File: HAT_ver_5.1-p3.sch

Title: ConnectBox HAT

Size: USLetter	Date: 2019-10-19	Rev: 6.0.1
KiCad E.D.A. ki	cad (6.0.0-rc1-dev-1413-ga48e89956)	ld: 2/3



Populate for Installed Battery Option ONLY



Populate for No Battery Option ONLY

- NOTE: Use Amber LED for LED1

This option will use the LED2 circuit ported to the LED1 position so that the case will show Amber (status) in the LED1 (red/charge) position to maintain symmetry

| Sheet: /Power control/|
| File: HAT_ver_5.1.+Power.sch |
| Title: HAT Power Options |
| Size: USLedger | Date: 2019-10-23 | Rev: 5.1.1 |
| KiCad E.D.A. kicad (6.0.0-rc1-dev-1413-ga48e89956) | Id: 3/3 |