BoosterPack Standard	FWM out GPIO (!) SPI CS wiveless GPIO (!) SPI CS wiveless GPIO (!) SSI	PWM out GPIO (!)
the EK-TIM4C123GXL BoosterPack connector. reack pinout standard. Din. De-prioritize this pin when making a BoosterPack.	PEZ (1) IZCUSCIL TECCPO PEZ (1) IZCUSCIL TECCPO TECC	PFZ + (I) SSIICIR - MOFAUITO MIPWING TICCPO TROD PFS + (I) SSIICIR - MOFAUITO MIPWINT TICCPI TRCIR PES + (I) CLOSDA TSCPI MOPWING DXI WITCCPO TICCFI PCA + (I) CL - U4RS UITX MIPCCPI PHAI MOPWINT UICTS PCB + (I) COH - U3RS PHBI WITCCPO USBOEPEN PCC + (I) COH - U3RS PHBI WITCCPO USBOEPEN PCC + (I) COH - U3RS PHBI WITCCPO USBOEPEN PCC + (I) COH - U3RS PHBI WITCCPO USBOEPEN PCC + (I) CALLEAUITO (WISCCPI) PF4 + (I) WIFAUITO (DXO TSCCPO USBOEPEN)
the EK-TIV ferPack pinout star 10 pin. De-priorit	TM4C123GXL	
rposed (6 with the Boost ly with this GF upt-capable.	EK-TIV	
BoosterPack EX-TM4C123GXL BoosterPack Standard Pinns exposed @ the EK-TM4C123GXL BoosterPack Also shown are functions that map with the BoosterPack pinout standard. ** Some LaunchPack so not comply with this GPIO pin. De-priorities this pin when making a BoosterPack. (1) Denotes I/O pins that are interrupt-capable.	CANOTS, SSIEFS, TICCPI, MORVING, AINLI] (I) PBS [T2CCPI] (USBOID (UIRX) (II) PBS [T2CCPI] (USBOID (UIRX) (II) PBI [T2CCPI] (USPOINS (II) PBI [WTZCCP0 (ECSSCI) (MIPWIM) (MOPWIM) (SSIICIR) (SSISCIR) (MIPWIM) (MOPWIM) (SSIICIR) (MIPWIM) (MOPWIM) (SSIICIR) (MIPWIM) (