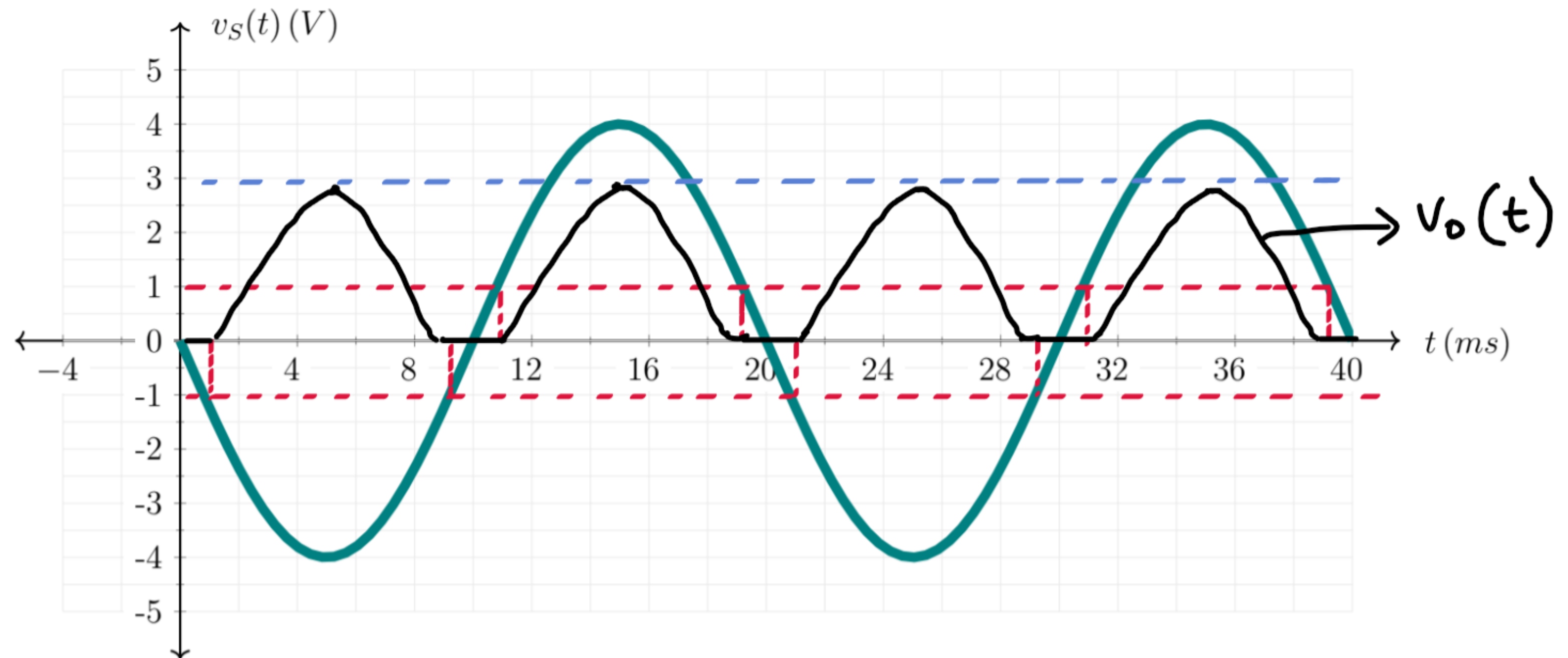
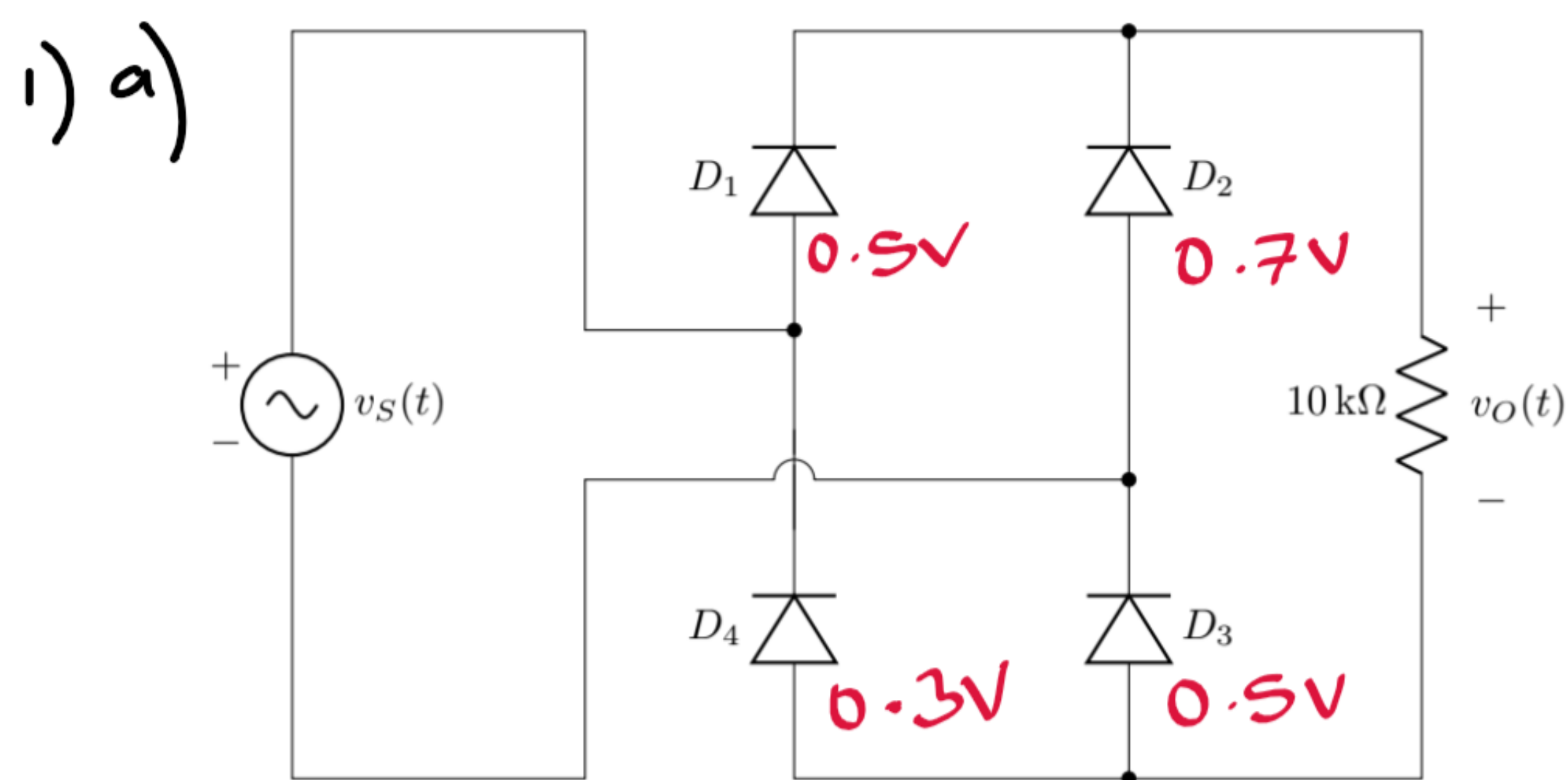


# CSE251 Final (Summer 25) - set A



In +ve cycle,  $D_1$  and  $D_3$  are ON

$$\therefore V_p = V_m - V_{D_1} - V_{D_3} = 4 - 0.5 - 0.5 = 3 \text{ V}$$

In -ve cycle,  $D_2$  and  $D_4$  are ON

$$\therefore V_p = V_m - V_{D_2} - V_{D_4} = 4 - 0.7 - 0.3 = 3 \text{ V}$$

b)  $V_r = 10\% \text{ of } V_p = 0.1 \times 3 = 0.3 \text{ V}$

$$V_r = \frac{V_p}{2fRC}$$

$$V_p = 3$$

$$f = \frac{1}{T} = \frac{1}{20 \text{ ms}} = 50 \text{ Hz}$$

$$\Rightarrow C = \frac{V_p}{2fRV_r}$$

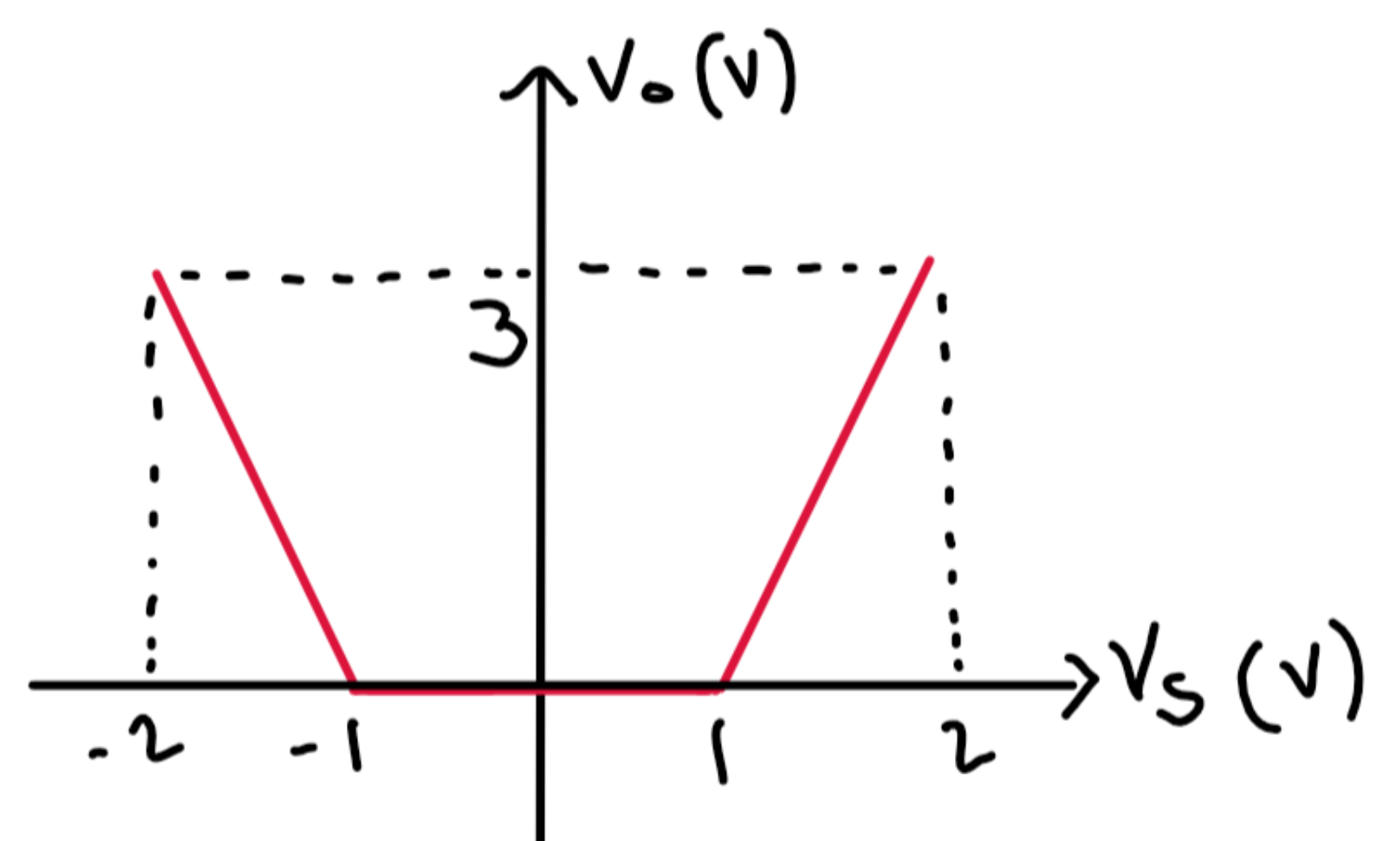
$$R = 10 \text{ k}\Omega$$

$$= \frac{3}{2 \times 50 \times 10 \times 10^3 \times 0.3}$$

$$= 1 \times 10^{-9} \text{ F or } 10 \text{ nF}$$

c)  $V_{dc} = V_p - \frac{V_r}{2} = 3 - \frac{0.3}{2} = 2.85 \text{ V}$

d)

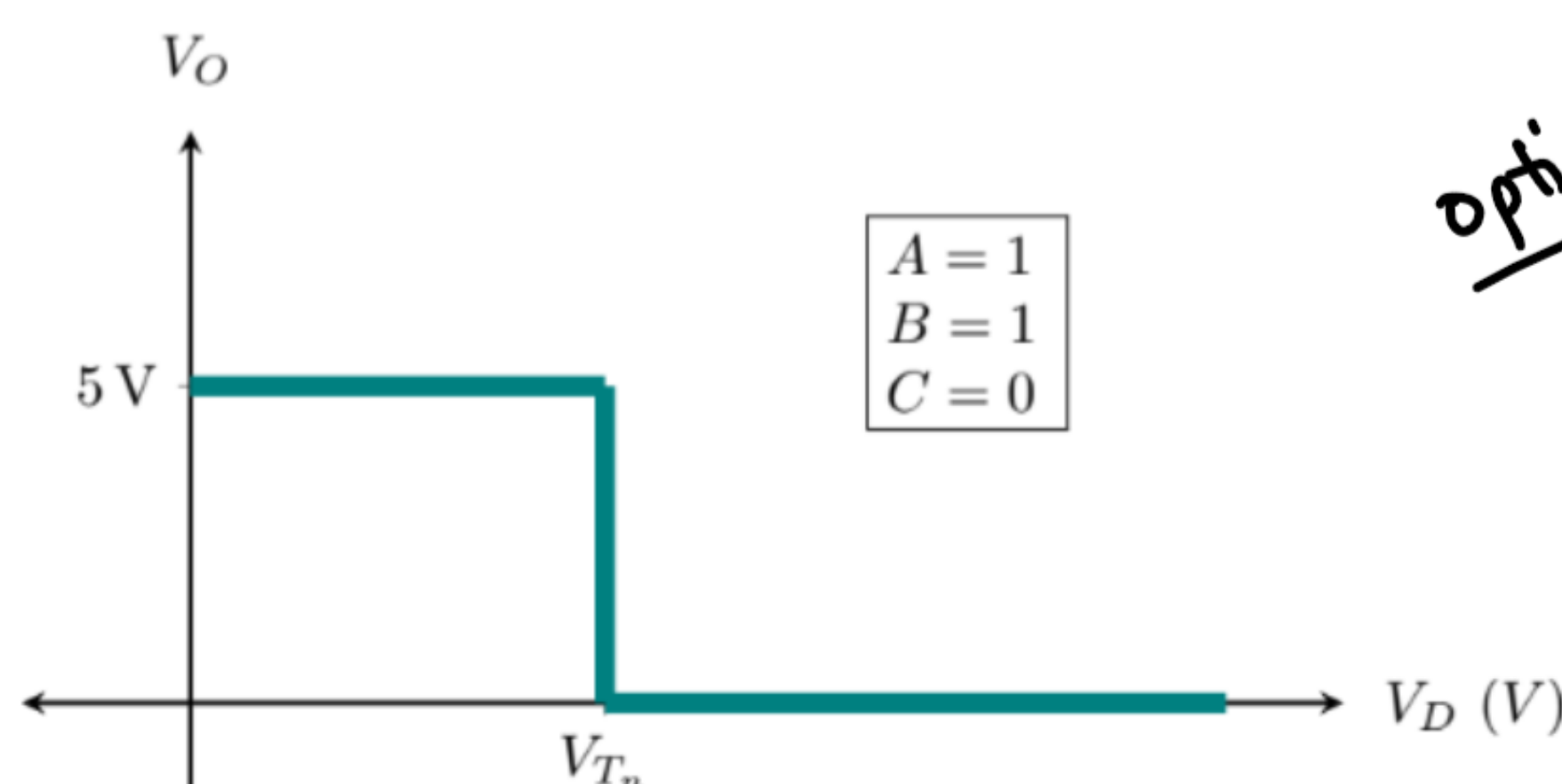
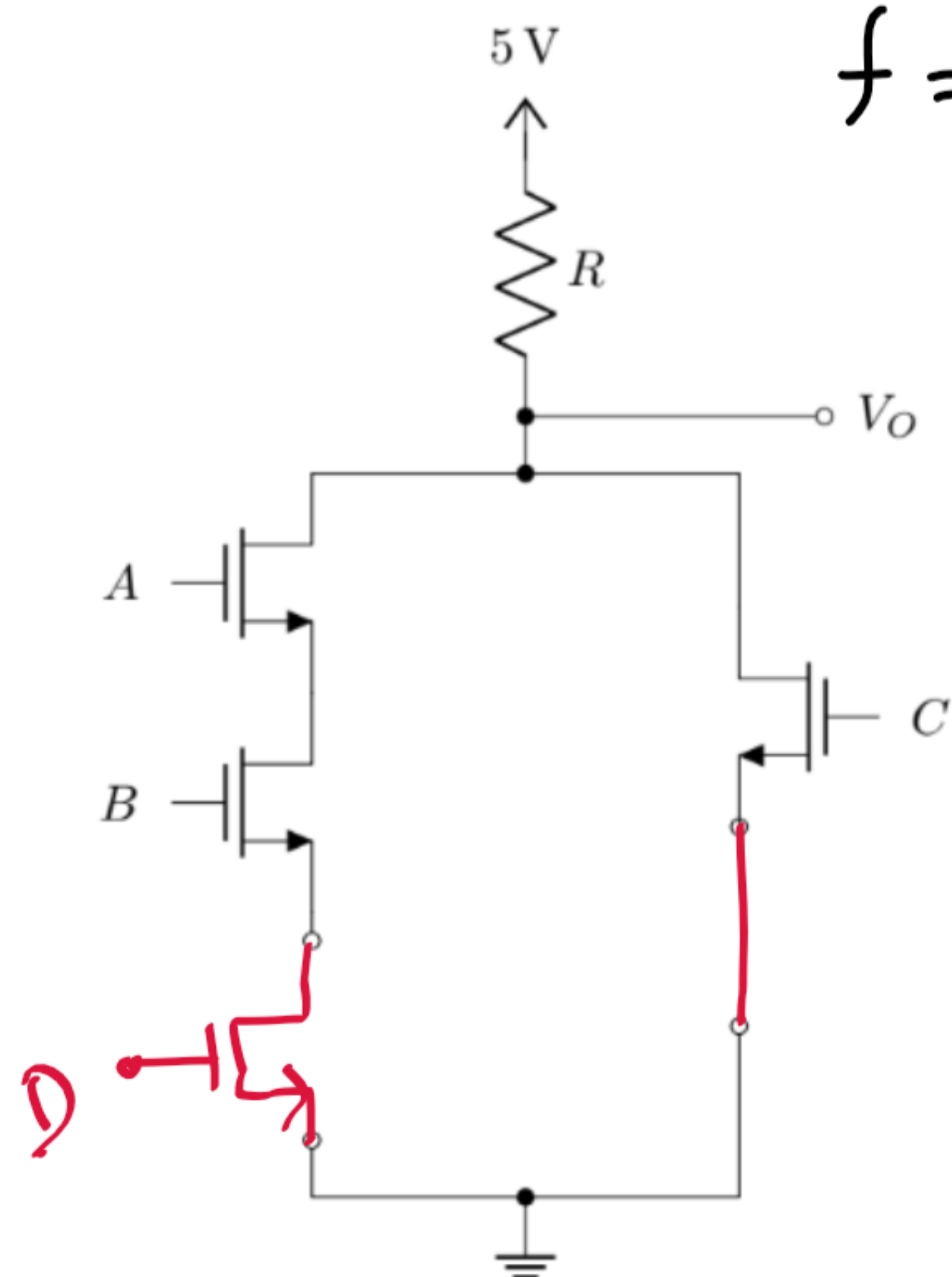


2) a) MOSFET  $\rightarrow$  triode and cutoff

BJT  $\rightarrow$  saturation and cutoff

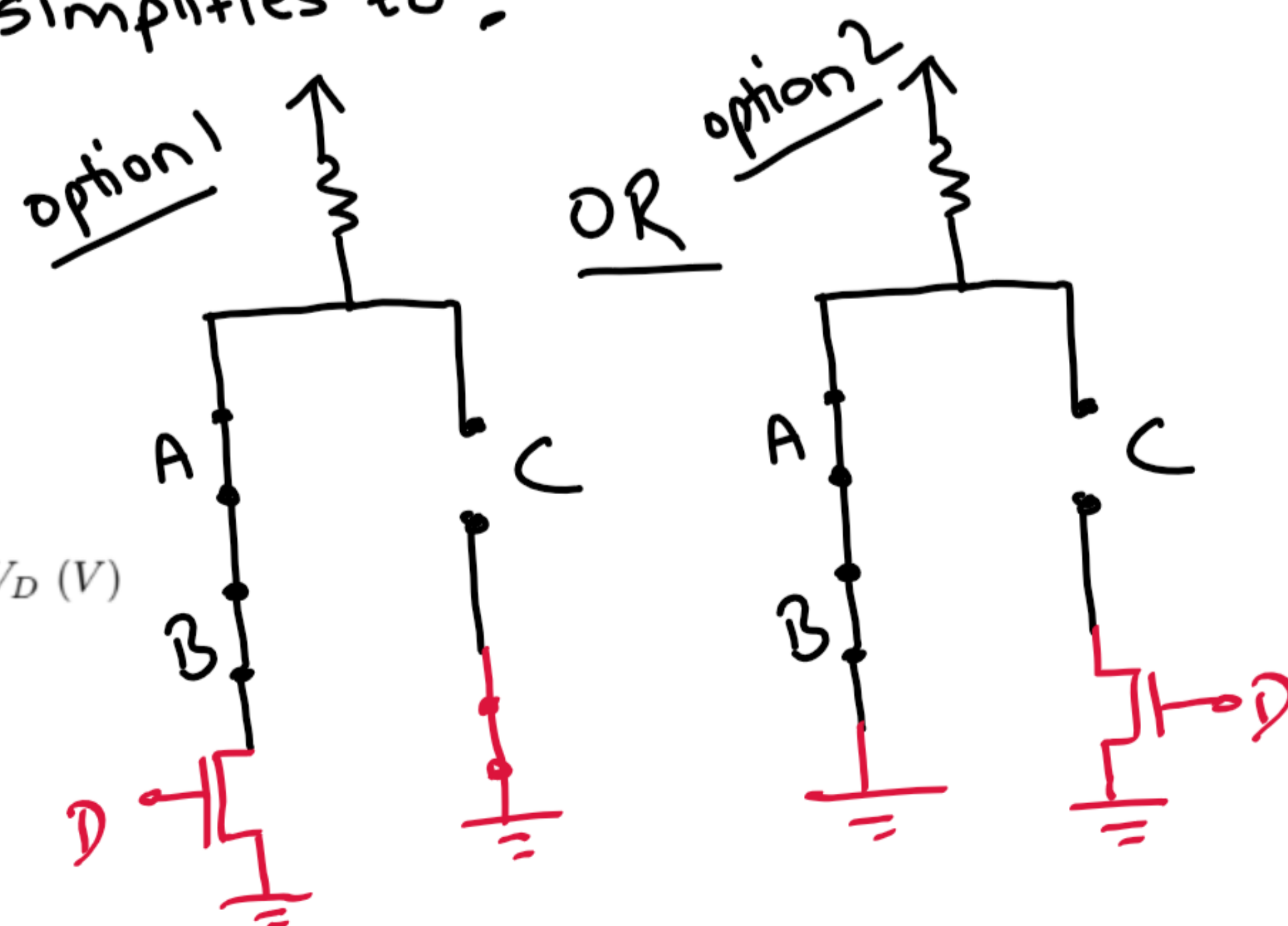
b) In cutoff.  $V_a = V_s$ , so  $V_{GS} = 0 < V_{T_n}$

c)  $f = \overline{ABD + C}$



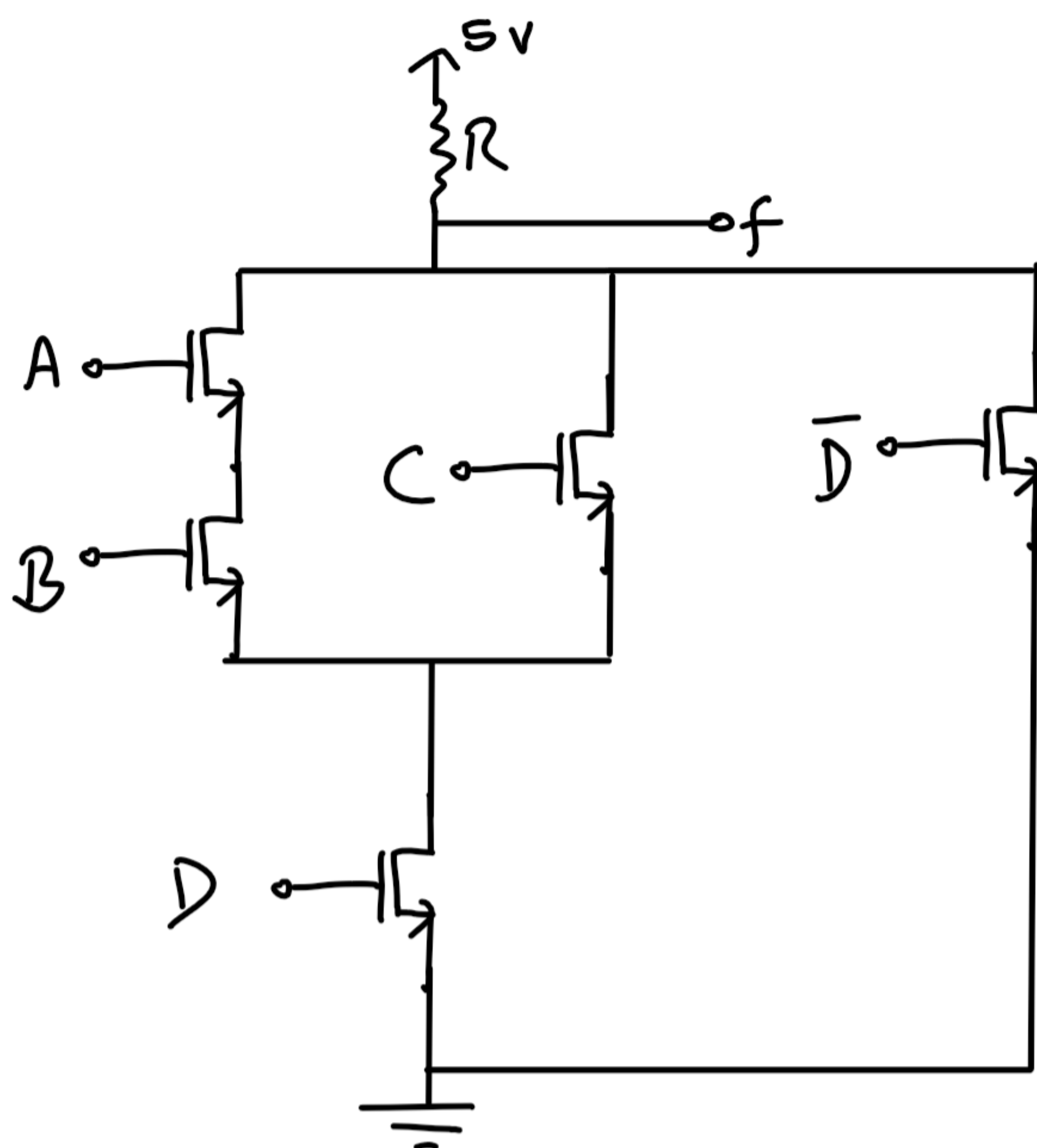
matches with option 1.

Using the VTC, the circuit simplifies to:





$$3) a) f = \overline{(AB + C)D + \overline{D}}$$



\*  $\bar{D}$  can be shown coming from another sub-circuit (any valid working is fine)

Equating these two removes the need to calculate  $k$ , making this step optional.

$$\begin{cases} I_{DS} = \frac{1}{2} \mu V_{OV}^2 \longrightarrow \mu = \frac{1}{2} \text{ mA/V}^2 \\ I_{DS} = \mu \left( V_{OV} - \frac{1}{2} V_{DS} \right) V_{DS} \end{cases}$$

$$\therefore V_{DS} = 4 \text{ V}$$

now,  

$$V_{DS} = V_D - V_S$$

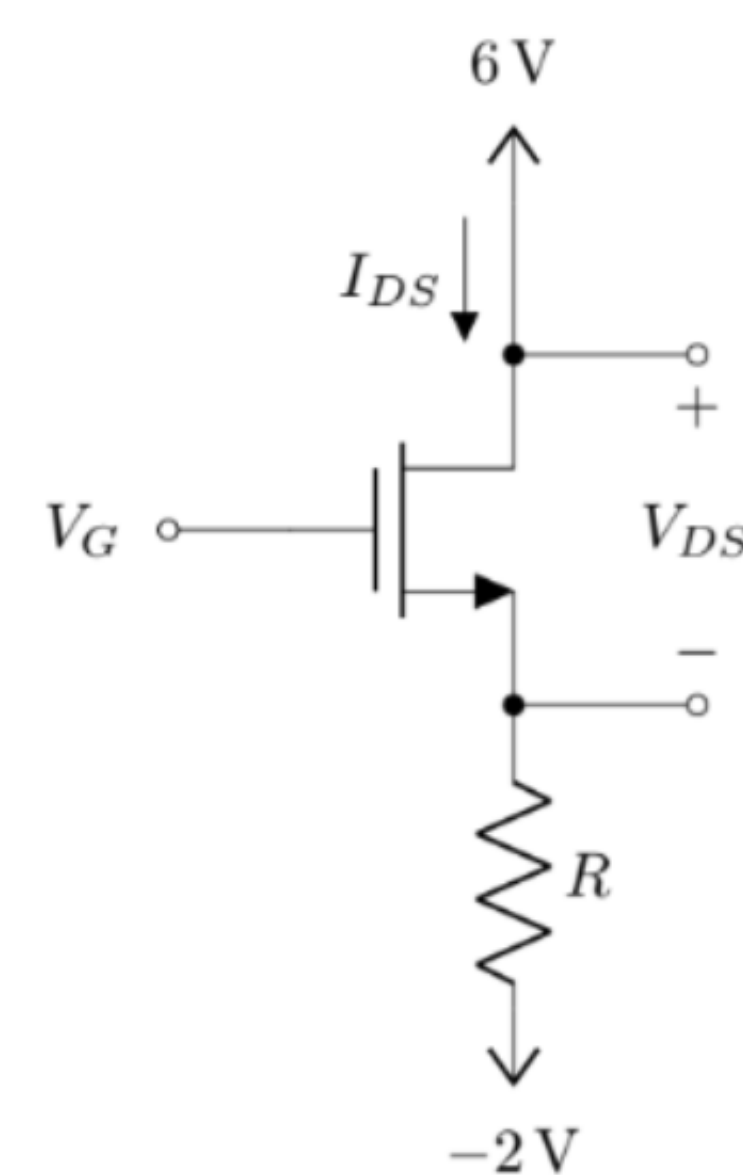
$$\therefore V_S = 2 \text{ V}$$

From  $V_{OV} = V_G - V_S - V_T$

$$\therefore V_G = 7 \text{ V}$$

$$\text{ii) } I_{05} = 4 = \frac{V_s - (-2)}{R}$$

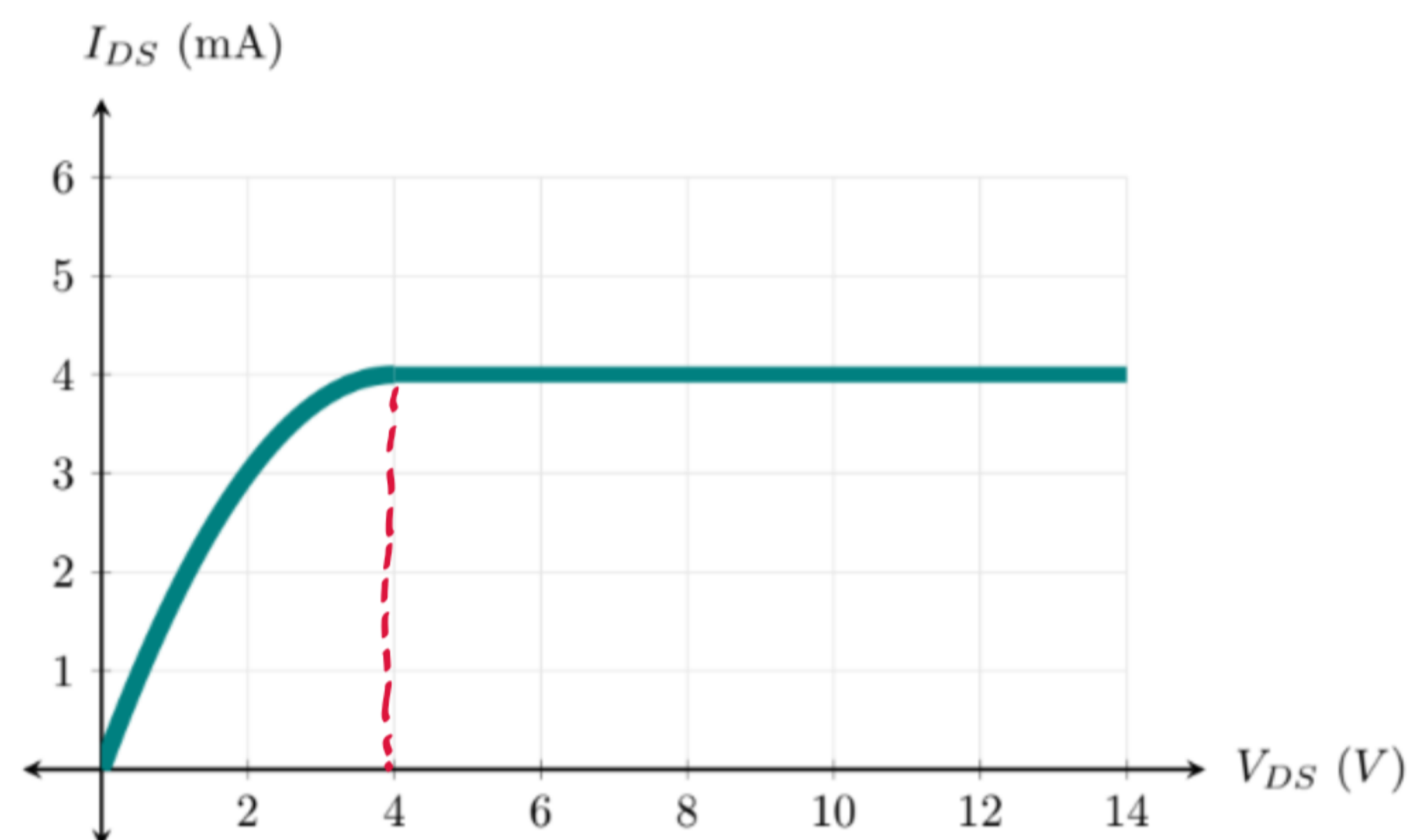
$$\therefore R = 1 \text{ k}\Omega$$



$$V_D = 6V$$

$$V_S = ?$$

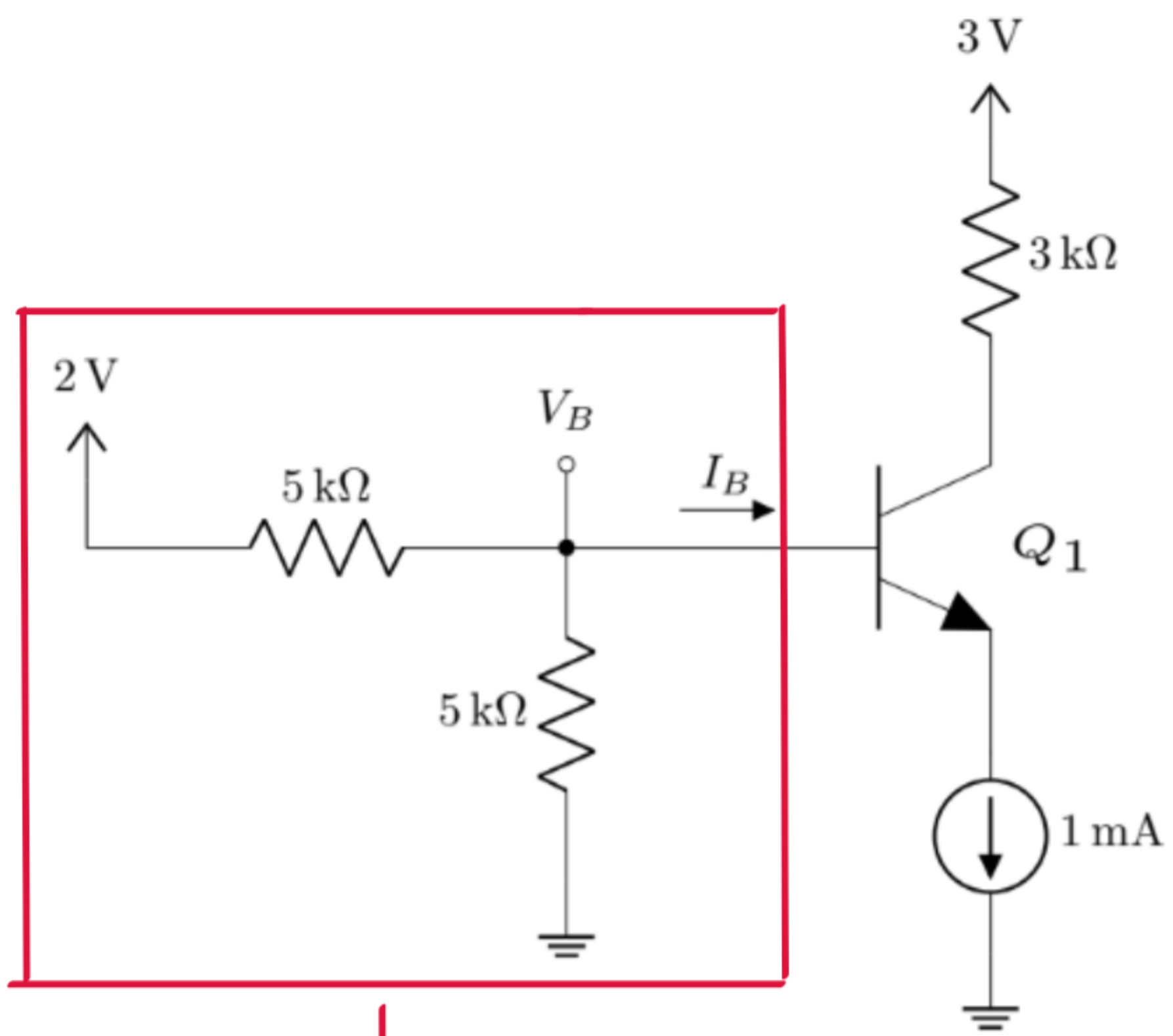
$$V_G = ?$$



From graph,  $V_{OV} = 4 \text{ V}$  at  $I_{DS} = 4 \text{ mA}$

At edge of saturation,

4)



**NPN BJT Parameters:**

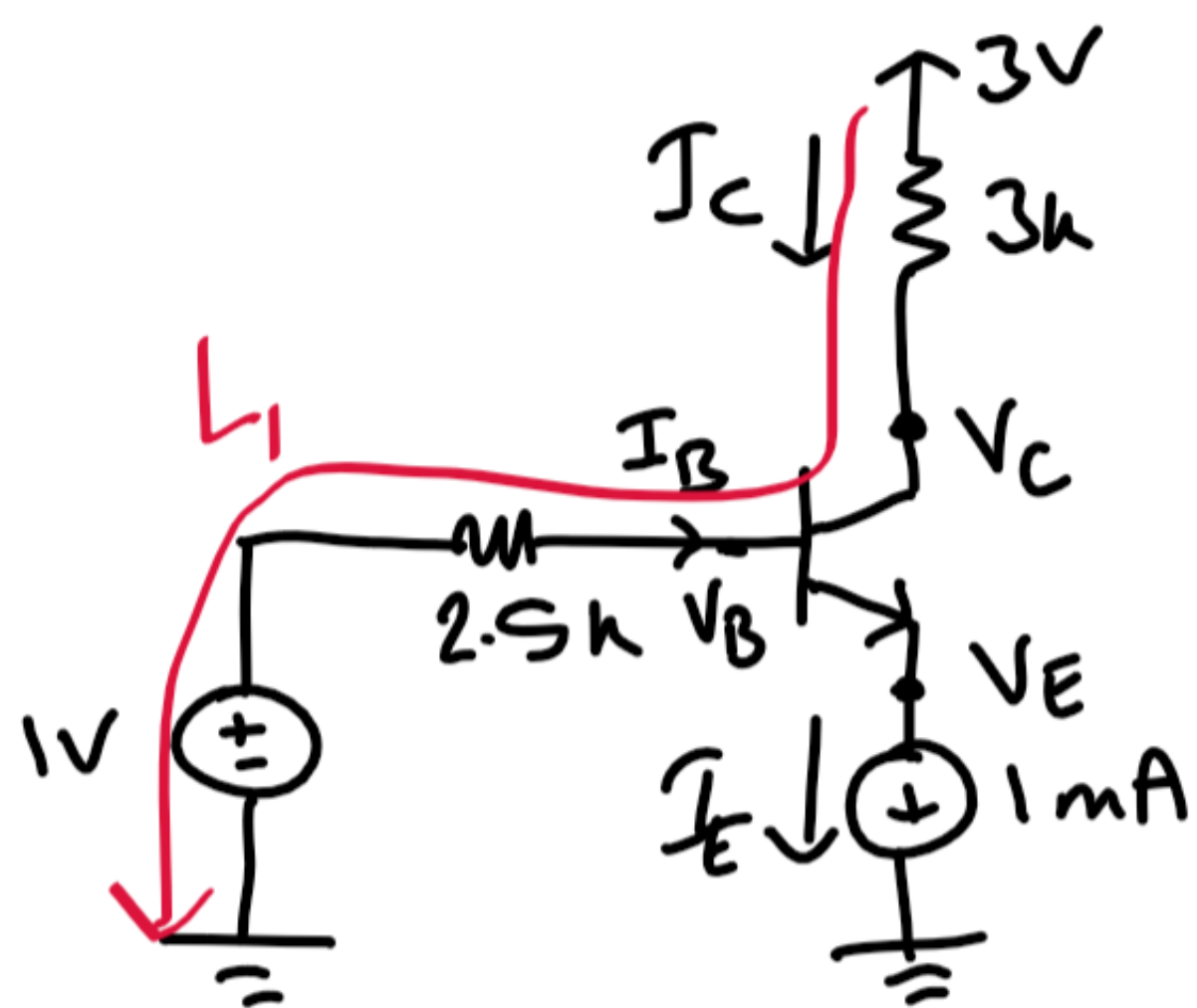
For the transistor  $Q_1$ :

Current gain:  $\beta = 100$

Base-emitter voltage in saturation:  $V_{BE(sat)} = 0.8 \text{ V}$

Collector-emitter voltage in saturation:  $V_{CE(sat)} = 0.2 \text{ V}$

↓ Thevenin's equivalent



$$\underline{L_1}: 3 = 3I_C + V_{CB} - 2.5I_B + 1$$

$$\Rightarrow 3 - 1 = 3I_C - 2.5I_B + \frac{V_{CE}}{0.2} - \frac{V_{BE}}{0.8} \quad (\text{Assuming saturation})$$

$$\Rightarrow 3I_C - 2.5I_B = 2.6 - \textcircled{i}$$

$$\underline{KCL}: I_E = I_C + I_B$$

$$\Rightarrow I_C + I_B = 1 - \textcircled{ii}$$

$$\text{solving, } I_C = 51/55 = 0.927 \text{ mA}$$

$$I_B = 4/55 = 0.073 \text{ mA}$$

$$I_C/I_B = 12.75 < \beta_{\text{active}} \therefore Q_1 \text{ is in saturation}$$

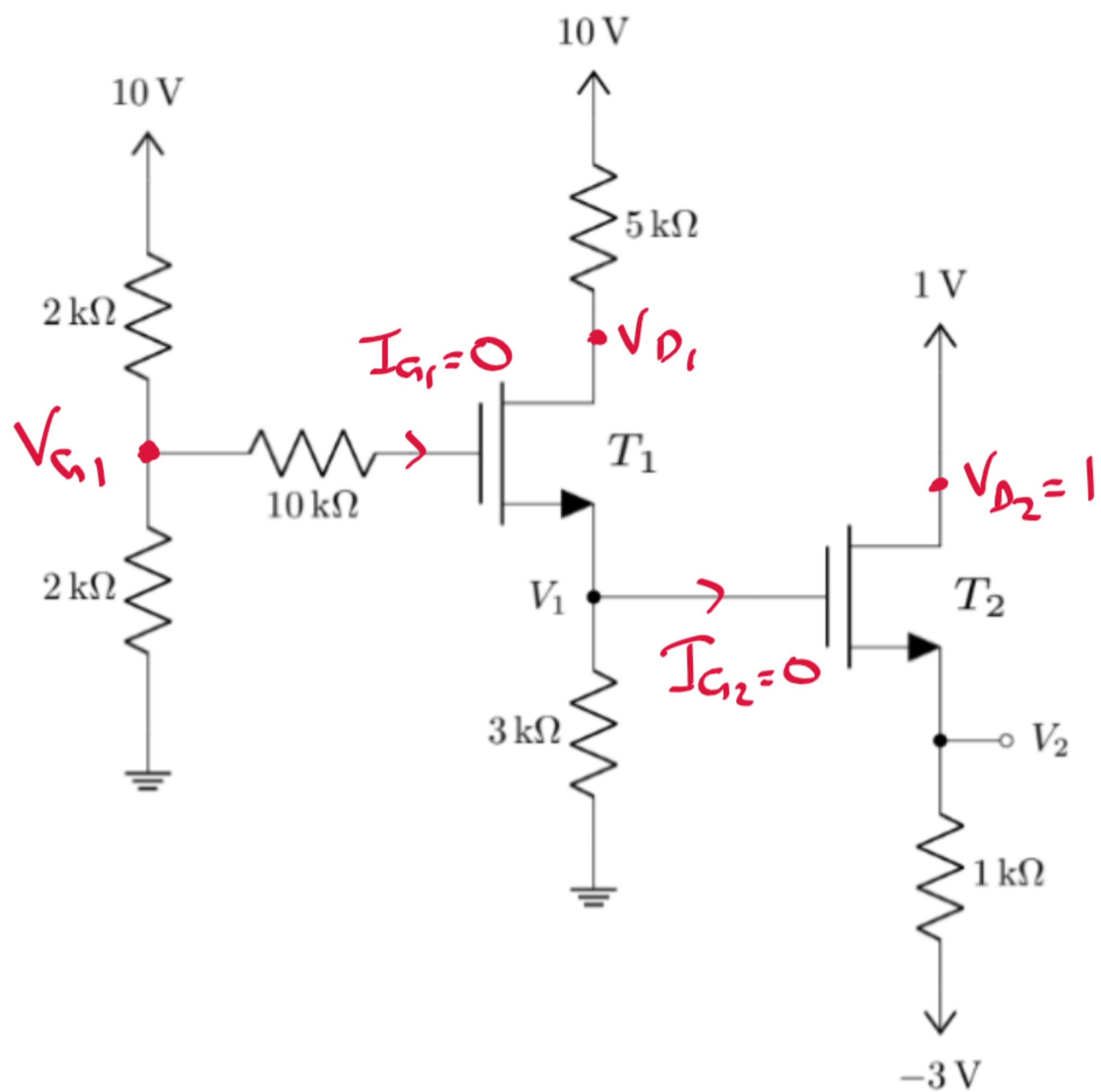
$$V_B = -2.5I_B + 1 = 0.818 \text{ V}$$

$$V_C = -3I_C + 3 = 0.218 \text{ V}$$

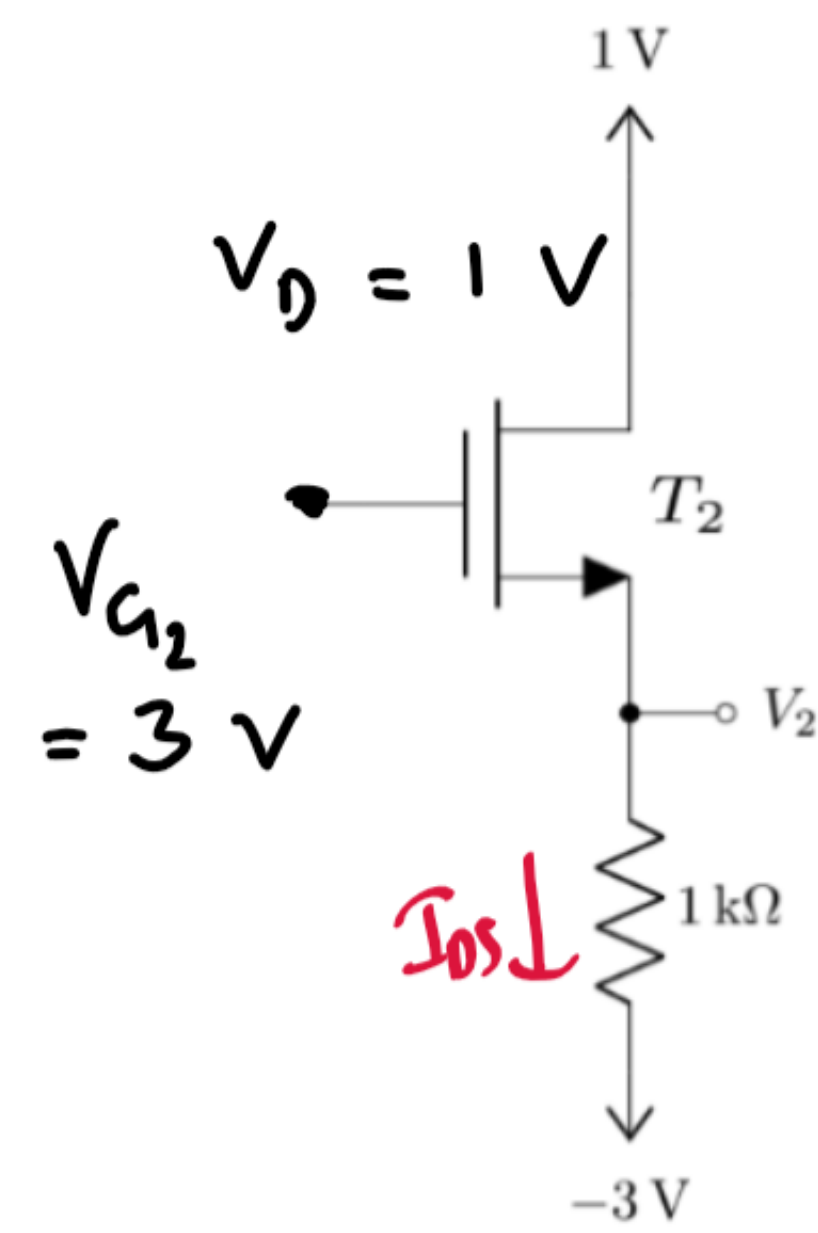
$$V_E = V_C - V_{CE} = 0.018 \text{ V}$$



5)



b)



Assume triode,

$$\begin{aligned} I_{DS} &= k \left( V_{OV} - \frac{1}{2} V_{DS} \right) V_{DS} \\ &= 2 \left( 3 - V_S - 1 - \frac{1}{2} \times 1 + \frac{1}{2} V_S \right) (1 - V_S) \\ &= 2 \left( \frac{3}{2} - \frac{1}{2} V_S \right) (1 - V_S) \\ &= (3 - V_S) (1 - V_S) \\ &= V_S^2 - 4V_S + 3 \quad \text{--- (i)} \end{aligned}$$

$$I_{DS} = \frac{V_S - (-3)}{1} \quad \text{--- (ii)}$$

Equating (i) and (ii),

$$V_S^2 - 5V_S = 0$$

$$\Rightarrow V_S = \frac{5V}{\times} \text{ or } 0V$$

$$V_{AS} < V_T$$

$$\therefore V_S = 0V$$

$$V_{AS} = 3V$$

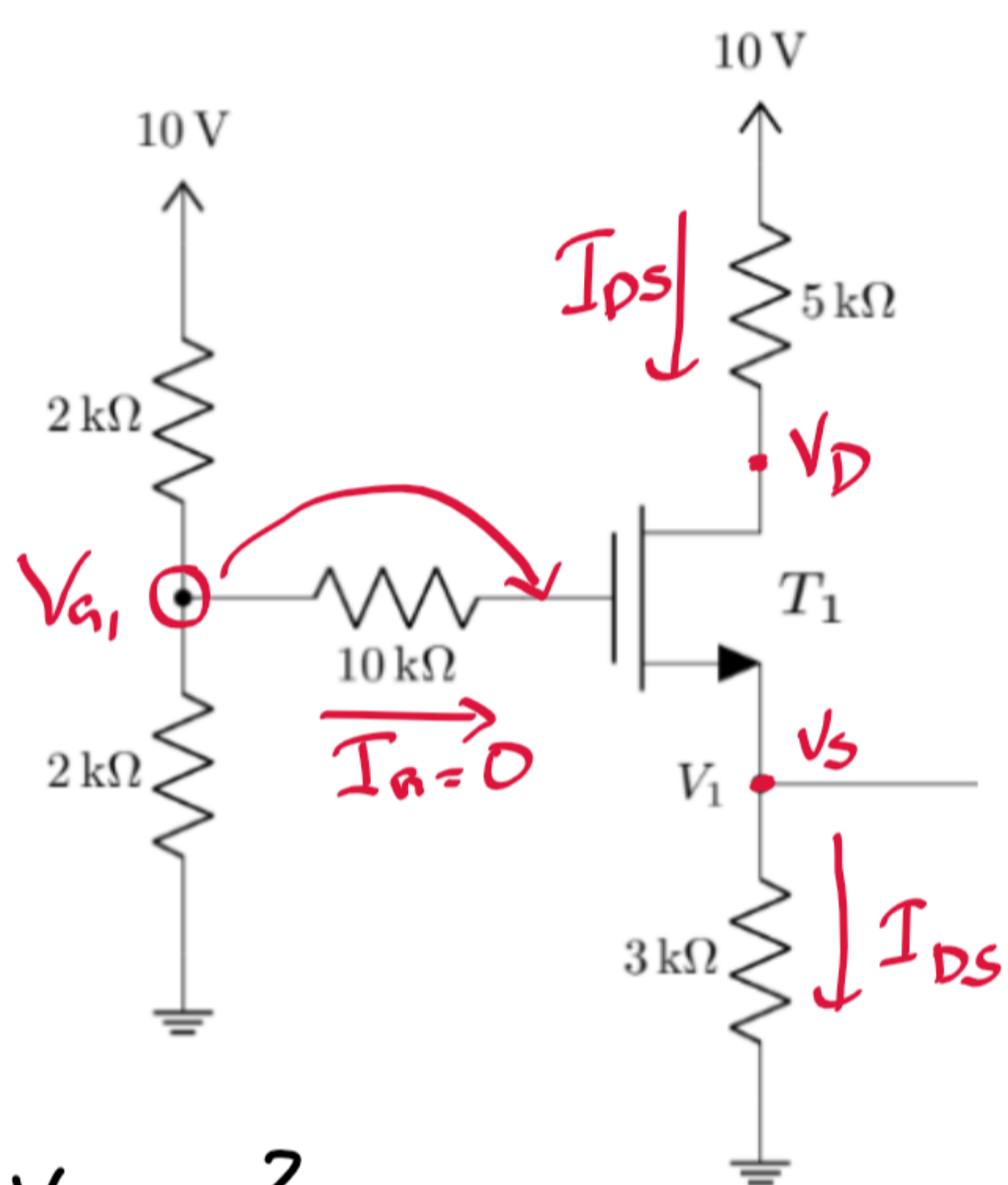
$$V_{OV} = 2V$$

$$V_{DS} = 1V$$

$V_{DS} < V_{OV} \therefore T_2$  is operating in triode mode.

$$\therefore V_2 = V_S = 0V$$

a)



$$\begin{aligned} V_{G1} &= \frac{2}{2+2} \times 10 \\ &= 5V \end{aligned}$$

Given that  $T_1$  is in saturation,

$$\begin{aligned} I_{DS} &= \frac{1}{2} k V_{OV}^2 \\ &= \frac{1}{2} \times 2 \times (V_G - V_S - V_T)^2 \\ &= (4 - V_S)^2 \\ &= V_S^2 - 8V_S + 16 \quad \text{--- (i)} \end{aligned}$$

$$I_{DS} = \frac{V_S - 0}{3} \quad \text{--- (ii)} \quad I_{DS} = \frac{10 - V_{D1}}{5} \quad \text{--- (iii)}$$

Equating (i) and (ii),

$$V_S^2 - \frac{25}{3} V_S + 16 = 0$$

$$\begin{aligned} V_S &= \frac{16/3}{\times} \text{ or } 3 \\ &\text{as } V_{AS} < V_T \end{aligned}$$

From (ii) and (iii),  $V_{D1} = 5V$  and  $V_1 = V_S = 3V$