

MPU  
File: mpu.kicad\_sch

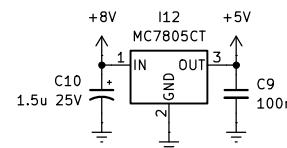
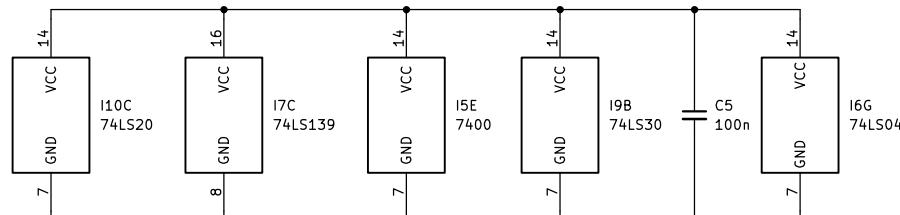
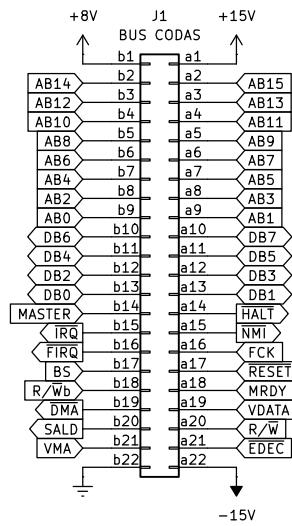
Control  
File: ctrl.kicad\_sch

A

B

C

D



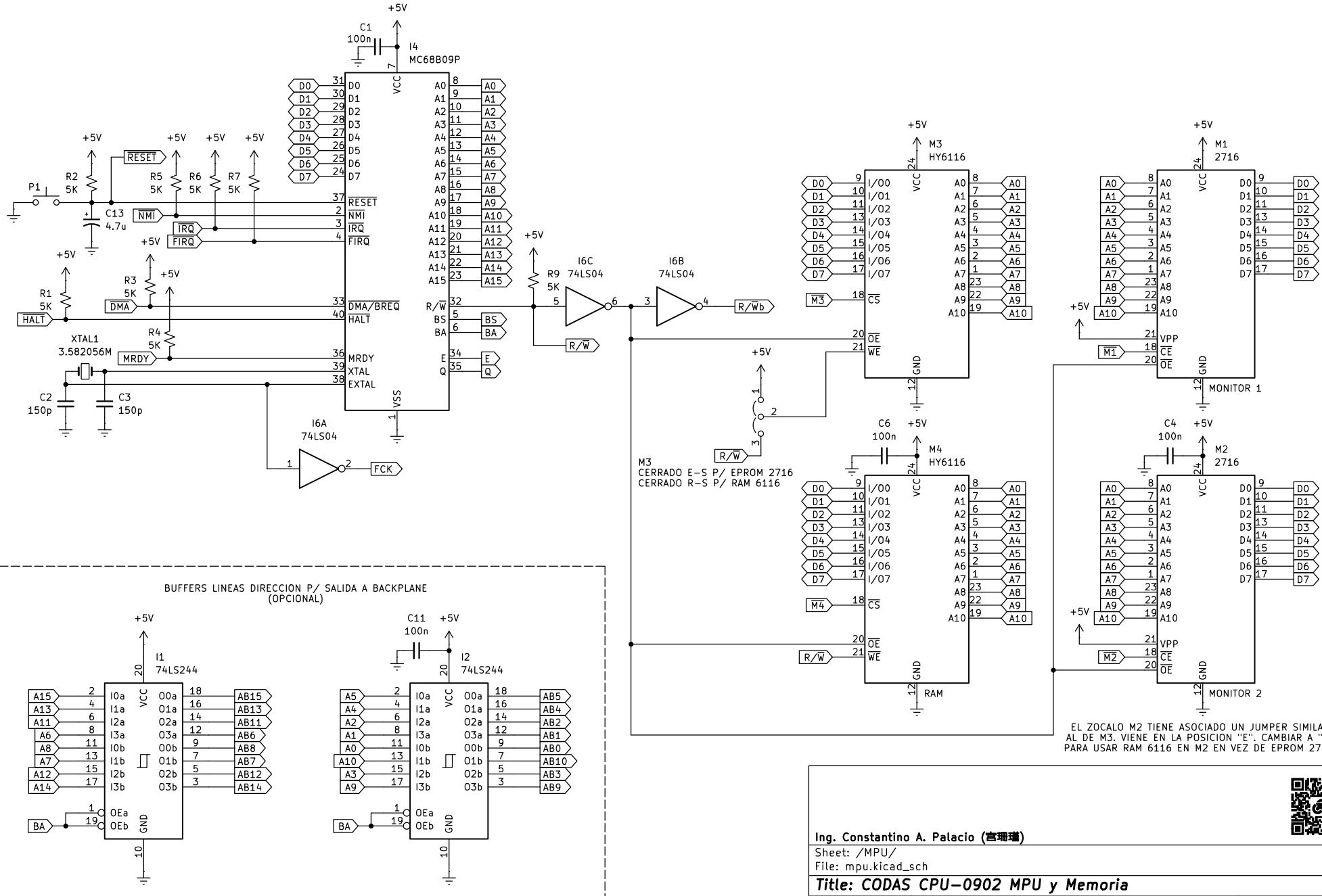
Ing. Constantino A. Palacio (宮珊瑚)

Sheet: /  
File: codas09.kicad\_sch

Title: CODAS CPU-0902 Alimentación y Backplane

Size: A4      Date: 2025-12-08  
KiCad E.D.A. 8.0.8

Rev: 1  
Id: 1/3



Ing. Constantino A. Palacio (宮珊瑚)

Sheet: /MPU/  
File: mpu.kicad\_sch

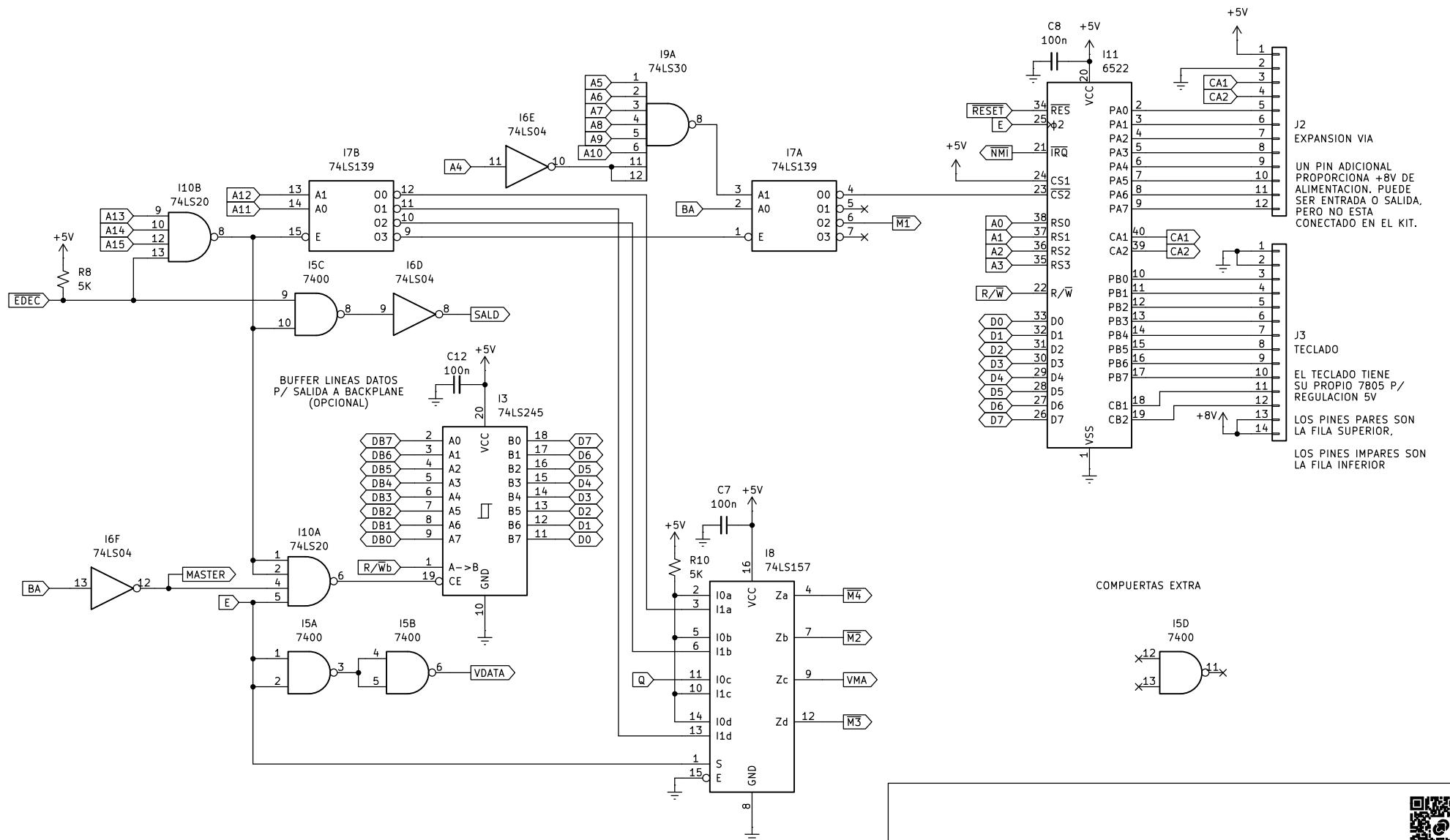
Title: CODAS CPU-0902 MPU y Memoria

Size: A4 Date: 2025-12-08

KiCad E.D.A. 8.0.8



Rev: 1  
Id: 2/3



Ing. Constantino A. Palacio (宮珊瑚)

Sheet: /Control/  
File: ctrl.kicad\_sch

Title: CODAS CPU-0902 Control TTL y VIA 6522

Size: A4 Date: 2025-12-08

KiCad E.D.A. 8.0.8



Rev: 1  
Id: 3/3