

What's all the fuzz about?

Project Robus *Aegis™ Platform*

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Project Robus

- Started in April 2013
- 17 advisories / 31 tickets
- Mostly DNP3, 1 Modbus
- Only 4 products so far without a detectable issue

www.automatak.com/robust

www.automatak.com/aegis

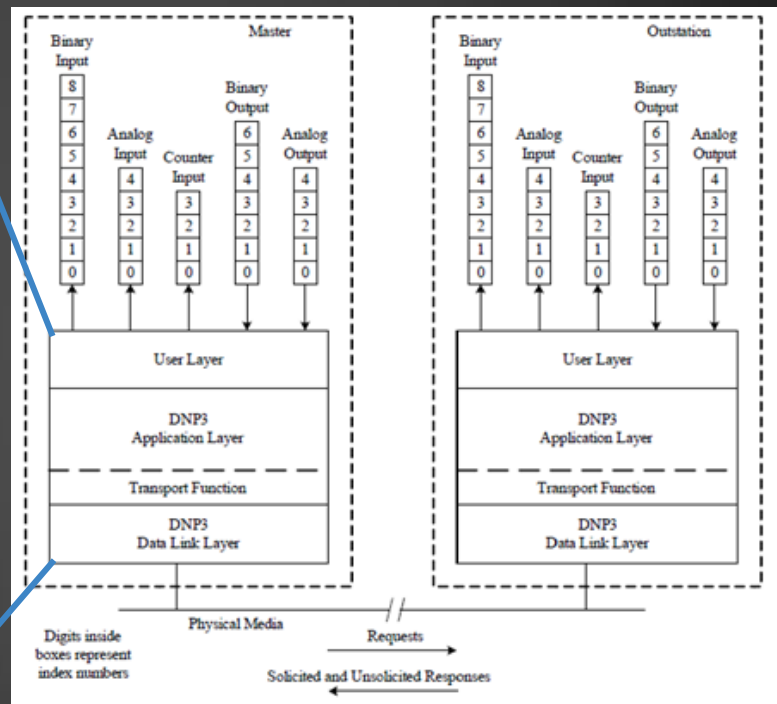
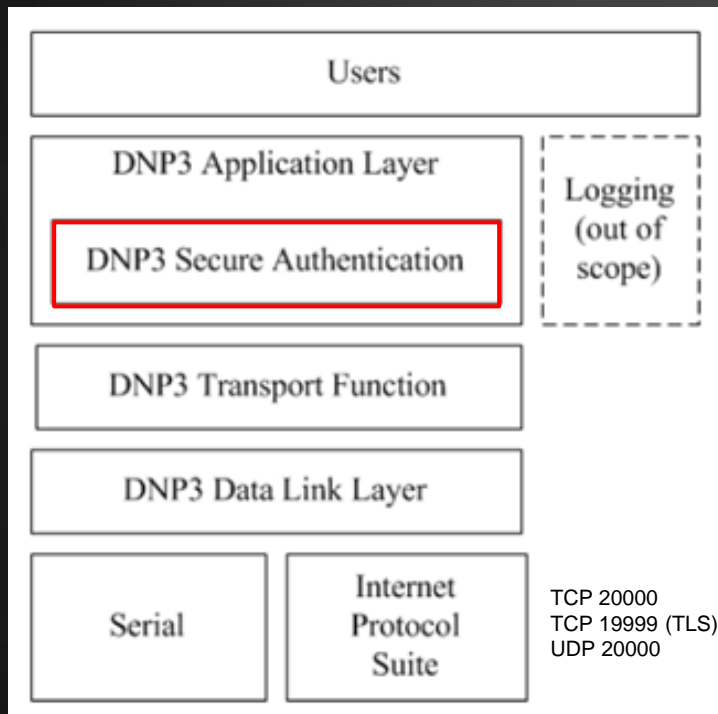


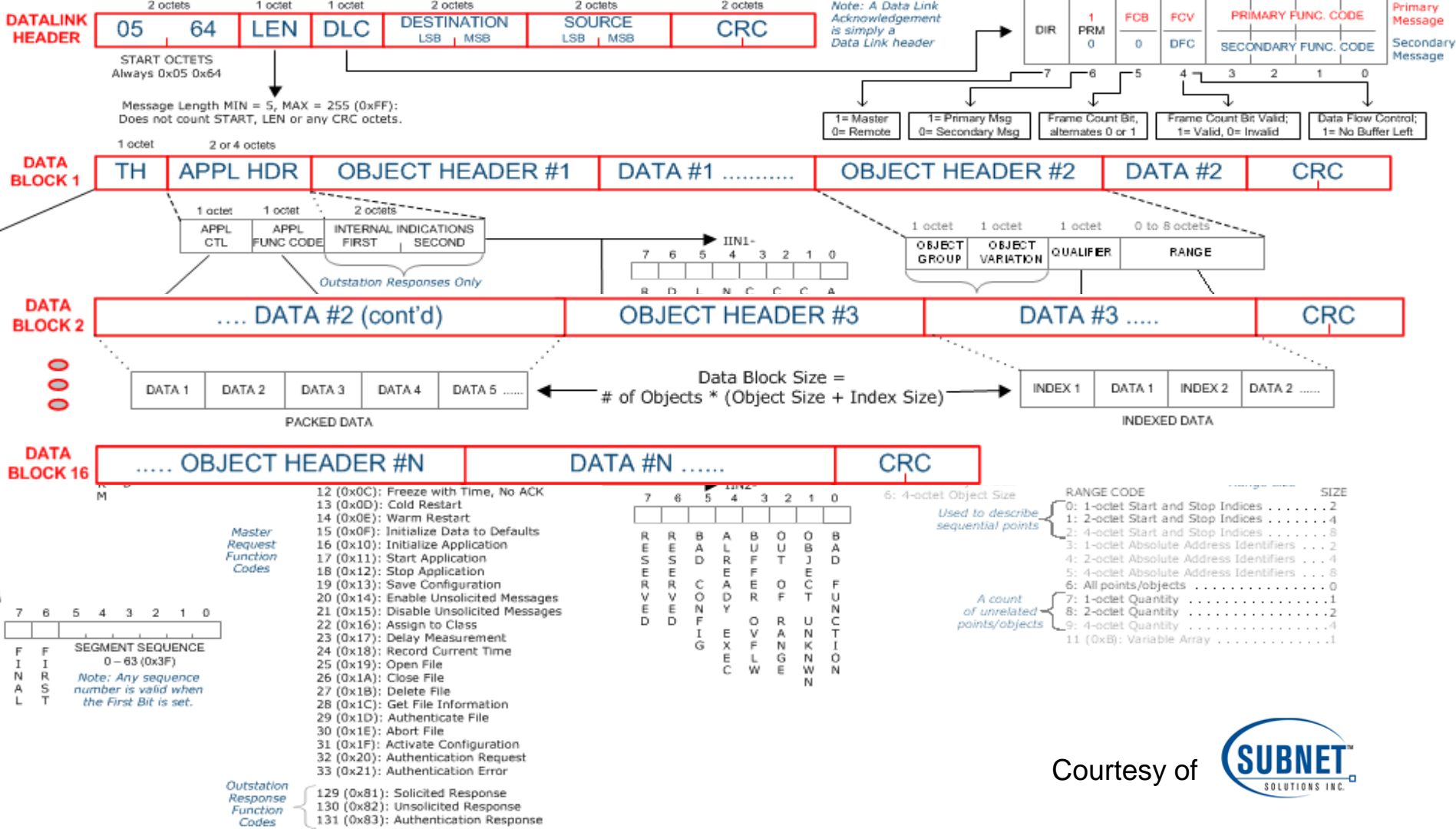
Vendor Response Matrix

#	ICS-CERT Adv	Company	Bug	Fix	Days	Advisory
1	ICSA-13-161-01	IOServer	4/24	5/24	30	6/10/2013
2	ICSA-13-213-03	IOServer	5/1	7/20	80	8/1/2013
3	ICSA-13-219-01	SEL	5/1	5/30	29	8/7/2013
4	ICSA-13-226-01	Kepware	4/24	6/18	55	8/14/2013
5	ICSA-13-234-02	TOP Server	4/24	6/18	55	8/22/2013
6	ICSA-13-240-01	TMW	4/24	6/17	54	8/28/2013
7	ICSA-13-213-04A	Matrikon	4/24	6/17	54	8/29/2013
8	ICSA-13-252-01	Subnet	4/24	8/30	128	9/9/2013
9	ICSA-13-282-01	Alstom	4/24	6/4	41	10/21/2013
10	ICSA-13-297-01	Catapult	4/24	10/1	160	11/22/2013
11	ICSA-13-297-02	GE IP	S.R.	10/1	n/a	11/22/2013
12	ICSA-13-337-01	Elecsys	9/12	11/4	53	12/3/2013
13	ICSA-13-346-02	Cooper OPC	7/31	None	∞day™	12/12/2013
14	ICSA-13-346-01	Cooper/Cybertec	5/1	12/12	225	12/12/2013
15	ICSA-13-352-01	Novatech	5/1	9/5	127	12/18/2013
16	ICSA-14-014-01	Schneider	8/6	8/23	17	1/14/2014
17	ICSA-14-006-01	Schneider/Telvent	8/29	10/16	48	1/30/2014

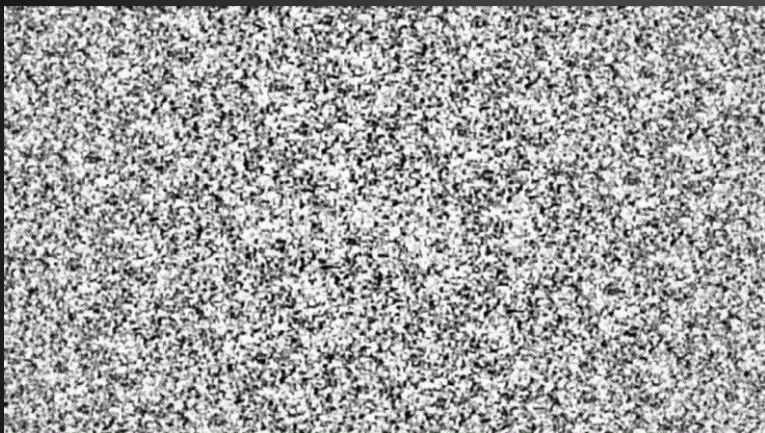


Breaking Down DNP3





White Noise Fuzzing



```
1  [|||||||||||||||||||||100.0%]  
2  [|||||||||||||||||||||100.0%]  
3  [|||||||||||||||||||||100.0%]  
4  [|||||||||||||||||||||100.0%]  
Mem[|||||||||||||||||5853/16010MB]  
Swp[|0/16341MB]
```

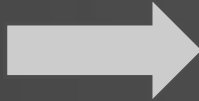
#1 random == really “dumb”



Template (mutational) Fuzzing



Generational “Smart” Fuzzing



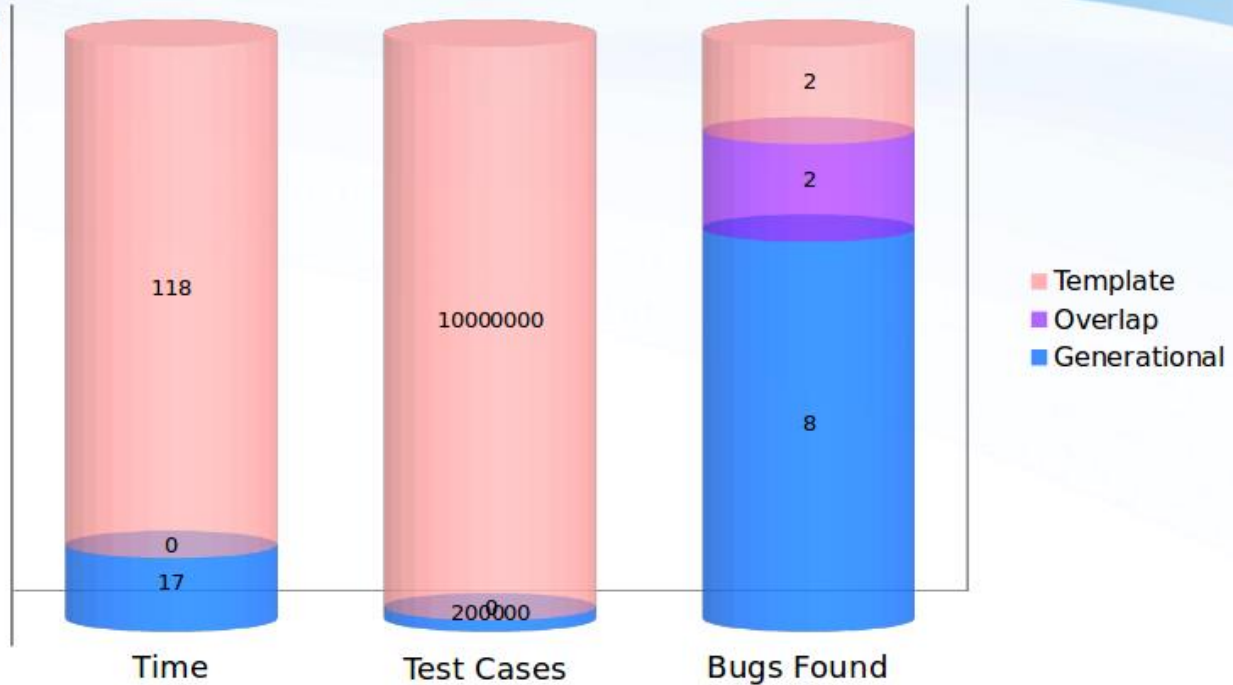
Multi-field Anomalies



Generational == most vulns!



Comparing template and generational fuzzing

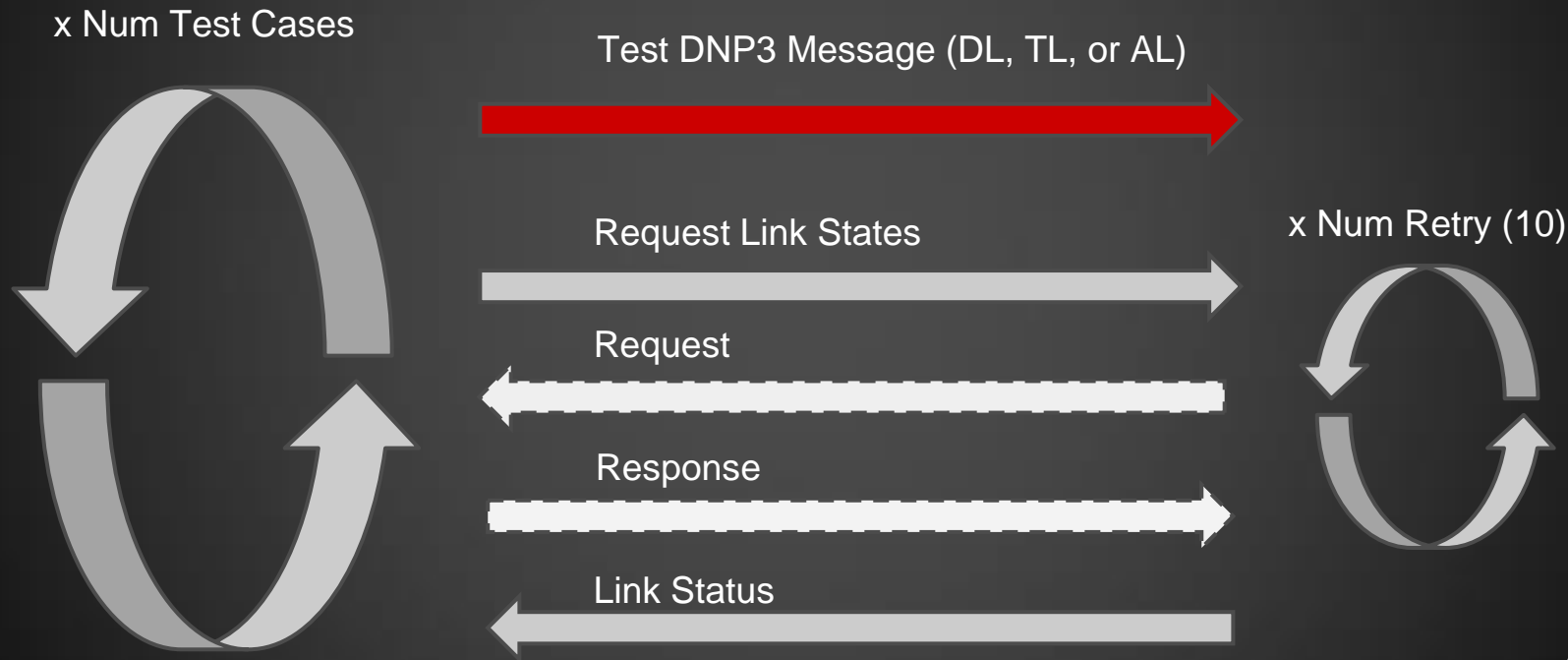


Aegis™ Specifics

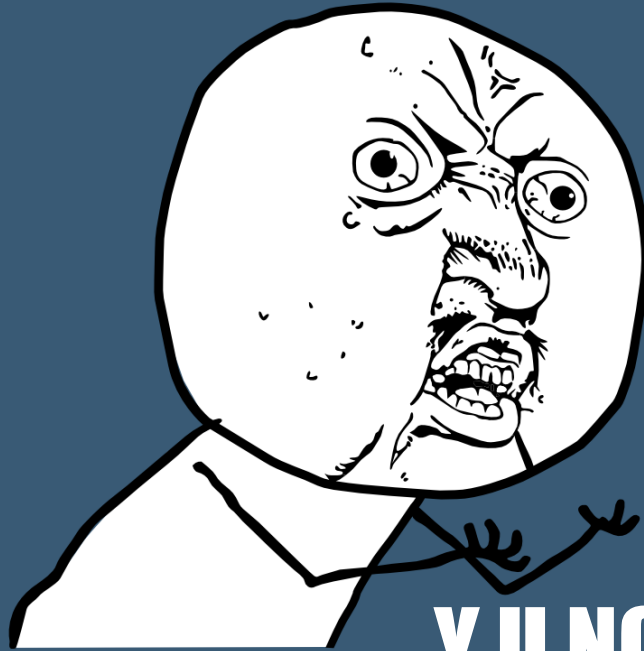
- Written in Scala www.scala-lang.org
- Currently porting it to C#
- Protocol boundary conditions
- Abstracts physical layer
- Combines aspects of generation and mutation
- Repeatable random seeds
- ~200,000 test cases with one seed



Fuzzer Test Flow



I 0x0564 U...



**Y U NO 0x0564
ME BAK ?!**

Combinatorics

```
val nums = List(1, 3)
```

```
val colors = List("red", "green")
```

```
// repeat the reversed string num times
```

```
def combine(i: Int, s: String) = List.fill(i)(s.reverse).mkString
```

```
val result = Cartesian.Transform(colors, nums)(combine)
```

What is result?

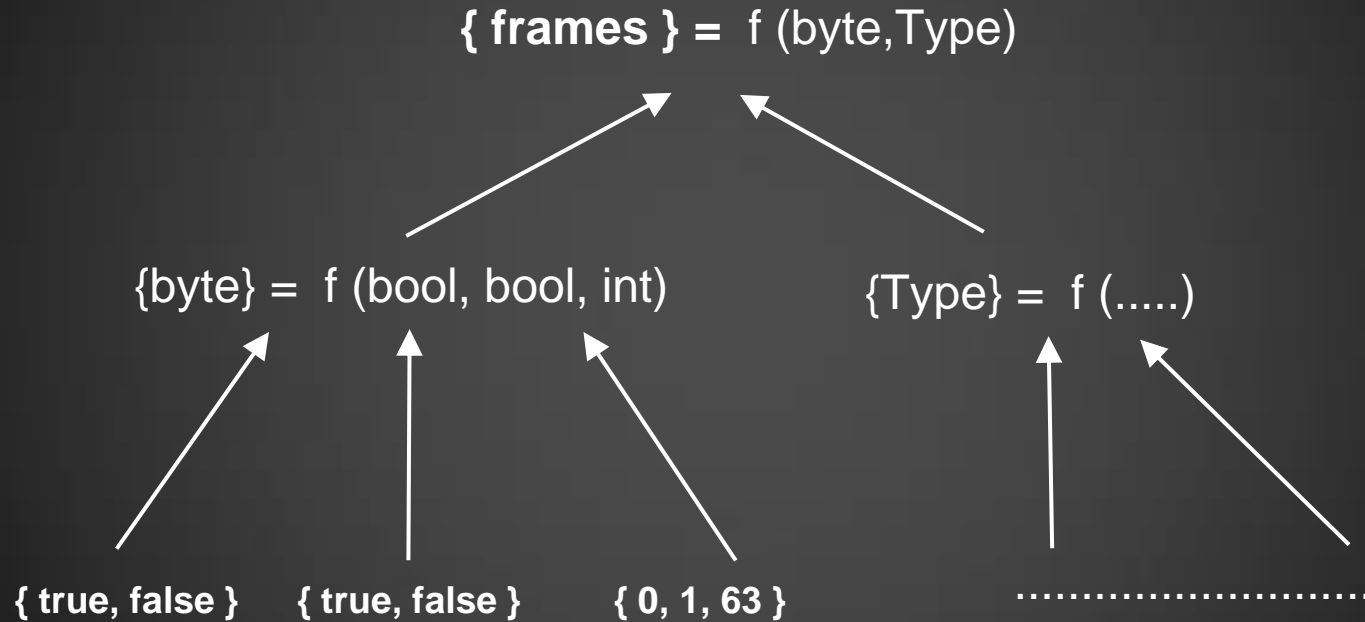


Lazy Generator

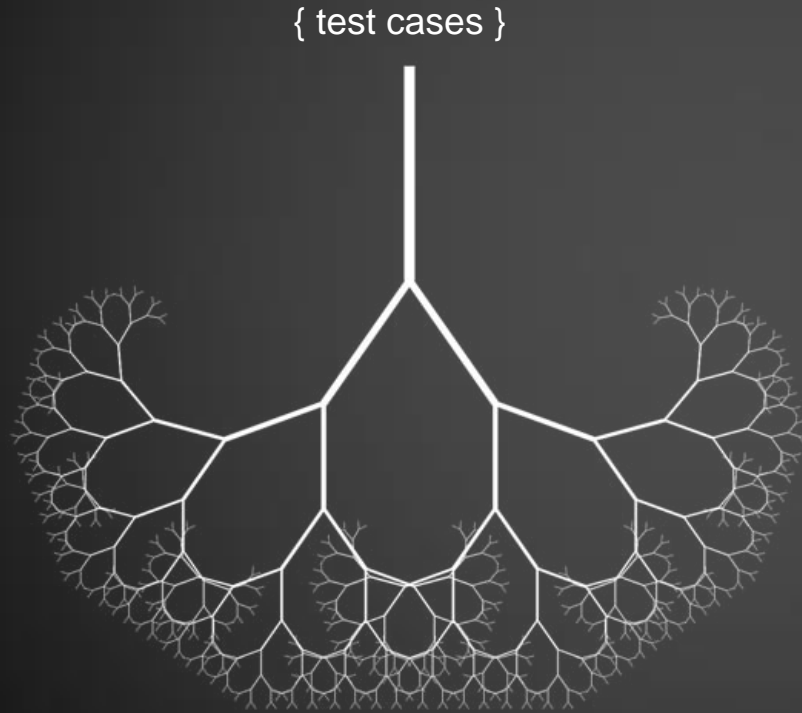
```
// val nums = List(1, 3)
// val colors = List("red", "green")
> result.foreach(println)
  der
  derderder
  neerg
  neergneergneerg
```



Fuzzing is $O(2^n)$



Generators can get large!



- Many function codes
- Many objects
- Header types
- Many field values

Types of Vulnerabilities

The collage features several overlapping windows from different applications and the Windows operating system:

- ASE 2000 V2 Communications Test Set**: A window with a blue header and a message: "ASE 2000 V2 Communications Test Set has encountered a problem. We are sorry for the inconvenience. Index was out of range. Must be non-negative and less than collection. Parameter name: index. Please tell Applied Systems Engineering about this problem. To help improve the software you use, Applied Systems Engineering is in learning more about this error. We have created a report about the error you to send to us." It includes a "Powered by" logo for {sa} and buttons for "Save as File", "Send Error Report", and "Done".
- Microsoft Visual C++ Runtime Library**: A blue-titled error box with a red 'X' icon. Text: "Buffer overrun detected! Program: ...ogram Files\Matrikon\OPC\SCADA DNP3\OPCDnp.exe. A buffer overrun has been detected which has corrupted the program internal state. The program cannot safely continue execution and it now be terminated." with an "OK" button.
- Services**: A window showing a warning icon and text: "Windows could not stop the TOP Server 5.11 Runtime service on Local Computer. Error 1053: The service did not respond to the start or control request in a timely fashion."
- 16-bit Analog Input - Object 30 Variation 2**: A white box with text: "Qualifier Code: 32-Bit Start and Stop Indices, Index Prefixing: None, First Point Index Number: 1, Last Point Index Number: 1, There are -1 point(s)/item(s)".
- DNP 3.0 devices network simulator**: A window with a red 'X' icon and text: "Access violation at address 00404074 in module 'DNPSim.exe'. Read of address 00690016." with an "OK" button.
- System Statistics**: Two side-by-side windows showing system metrics. The left one shows CPU Usage at 100%, Memory Usage (RAM) at 464140 KB, Memory Available (RAM) at 52264 KB, Storage Usage at 55800 KB, Storage Available at 1861320 KB, Number of Users Logged In at 1, USB A Port In Use at False, Current Project at THQ_RTAC, and Modified Time of Project. The right one shows similar metrics with CPU Usage at 18%, Memory Usage (RAM) at 44892 KB, Memory Available (RAM) at 471512 KB, Storage Usage at KB, Storage Available at KB, Number of Users Logged In at 1, USB A Port In Use at False, Current Project at THQ_RTAC, and Project failed. Running Fallover Project. It also shows a timestamp: 5-01 13:16:24.
- SEL RTAC Error**: A blue-titled error box with a question mark icon and text: "The application running on the SEL RTAC has failed. Continue anyway?" with "Yes" and "No" buttons.



Using Aegis

```
C:\aegis\aegis-console\bin>aegis-console -mid dnp3
```



```
Aegis Platform - CONFIDENTIAL - Automatak, LLC
```

```
Required argument not found: pid (Procedure id within module)
```

```
usage: aegis-console [flags ... ]
```

```
Valid module ids: [dnp3]
```

-mid	<arg>	Module id of protocol
-pid	<arg>	Procedure id within module
-host	<arg>(127.0.0.1)	IP address for client connection
-port	<arg>(20000)[0, 65535]	Port to connect or listen on
-listen		Listens on the specified port instead of connecting
-help		Prints help information
-start	<arg>	Starts testing at a specified test case #
-count	<arg>	Limits execution to the specified number of test cases
-repeats	<arg>(1)[min=1]	Number of times to repeat the specified test case



```

1
13:48:51.049 - >> 05 64 0A C4 00 04 01 00 2D 3F C0 7F 0F 00 00 E2 AE
13:48:51.049 - -> master: true pri: true fcb: false fcv: false func: REQUEST_LINK_STATES(0x09) 0xC9 length: 5 dest: 1024 src: 1
13:48:51.050 - >> 05 64 05 C9 00 04 01 00 98 81
13:48:51.263 - <- master: false pri: false fcb: false fcv: false func: LINK_STATUS(0x0B) 0x0B length: 5 dest: 1 src: 1024
13:48:51.263 - Test: 2301 - ahfuzz[4176] - Pass
13:48:51.263 - Test: 2302 - ahfuzz[4176] - Begin
13:48:51.263 - => fir: false fin: true con: true uns: true seq: 0x0F func: InitData(0x0F) IIN(0xFF: [AllStations, Class1Events,
Class2Events, Class3Events, NeedTime, LocalControl, DeviceTrouble, DeviceRestart] : 0xFF: [FuncNotSupported, ObjectUnknown, ParamError, EventBufferOverflow, AlreadyExecuting, ConfigCorrupt, Reserved1, Reserved2])
13:48:51.263 - ~> fir: true fin: true seq: 0 payload size: 4
13:48:51.263 - -> master: true pri: true fcb: false fcv: false func: UNCONFIRMED_USER_DATA(0x04) 0xC4 length: 10 dest: 1024 src:
1
13:48:51.263 - >> 05 64 0A C4 00 04 01 00 2D 3F C0 7F 0F FF FF 27 C8
13:48:51.264 - -> master: true pri: true fcb: false fcv: false func: REQUEST_LINK_STATES(0x09) 0xC9 length: 5 dest: 1024 src: 1
13:48:51.264 - >> 05 64 05 C9 00 04 01 00 98 81
13:48:51.464 - <- master: false pri: false fcb: false fcv: false func: LINK_STATUS(0x0B) 0x0B length: 5 dest: 1 src: 1024
13:48:51.464 - Test: 2302 - ahfuzz[4176] - Pass
13:48:51.464 - Test: 2303 - ahfuzz[4176] - Begin
13:48:51.464 - => fir: false fin: true con: true uns: true seq: 0x0F func: InitData(0x0F)
13:48:51.464 - ~> fir: true fin: true seq: 0 payload size: 2
13:48:51.464 - -> master: true pri: true fcb: false fcv: false func: UNCONFIRMED_USER_DATA(0x04) 0xC4 length: 8 dest: 1024 src:
1
13:48:51.465 - >> 05 64 08 C4 00 04 01 00 9A 19 C0 7F 0F FB 35
13:48:51.465 - -> master: true pri: true fcb: false fcv: false func: REQUEST_LINK_STATES(0x09) 0xC9 length: 5 dest: 1024 src: 1
13:48:51.465 - >> 05 64 05 C9 00 04 01 00 98 81
13:48:51.511 - Retrying link status: 11 attempts remaining
13:48:51.511 - -> master: true pri: true fcb: false fcv: false func: REQUEST_LINK_STATES(0x09) 0xC9 length: 5 dest: 1024 src: 1
13:48:51.511 - >> 05 64 05 C9 00 04 01 00 98 81
13:48:51.512 - Retrying link status: 10 attempts remaining
13:48:51.512 - -> master: true pri: true fcb: false fcv: false func: REQUEST_LINK_STATES(0x09) 0xC9 length: 5 dest: 1024 src: 1
13:48:51.512 - >> 05 64 05 C9 00 04 01 00 98 81
13:48:51.514 - The target has dropped the connection: Broken pipe

```

Examples

Run 10 link layer test cases starting at #123

```
$ aegis-console -mid dnp3 -pid lfuzz -start 123 -count 10
```

Unsolicited response fuzzing of a master listening on default port 20000 with master address of 0 and an outstation address of 1

```
$ aegis-console -mid dnp3 -pid aufuzz -dest 0 -src 1 -master -listen
```

Outstation link layer fuzzing test case #100 only

```
$ aegis-console -mid dnp3 -pid lfuzz -start 100 -count 1
```

Outstation application object fuzzing against 192.168.1.55:20001 with default addressing

```
$ aegis-console -mid dnp3 -id aofuzz -host 192.168.1.55 -port 20001
```



Recorded Demos

Video 1: a DNP3 outstation

`-pid aofuzz`

Video 2: a DNP3 master

`-pid aufuzz -listen -master -seed 1`



White-box vs. Black-box Testing

- Defender has the advantage, but has to choose to exercise it.
- Software-based solutions allow developers to test continually.

There are many OSS tools of the trade.

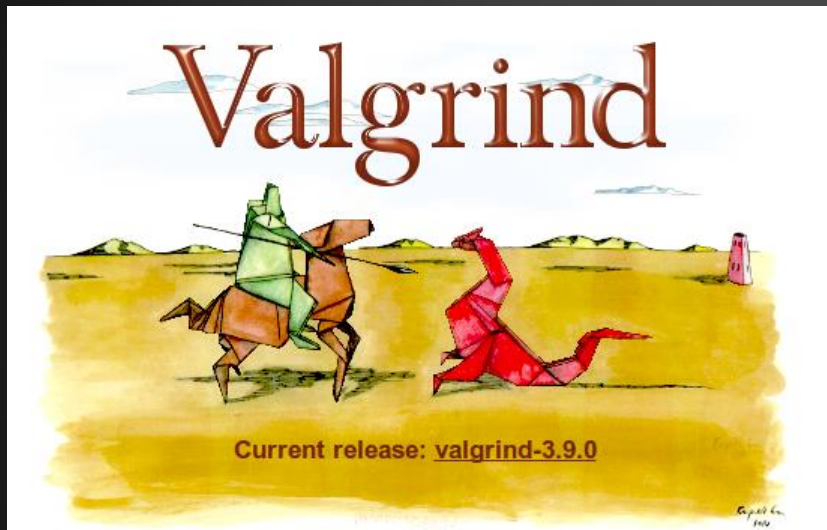


Code Coverage with gcov

- If you don't run a line of code, you'll never find a bug in it
- Important metric, but not a guarantee of success



Dynamic Analysis with Valgrind



valgrind.org

- Virtualized binary execution
 - hooks system calls
- memcheck is your friend
 - leaks
 - overrun / underrun
 - user after free
- callgrind
 - find true bottlenecks

Wurldtech Achilles - open^{dnp3} outstation - SELECT code coverage

```
430 : void Slave::HandleSelect(const APDU& arRequest, SequenceInfo aSeqInfo)
431 : {
432 :     mResponse.Set(FC_RESPONSE);
433 :     uint8_t seq = arRequest.GetControl().SEQ;
434 :     18 : for (HeaderReadIterator hdr = arRequest.BeginRead(); !hdr.IsEnd(); ++hdr) {
435 :         10 : ObjectReadIterator i = hdr.BeginRead();
436 :         10 : QualifierCode qual = i.Header().GetQualifier();
437 :         10 : switch (MACRO_DNP_RADIX(hdr->GetGroup(), hdr->GetVariation())) {
438 :             :
439 :             : case (MACRO_DNP_RADIX(12, 1)):
440 :                 0 : this->RespondToCommands<ControlRelayOutputBlock>(Group12Var1::Inst(), i, [=](ControlRelayOutputBlock cmd, size_t idx) {
441 :                     0 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
442 :                 });
443 :             :
444 :             : case (MACRO_DNP_RADIX(41, 1)):
445 :                 2 : this->RespondToCommands<AnalogOutputInt32>(Group41Var1::Inst(), i, [=](AnalogOutputInt32 cmd, size_t idx) {
446 :                     0 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
447 :                 });
448 :             :
449 :             : case (MACRO_DNP_RADIX(41, 2)):
450 :                 0 : this->RespondToCommands<AnalogOutputInt16>(Group41Var2::Inst(), i, [=](AnalogOutputInt16 cmd, size_t idx) {
451 :                     0 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
452 :                 });
453 :             :
454 :             : case (MACRO_DNP_RADIX(41, 3)):
455 :                 0 : this->RespondToCommands<AnalogOutputFloat32>(Group41Var3::Inst(), i, [=](AnalogOutputFloat32 cmd, size_t idx) {
456 :                     0 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
457 :                 });
458 :             :
459 :             : case (MACRO_DNP_RADIX(41, 4)):
460 :                 0 : this->RespondToCommands<AnalogOutputDouble64>(Group41Var4::Inst(), i, [=](AnalogOutputDouble64 cmd, size_t idx) {
461 :                     0 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
462 :                 });
463 :             :
464 :             : default:
465 :                 8 : mRspIIN.SetFuncNotSupported(true);
466 :                 10 : ERROR_BLOCK(LEV_WARNING, "Object/Function mismatch", SERR_OBJ_FUNC_MISMATCH);
467 :             :
468 :             : break;
469 :             :
470 :             : break;
471 :             :
472 :             : break;
473 :             :
474 :             : break;
475 :             :
476 :             : break;
477 :             :
478 :             : break;
```



Mu4000 - open^{dnp3} outstation - SELECT code coverage

```
424 : void Slave::HandleSelect(const APDU& arRequest, SequenceInfo aSeqInfo)
425 : {
426 :     7436 :     mResponse.Set(FC_RESPONSE);
427 :     7436 :     uint8_t seq = arRequest.GetControl().SEQ;
428 :
429 :
430 :     14872 :     for (HeaderReadIterator hdr = arRequest.BeginRead(); !hdr.IsEnd(); ++hdr) {
431 :
432 :         7436 :         ObjectReadIterator i = hdr.BeginRead();
433 :         7436 :         QualifierCode qual = i.Header().GetQualifier();
434 :
435 :         7436 :         switch (MACRO_DNP_RADIX(hdr->GetGroup(), hdr->GetVariation())) {
436 :
437 :             case (MACRO_DNP_RADIX(12, 1)):
438 :                 14848 :                 this->RespondToCommands<ControlRelayOutputBlock>(Group12Var1::Inst(), i, [=](ControlRelayOutputBlock cmd, size_t idx) {
439 :                     7424 :                     return this->mSBOHandler.Select(cmd, idx, seq, qual);
440 :                 });
441 :                 22272 :                 break;
442 :
443 :             case (MACRO_DNP_RADIX(41, 1)):
444 :                 0 :                 this->RespondToCommands<AnalogOutputInt32>(Group41Var1::Inst(), i, [=](AnalogOutputInt32 cmd, size_t idx) {
445 :                     0 :                     return this->mSBOHandler.Select(cmd, idx, seq, qual);
446 :                 });
447 :                 0 :                 break;
448 :
449 :             case (MACRO_DNP_RADIX(41, 2)):
450 :                 0 :                 this->RespondToCommands<AnalogOutputInt16>(Group41Var2::Inst(), i, [=](AnalogOutputInt16 cmd, size_t idx) {
451 :                     0 :                     return this->mSBOHandler.Select(cmd, idx, seq, qual);
452 :                 });
453 :                 0 :                 break;
454 :
455 :             case (MACRO_DNP_RADIX(41, 3)):
456 :                 0 :                 this->RespondToCommands<AnalogOutputFloat32>(Group41Var3::Inst(), i, [=](AnalogOutputFloat32 cmd, size_t idx) {
457 :                     0 :                     return this->mSBOHandler.Select(cmd, idx, seq, qual);
458 :                 });
459 :                 0 :                 break;
460 :
461 :             case (MACRO_DNP_RADIX(41, 4)):
462 :                 0 :                 this->RespondToCommands<AnalogOutputDouble64>(Group41Var4::Inst(), i, [=](AnalogOutputDouble64 cmd, size_t idx) {
463 :                     0 :                     return this->mSBOHandler.Select(cmd, idx, seq, qual);
464 :                 });
465 :                 0 :                 break;
466 :
467 :             default:
468 :                 12 :                 mRspIIN.SetFuncNotSupported(true);
469 :                 12 :                 ERROR_BLOCK(LEV_WARNING, "Object/Function mismatch", SERR_OBJ_FUNC_MISMATCH);
470 :
471 :                 12 :                 break;
472 :
473 :         }
474 :     }
475 : }
```

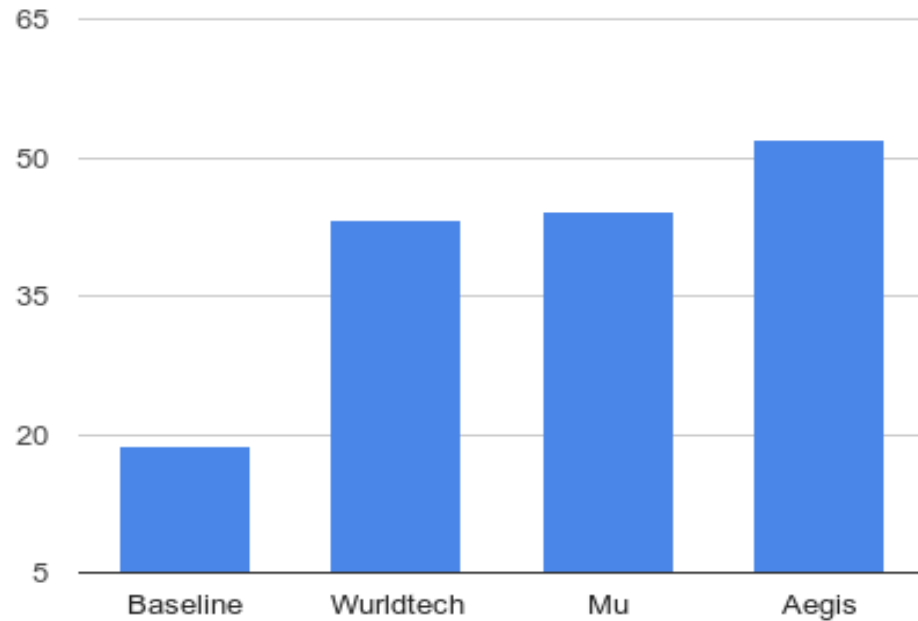


Aegis™ 0.1.0 - open**dnp3** outstation - SELECT code coverage

```
430 : void Slave::HandleSelect(const APDU& arRequest, SequenceInfo aSeqInfo)
431 : {
432 :     {
433 :         mResponse.Set(FC_RESPONSE);
434 :         uint8_t seq = arRequest.GetControl().SEQ;
435 :
436 :         for (HeaderReadIterator hdr = arRequest.BeginRead(); !hdr.IsEnd(); ++hdr) {
437 :             {
438 :                 ObjectReadIterator i = hdr.BeginRead();
439 :                 QualifierCode qual = i.Header().GetQualifier();
440 :
441 :                 switch (MACRO_DNP_RADIX(hdr->GetGroup(), hdr->GetVariation())) {
442 :                     :
443 :                     case (MACRO_DNP_RADIX(12, 1)):
444 :                         10 : this->RespondToCommands<ControlRelayOutputBlock>(Group12Var1::Inst(), i, [=](ControlRelayOutputBlock cmd, size_t idx) {
445 :                         9 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
446 :                         29 : });
447 :                         9 : break;
448 :                     :
449 :                     case (MACRO_DNP_RADIX(41, 1)):
450 :                         9 : this->RespondToCommands<AnalogOutputInt32>(Group41Var1::Inst(), i, [=](AnalogOutputInt32 cmd, size_t idx) {
451 :                         9 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
452 :                         27 : });
453 :                         9 : break;
454 :                     :
455 :                     case (MACRO_DNP_RADIX(41, 2)):
456 :                         9 : this->RespondToCommands<AnalogOutputInt16>(Group41Var2::Inst(), i, [=](AnalogOutputInt16 cmd, size_t idx) {
457 :                         9 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
458 :                         27 : });
459 :                         9 : break;
460 :                     :
461 :                     case (MACRO_DNP_RADIX(41, 3)):
462 :                         9 : this->RespondToCommands<AnalogOutputFloat32>(Group41Var3::Inst(), i, [=](AnalogOutputFloat32 cmd, size_t idx) {
463 :                         9 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
464 :                         27 : });
465 :                         9 : break;
466 :                     :
467 :                     case (MACRO_DNP_RADIX(41, 4)):
468 :                         9 : this->RespondToCommands<AnalogOutputDouble64>(Group41Var4::Inst(), i, [=](AnalogOutputDouble64 cmd, size_t idx) {
469 :                         9 : return this->mSBOHandler.Select(cmd, idx, seq, qual);
470 :                         27 : });
471 :                         9 : break;
472 :                     :
473 :                     default:
474 :                         276 : mRspIIN.SetFuncNotSupported(true);
475 :                         277 : ERROR_BLOCK(LEV_WARNING, "Object/Function mismatch", SERR_OBJ_FUNC_MISMATCH);
476 :                         276 : break;
477 :                     :
478 :                 }
479 :             }
480 :         }
```



opendnp3 outstation code coverage %

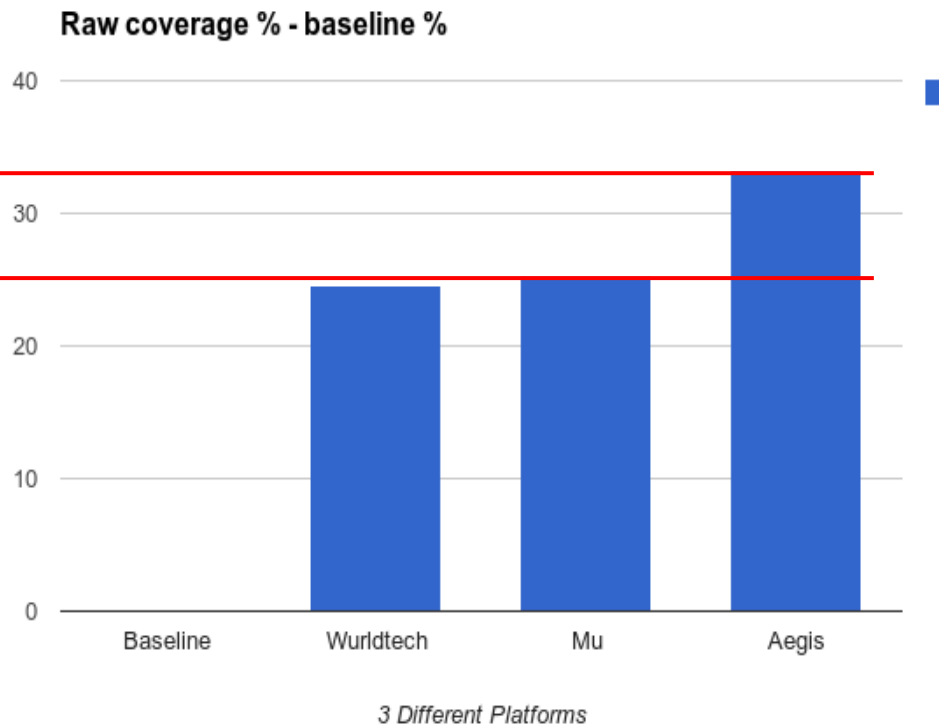


3 Diferent Platforms

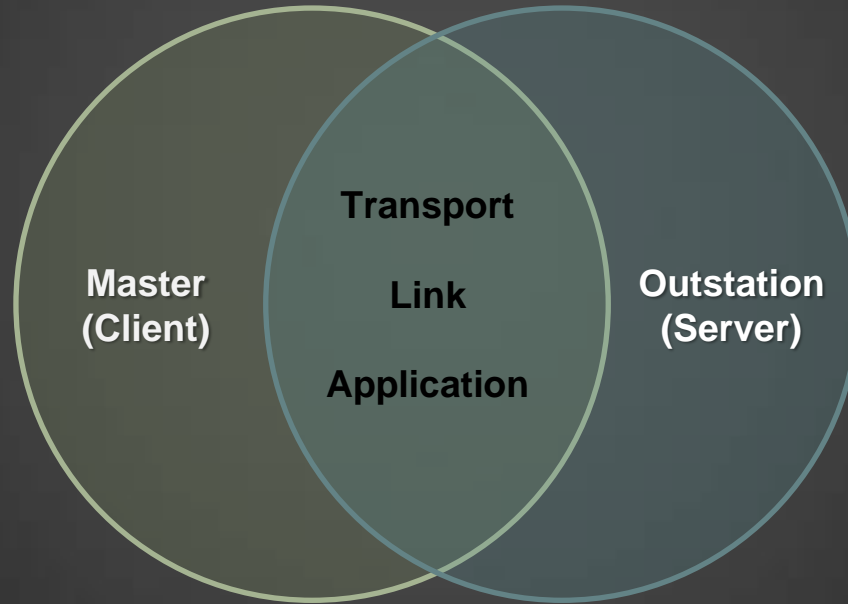


8%

33% more
relative to
others



Total code coverage



More Fuzzers are Better

- Aegis fuzzing with every release
- In-memory fuzzing
- Checks from Codenomicon, Wurldtech, Mu



Fuzzing is just another tool

- Unit test coverage in excess of 90%
- Valgrind for dynamic analysis
- Open source conformance test harness
- Static analysis using Clang / Coverity / CppCheck

*open***dnp**3



Security!

HOST Grant

- Adding authentication (SAv5)
- Adding encryption (TLS)

<http://investments.opencybersecurity.org/>

*open***dnp3**



SHODAN update

Probably default configs

- Many similar responses
- Same DNP Addresses

```
python shell
```

```
>>> " ".join("%02x" % ord(i) for  
i in "DNP3 paste from shodan")
```

Unsolicited Response, IIN
Restart & Need Time
Synch

Unsolicited Response with
Binary and Analog Data

Class 1/2/3/0 Poll!!!



SHODAN search results for port:20000.

Top Countries

Country	Count
United States	273
Slovakia	30
Turkey	15
Korea, Republic of	10
Italy	8

Top Organizations

Organization	Count
Verizon Wireless	127
Spacenet	51
AT&T Wireless	34
Orange Slovensko, a.s.	26
Vodafone Telekomunikas...	3

Top Domains

Domain	Count
myvzw.com	127
starband.net	49
mycingular.net	34
orange.sk	28
direcway.com	5

Search Results:

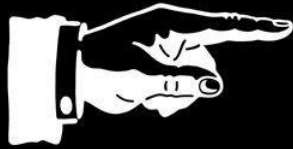
- 37.25.89.182**
Manx Telecom
Added on 07.03.2014
Details
host37.25.89.182.manx.net
- 216.45.67.67**
Gvec.net
Added on 06.03.2014
Details
216-45-67-67.gvec.net
- 166.159.65.200**
Verizon Wireless
Added on 05.03.2014
Details
200.sub-166-159-65.myvzw.com
- 148.64.230.90**
Spacenet
Added on 05.03.2014
Details
misc-148-64-230-90.pool.starband.net
- 148.64.230.42**
Spacenet
Added on 04.03.2014
Details
misc-148-64-230-42.pool.starband.net
- 85.237.243.85**
Orange Slovensko, a.s.
Added on 04.03.2014
Details



Conclusions

- DNP3 is not a special case, other protocols same fate
Modbus, IEC 60870, IEC 61850, ICCP, EtherNet/IP...
- Early testing both slave/server AND master/client sides of the protocol are important!
- Compliance != Security, but the culture is important
- Don't have to be a nation/state or large firm to do this
- A few good folks can make a difference in the industry

Go fuzz
yourself
before
does...



Questions?



@jadamcrain

@chrissistrunk

