

Please let the students know that for the Table 3: PISO Simulation Results, there is one more row with "Load/Shift (1 for load)" with the following values. The new table is as shown below.

**Table 3: PISO Simulation Results**

Clock Pulse Rising Edge	1	2	3	4	5	6	7	8	9
I0	1	1	1	1	1	1	1	0	0
I1	0	0	0	1	0	1	1	0	0
I2	1	1	1	0	0	1	1	1	0
I3	1	1	1	0	0	1	1	0	0
Load/Shift (1 for Load)	1	0	0	1	1	1	0	1	0
Output									

Along with this, there is also an amendment to the PISO question with the following added:

b) Parallel-in to Serial-Out (PISO) (Initial value for first Mux = 0)