Table X

Cisco IOS Software, `<platform>` Software (`<image-id>`), Version `<software-version>`, `<software-type>`

Technical Support: http://www.cisco.com/techsupport

Copyright (c) `<date-range>` by Cisco Systems, Inc.

Compiled `<day>` `<date>` `<time>` by `<compiler-id>`

ROM: System Bootstrap, Version `<software-version>`, `<software-type>`

BOOTLDR: `<platform>` Software (image-id), Version `<software-version>`, `<software-type>`

`<router-name>` uptime is `<w>` weeks, `<d>` days, `<h>` hours, `<m>` minutes

System returned to ROM by reload at `<time>` `<day>` `<date>`

System image file is "`<filesystem-location>`/`<software-image-name>`"

Last reload reason: `<reload-reason>`

Cisco `<platform-processor-type>` processor (revision `<processor-revision-id>`) with `<free-DRAM-memory>`K/`<packet-memory>`K bytes of memory.

Processor board ID `<ID-number>`

`<CPU-type>` CPU at `<clock-speed>`Mhz, Implementation `<number>`, Rev `<Revision-number>`, `<kilobytes-Processor-Cache-Memory>`KB `<cache-Level>` Cache

Table X1

```
Cisco IOS Software, 7200 Software (C7200-JS-M), Version 12.4(4)T, RELEASE SOFTW)
 Technical Support: http://www.cisco.com/techsupport
 Copyright (c) 1986-2005 by Cisco Systems, Inc.
 Compiled Thu 27-Oct-05 05:58 by ccai
```

1

```
ROM: System Bootstrap, Version 12.1(20000710:044039) [nlaw-121E_npeb 117], DEVEE

BOOTLDR: 7200 Software (C7200-KBOOT-M), Version 12.3(16), RELEASE SOFTWARE (fc4)


router uptime is 5 days, 18 hours, 2 minutes

System returned to ROM by reload at 02:45:12 UTC Tue Feb 14 2006

System image file is "disk0:c7200-js-mz.124-4.T"

Last reload reason: Reload Command


Cisco 7206VXR (NPE400) processor (revision A) with 491520K/32768K bytes of memo.

Processor board ID 26793934

R7000 CPU at 350MHz, Implementation 39, Rev 3.2, 256KB L2 Cache

6 slot VXR midplane, Version 2.6


Last reset from power-on


PCI bus mb0_mb1 (Slots 0, 1, 3 and 5) has a capacity of 600 bandwidth points.

Current configuration on bus mb0_mb1 has a total of 440 bandwidth points.

This configuration is within the PCI bus capacity and is supported.


PCI bus mb2 (Slots 2, 4, 6) has a capacity of 600 bandwidth points.

Current configuration on bus mb2 has a total of 390 bandwidth points

This configuration is within the PCI bus capacity and is supported.


Please refer to the following document "Cisco 7200 Series Port Adaptor

Hardware Configuration Guidelines" on Cisco.com <http://www.cisco.com>

for c7200 bandwidth points oversubscription and usage guidelines.


4 Ethernet interfaces

2 FastEthernet interfaces
```

```
  2 ATM interfaces

  125K bytes of NVRAM.


  62976K bytes of ATA PCMCIA card at slot 0 (Sector size 512 bytes).

  125952K bytes of ATA PCMCIA card at slot 1 (Sector size 512 bytes).

  8192K bytes of Flash internal SIMM (Sector size 256K).

Configuration register is 0x2002 (0x2102)

___

Cisco IOS Software, 7200 Software (C7200-JS-M), Version 12.4(4)T, RELEASE SOFTW)

  Technical Support: http://www.cisco.com/techsupport

  Copyright (c) 1986-2005 by Cisco Systems, Inc.

  Compiled Thu 25-Oct-05 07:27 by shamrock
```

Table XII

```
Router# show running-config


  Building configuration...


  Current configuration:

  !

  version 12.0

  service timestamps debug datetime localtime

  service timestamps log datetime localtime

  no service password-encryption

  !

  hostname Four-Leaf-Clover

  !

  boot buffersize 126968

  boot system flash slot0:7600r

  boot bootldr bootflash:c6msfc-boot-mz.120-6.5T.XE1.0.83.bin
```

```
enable password labrador
!
clock timezone Pacific -8
clock summer-time Daylight recurring
redundancy
main-cpu
auto-sync standard
!
ip subnet-zero
!
ip domain-name rad-turtle.com
no ip finger
ip multicast-routing
ip dvmrp route-limit 20000
ip cef

mls flow ip destination
mls flow ipx destination
cns event-service server
!
spanning-tree portfast bpdu-guard
spanning-tree uplinkfast
spanning-tree vlan 200 forward-time 21
port-channel load-balance sdip
!
!
!
shutdown
!
!
```

-
-
-