APPENDIX A

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Sample Boot Setting File (BSF)

```
/** @file

  Boot Setting File for Platform Configuration.

  This file contains an 'Intel Peripheral Driver' and is
  licensed for Intel CPUs and chipsets under the terms of your
  license agreement with Intel or your vendor.  This file must not
  be modified by end users or could render the generated boot loader
  inoperable.

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  Intel Corporation.

  This file is automatically generated. Please do NOT modify !!!

**/

GlobalDataDef
    SKUID = 0, "DEFAULT"
EndGlobalData
```

StructDef

```
    Find "VLV2UPDR"
        Skip 24 bytes
        $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitTsegSize        2 bytes
        $_DEFAULT_ = 0x0001
        $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitMmioSize        2 bytes
        $_DEFAULT_ = 0x0800
        $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitSPDAddr1        1 bytes
        $_DEFAULT_ = 0xA0
        $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitSPDAddr2        1 bytes
        $_DEFAULT_ = 0xA2
        $gPlatformFspPkgTokenSpaceGuid_PcdeMMCBootMode           1 bytes
        $_DEFAULT_ = 2
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableSdio             1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableSdcard           1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableHsuart0          1 bytes
        $_DEFAULT_ = 0
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableHsuart1          1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableSpi              1 bytes
        $_DEFAULT_ = 1
        Skip 1 bytes
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableSata             1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdSataMode               1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableAzalia           1 bytes
        $_DEFAULT_ = 0
        $gPlatformFspPkgTokenSpaceGuid_AzaliaConfigPtr           4 bytes
        $_DEFAULT_ = 0
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableXhci             1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableLpe              1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdLpssSioEnablePciMode   1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableDma0             1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableDma1             1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C0             1 bytes
        $_DEFAULT_ = 1
        $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C1             1 bytes
        $_DEFAULT_ = 1
```

```
$gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C2          1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C3          1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C4          1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C5          1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C6          1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_PcdEnablePwm0          1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_PcdEnablePwm1          1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_PcdEnableHsi           1 bytes
$_DEFAULT_ = 0
$gPlatformFspPkgTokenSpaceGuid_PcdIgdDvmt50PreAlloc   1 bytes
$_DEFAULT_ = 2
$gPlatformFspPkgTokenSpaceGuid_PcdApertureSize        1 bytes
$_DEFAULT_ = 2
$gPlatformFspPkgTokenSpaceGuid_PcdGttSize             1 bytes
$_DEFAULT_ = 2
Skip 5 bytes
$gPlatformFspPkgTokenSpaceGuid_PcdMrcDebugMsg         1 bytes
$_DEFAULT_ = 0
$gPlatformFspPkgTokenSpaceGuid_ISPEnable              1 bytes
$_DEFAULT_ = 0
$gPlatformFspPkgTokenSpaceGuid_PcdSccEnablePciMode    1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_IgdRenderStandby       1 bytes
$_DEFAULT_ = 0
$gPlatformFspPkgTokenSpaceGuid_TxeUmaEnable           1 bytes
$_DEFAULT_ = 0
$gPlatformFspPkgTokenSpaceGuid_PcdOsSelection         1 bytes
$_DEFAULT_ = 0x4
$gPlatformFspPkgTokenSpaceGuid_eMMC45DDR50Enabled     1 bytes
$_DEFAULT_ = 1
$gPlatformFspPkgTokenSpaceGuid_eMMC45HS200Enabled     1 bytes
$_DEFAULT_ = 0
$gPlatformFspPkgTokenSpaceGuid_eMMC45RetuneTimerValue 1 bytes
$_DEFAULT_ = 8
Skip 156 bytes
$gPlatformFspPkgTokenSpaceGuid_PcdEnableMemoryDown    1 bytes
$_DEFAULT_ = 0x00
$gPlatformFspPkgTokenSpaceGuid_PcdDRAMSpeed           1 bytes
$_DEFAULT_ = 0x02
```

```
    $gPlatformFspPkgTokenSpaceGuid_PcdDRAMType                    1 bytes
    $_DEFAULT_ = 0x01
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMM0Enable                 1 bytes
    $_DEFAULT_ = 0x01
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMM1Enable                 1 bytes
    $_DEFAULT_ = 0x00
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMDWidth                  1 bytes
    $_DEFAULT_ = 0x00
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMDensity                 1 bytes
    $_DEFAULT_ = 0x01
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMBusWidth                1 bytes
    $_DEFAULT_ = 0x03
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMSides                   1 bytes
    $_DEFAULT_ = 0x00
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtCL                     1 bytes
    $_DEFAULT_ = 0x09
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtRPtRCD                 1 bytes
    $_DEFAULT_ = 0x09
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtWR                     1 bytes
    $_DEFAULT_ = 0x0A
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtWTR                    1 bytes
    $_DEFAULT_ = 0x05
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtRRD                    1 bytes
    $_DEFAULT_ = 0x04
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtRTP                    1 bytes
    $_DEFAULT_ = 0x05
    $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtFAW                    1 bytes
    $_DEFAULT_ = 0x14

Find "VLYVIEW1"
    $gPlatformFspPkgTokenSpaceGuid_PcdImageRevision              4
    bytes    $_DEFAULT_ = 0x00000303
    Skip 24 bytes
    $gPlatformFspPkgTokenSpaceGuid_PcdPlatformType               1
    bytes    $_DEFAULT_ = 2
    $gPlatformFspPkgTokenSpaceGuid_PcdEnableSecureBoot           1
    bytes    $_DEFAULT_ = 2

EndStruct

List &EN_DIS
    Selection 0x1 , "Enabled"
    Selection 0x0 , "Disabled"
EndList
```

```
List &gPlatformFspPkgTokenSpaceGuid_PcdDIMMSides
    Selection 0x0 , "1 Ranks"
    Selection 0x1 , "2 Ranks"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdIgdDvmt50PreAlloc
    Selection 0x01 , "32 MB"
    Selection 0x02 , "64 MB"
    Selection 0x03 , "96 MB"
    Selection 0x04 , "128 MB"
    Selection 0x05 , "160 MB"
    Selection 0x06 , "192 MB"
    Selection 0x07 , "224 MB"
    Selection 0x08 , "256 MB"
    Selection 0x09 , "288 MB"
    Selection 0x0A , "320 MB"
    Selection 0x0B , "352 MB"
    Selection 0x0C , "384 MB"
    Selection 0x0D , "416 MB"
    Selection 0x0E , "448 MB"
    Selection 0x0F , "480 MB"
    Selection 0x10 , "512 MB"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdOsSelection
    Selection 0x1 , "Android"
    Selection 0x4 , "Linux OS"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdDIMMDensity
    Selection 0x0 , "1 Gbit"
    Selection 0x1 , "2 Gbit"
    Selection 0x2 , "4 Gbit"
    Selection 0x3 , "8 Gbit"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdEnableLpe
    Selection 0x2 , "ACPI Mode"
    Selection 0x1 , "PCI Mode"
    Selection 0x0 , "Disabled"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdDRAMType
    Selection 0x0 , "DDR3"
    Selection 0x1 , "DDR3L"
    Selection 0x2 , "DDR3ECC"
    Selection 0x4 , "LPDDR2"
```

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    Selection 0x5 , "LPDDR3"
    Selection 0x6 , "DDR4"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdMrcInitTsegSize
    Selection 0x01 , "1 MB"
    Selection 0x02 , "2 MB"
    Selection 0x04 , "4 MB"
    Selection 0x08 , "8 MB"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdPlatformType
    Selection 0x2 , "BayleyBay Platform Type"
    Selection 0x3 , "BakerSport Platform (ECC) Type"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdMrcInitMmioSize
    Selection 0x400 , "1.0 GB"
    Selection 0x600 , "1.5 GB"
    Selection 0x800 , "2.0 GB"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdGttSize
    Selection 0x1 , "1 MB"
    Selection 0x2 , "2 MB"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdSataMode
    Selection 1 , "AHCI"
    Selection 0 , "IDE"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdDIMMBusWidth
    Selection 0x0 , "8 bits"
    Selection 0x1 , "16 bits"
    Selection 0x2 , "32 bits"
    Selection 0x3 , "64 bits"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdEnableSecureBoot
    Selection 0 , "Disabled"
    Selection 1 , "Enabled"
    Selection 2 , "Auto"
EndList
```

```
List &gPlatformFspPkgTokenSpaceGuid_PcdApertureSize
    Selection 0x1 , "128 MB"
    Selection 0x2 , "256 MB"
    Selection 0x3 , "512 MB"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdDRAMSpeed
    Selection 0x0 , "800 MT/s"
    Selection 0x1 , "1066 MT/s"
    Selection 0x2 , "1333 MT/s"
    Selection 0x3 , "1600 MT/s"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdDIMMDWidth
    Selection 0x0 , "x8"
    Selection 0x1 , "x16"
    Selection 0x2 , "x32"
EndList

List &gPlatformFspPkgTokenSpaceGuid_PcdeMMCBootMode
    Selection 0x0 , "Disabled"
    Selection 0x1 , "Auto"
    Selection 0x2 , "eMMC 4.1"
    Selection 0x3 , "eMMC 4.5"
EndList

BeginInfoBlock
    PPVer      "1.0"
    Description "BayTrail platform"
EndInfoBlock

Page "Platform"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdPlatformType, "Platform Type",
    &gPlatformFspPkgTokenSpaceGuid_PcdPlatformType,
        Help "Select Platform Type."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableSecureBoot, "Enable Secure
    Boot", &gPlatformFspPkgTokenSpaceGuid_PcdEnableSecureBoot,
        Help "Enable/disable secure boot. Auto by default."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdOsSelection, "OS Selection",
    &gPlatformFspPkgTokenSpaceGuid_PcdOsSelection,
        Help "Select Operating System"
EndPage

Page "Memory Down"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableMemoryDown, "Enable Memory
    Down", &EN_DIS,
        Help "Enable = Memory Down, Disable = DIMM"
```

```
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDRAMSpeed, "DRAM Speed",
    &gPlatformFspPkgTokenSpaceGuid_PcdDRAMSpeed,
        Help "DRAM Speed"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDRAMType, "DRAM Type",
    &gPlatformFspPkgTokenSpaceGuid_PcdDRAMType,
        Help "DRAM Type"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDIMM0Enable, "DIMM 0 Enable",
    &EN_DIS,
        Help "Please populate DIMM slot 0 if only one DIMM is supported."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDIMM1Enable, "DIMM 1 Enable",
    &EN_DIS,
        Help "Please populate DIMM slot 1 if only one DIMM is supported."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDIMMDWidth, "DIMM_DWidth",
    &gPlatformFspPkgTokenSpaceGuid_PcdDIMMDWidth,
        Help "DRAM device data width."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDIMMDensity, "DIMM_Density",
    &gPlatformFspPkgTokenSpaceGuid_PcdDIMMDensity,
        Help "DRAM device data density."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDIMMBusWidth, "DIMM_BusWidth",
    &gPlatformFspPkgTokenSpaceGuid_PcdDIMMBusWidth,
        Help "DIMM Bus Width."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdDIMMSides, "DIMM_Sides",
    &gPlatformFspPkgTokenSpaceGuid_PcdDIMMSides,
        Help "Ranks Per DIMM. "
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtCL, "tCL", DEC,
        Help "tCL"
            "Valid range: 1 ~ 255"
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtRPtRCD, "tRP_tRCD", DEC,
        Help "tRP and tRCD in DRAM clk - 5:12.5ns, 6:15ns, etc."
            "Valid range: 1 ~ 255"
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtWR, "tWR", DEC,
        Help "tWR in DRAM clk"
            "Valid range: 1 ~ 255"
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtWTR, "tWTR", DEC,
        Help "tWTR in DRAM clk"
            "Valid range: 1 ~ 255"
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtRRD, "tRRD", DEC,
        Help "tRRD in DRAM clk"
            "Valid range: 1 ~ 255"
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtRTP, "tRTP", DEC,
        Help "tRTP in DRAM clk"
            "Valid range: 1 ~ 255"
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdDIMMtFAW, "tFAW", DEC,
        Help "tFAW in DRAM clk"
            "Valid range: 1 ~ 255"
EndPage
```

```
Page "South Complex"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdeMMCBootMode, "eMMC Boot Mode",
    &gPlatformFspPkgTokenSpaceGuid_PcdeMMCBootMode,
        Help "Select EMMC Mode."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableSdio, "Enable SDIO",
    &EN_DIS,
        Help "Enable/disable SDIO."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableSdcard, "Enable SD Card",
    &EN_DIS,
        Help "Enable/disable SD Card."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableHsuart0, "Enable HSUART0",
    &EN_DIS,
        Help "Enable/disable HSUART0."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableHsuart1, "Enable HSUART1",
    &EN_DIS,
        Help "Enable/disable HSUART1."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableSpi, "Enable SPI",
    &EN_DIS,
        Help "Enable/disable SPI."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableSata, "Enable SATA",
    &EN_DIS,
        Help "Enable/disable SATA controller."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdSataMode, "SATA Mode",
    &gPlatformFspPkgTokenSpaceGuid_PcdSataMode,
        Help "Select SATA controller working mode."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableAzalia, "Enable Azalia",
    &EN_DIS,
        Help "Enable/disable Azalia controller."
    EditNum $gPlatformFspPkgTokenSpaceGuid_AzaliaConfigPtr, "Azalia
    Configuration Pointer", HEX,
        Help "Address of the Azalia Configuration Data."
            "Valid range: 0x00 ~ 0xFFFFFFFF"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableXhci, "Enable XHCI",
    &EN_DIS,
        Help "Enable/disable XHCI controller."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableLpe, "Enable LPE",
    &gPlatformFspPkgTokenSpaceGuid_PcdEnableLpe,
        Help "Choose LPE Mode."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdLpssSioEnablePciMode, "Enable
    PCI mode for LPSS SIO devices", &EN_DIS,
        Help "Enable PCI Mode for LPSS SIO devices. If disabled, LPSS SIO
        devices will run in ACPI mode."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableDma0, "Enable DMA0",
    &EN_DIS,
        Help "Enable/disable DMA0."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableDma1, "Enable DMA1",
    &EN_DIS,
        Help "Enable/disable DMA1."
```

```
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C0, "Enable I2C0",
    &EN_DIS,
        Help "Enable/disable I2C0."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C1, "Enable I2C1",
    &EN_DIS,
        Help "Enable/disable I2C1."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C2, "Enable I2C2",
    &EN_DIS,
        Help "Enable/disable I2C2."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C3, "Enable I2C3",
    &EN_DIS,
        Help "Enable/disable I2C3."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C4, "Enable I2C4", &EN_DIS,
        Help "Enable/disable I2C4."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C5, "Enable I2C5",
    &EN_DIS,
        Help "Enable/disable I2C5."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableI2C6, "Enable I2C6",
    &EN_DIS,
        Help "Enable/disable I2C6."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnablePwm0, "Enable PWM0",
    &EN_DIS,
        Help "Enable/disable PWM0."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnablePwm1, "Enable PWM1", &EN_DIS,
        Help "Enable/disable PWM1."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdEnableHsi, "Enable HSI", &EN_DIS,
        Help "Enable/disable HSI."
    Combo $gPlatformFspPkgTokenSpaceGuid_ISPEnable, "Enable ISP", &EN_DIS,
        Help "Enable/disable ISP."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdSccEnablePciMode, "Enable PCI
    mode for SCC devices", &EN_DIS,
        Help "Enable PCI Mode for SCC devices. If disabled, SCC devices will
        run in ACPI mode."
    Combo $gPlatformFspPkgTokenSpaceGuid_eMMC45DDR50Enabled, "eMMC45 DDR50",
    &EN_DIS,
        Help "Enable eMMC45 DDR50"
    Combo $gPlatformFspPkgTokenSpaceGuid_eMMC45HS200Enabled, "eMMC45 HS200",
    &EN_DIS,
        Help "Enable eMMC45 HS200"
    EditNum $gPlatformFspPkgTokenSpaceGuid_eMMC45RetuneTimerValue, "eMMC45
    Retune Timer Value", DEC,
        Help "Select Timer Value"
            "Valid range: 0 ~ 15"
EndPage
```

```
Page "North Complex"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitTsegSize, "Tseg Size",
    &gPlatformFspPkgTokenSpaceGuid_PcdMrcInitTsegSize,
        Help "Size of SMRAM memory reserved."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitMmioSize, "MMIO Size",
    &gPlatformFspPkgTokenSpaceGuid_PcdMrcInitMmioSize,
        Help "Size of memory address space reserved for MMIO (Memory
        Mapped I/O)."
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitSPDAddr1, "DIMM 0 SPD
    SMBus Address", HEX,
        Help "SPD Address of DIMM."
            "Valid range: 0x00 ~ 0xFF"
    EditNum $gPlatformFspPkgTokenSpaceGuid_PcdMrcInitSPDAddr2, "DIMM 1 SPD
    SMBus Address", HEX,
        Help "SPD Address of DIMM."
            "Valid range: 0x00 ~ 0xFF"
    Combo $gPlatformFspPkgTokenSpaceGuid_TxeUmaEnable, "Enable TXE UMA",
    &EN_DIS,
        Help "Enable/disable Unified Memory Reservation for TXE engine."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdMrcDebugMsg, "MRC RMT Message
    Display", &EN_DIS,
        Help "Enable/disable MRC RMT Message Display."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdIgdDvmt50PreAlloc, "Internal
    Graphics Pre-allocated Memory", &gPlatformFspPkgTokenSpaceGuid_
    PcdIgdDvmt50PreAlloc,
        Help "Size of memory preallocated for internal graphics"
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdApertureSize, "Aperture Size",
    &gPlatformFspPkgTokenSpaceGuid_PcdApertureSize,
        Help "Select the Aperture Size."
    Combo $gPlatformFspPkgTokenSpaceGuid_PcdGttSize, "GTT Size",
    &gPlatformFspPkgTokenSpaceGuid_PcdGttSize,
        Help "Select the GTT Size."
    Combo $gPlatformFspPkgTokenSpaceGuid_IgdRenderStandby, "Enable Render
    Standby", &EN_DIS,
        Help "Enable/disable Integrated Graphics Render Standby."
EndPage
```