

SROM fields				board name	
Byte Offset	Word Offset	Description		Value	Explanation
0	0	0	0	functionEnabled	
2	2	1	1	commonPower	
4	4	2	2	SubsystemID	
6	6	3	3	SubVendorID	
8	8	4	4	Ind/ASPM	
10	A	5	5	SER, L0/L1 latency	
12	C	6	6	DLink/portnum	
14	E	7	7	PowerBudget0	
16	10	8	8	PowerBudget1	
18	12	9	9	PowerBudget2	
20	14	10	A	PowerBudget3	
22	16	11	B	PowerBudget4	
24	18	12	C	MacPtr/PhyBits	
26	1A	13	D	TLP War	
28	1C	14	E	reserved	
30	1E	15	F	reserved	
32	20	16	10	deviceId	
34	22	17	11	classCode	
36	24	18	12	classCode	
38	26	19	13	PME/Pow	
40	28	20	14	b0l/pme/b1En&Sz	
42	2A	21	15	bar0Value	
44	2C	22	16	CISPointer	
46	2E	23	17	CISPointer	
48	30	24	18	Reserved	ffff
50	32	25	19		ffff
52	34	26	1A		ffff
54	36	27	1B		ffff
56	38	28	1C	Reservec	0000 Must be zero
58	3A	29	1D	wl1l/1h MaxPower	5Ghz low/high maxpower
60	3C	30	1E	wl1l PA b0	5Ghz low PA parms
62	3E	31	1F	wl1l PA b1	
64	40	32	20	wl1l PA b2	
66	42	33	21	wl1h PA b0	5Ghz high PA parms
68	44	34	22	wl1h PA b1	
70	46	35	23	wl1h PA b2	
72	48	36	24	Reservec	0000 Must be zero
74	4A	37	25	mac	0090 Mac hi
76	4C	38	26	mac	4cxx Mac mid
78	4E	39	27	mac	xxx> Mac lo
80	50	40	28	bx2g/rssiXX2g	boardswitcharch, rssiav, rssiic & rssiif for 2.4Ghz
82	52	41	29	bx2g/rssiXX5g	boardswitcharch, rssiav, rssiic & rssiif for 5Ghz
84	54	42	2A	tri5g/tri2g	trisolation 5 (mid) / 2.4 Ghz
86	56	43	2B	tri5gh/tri5gl	trisolation 5 Ghz (high/low)
88	58	44	2C	Reservec	0000 Must be zero
90	5A	45	2D	rxpo5g/rxpo2g	rxpoweroffset 2.4 / 5 Ghz
92	5C	46	2E	aa5g/2g,BdRev	Antennas avail, Board Rev
94	5E	47	2F	wl0 PA b0	2.4 Ghz PA parms
96	60	48	30	wl0 PA b1	
98	62	49	31	wl0 PA b2	
100	64	50	32	ledbh10	LED behavior 1/0
102	66	51	33	ledbh32	LED behavior 3/2
104	68	52	34	wl1/0 MaxPower	5 / 2.4 Ghz maxpower
106	6A	53	35	wl1 PA b0	5Ghz (mid) PA parms
108	6C	54	36	wl1 PA b1	
110	6E	55	37	wl1 PA b2	
112	70	56	38	itt	5 / 2.4 Ghz idle tssi target
114	72	57	39	boardflags	Boardflags
116	74	58	3A	ag0/1	AntennaGain
118	76	59	3B	cocode	0000 Worldwide
120	78	60	3C	regrev/ofdmof	Regulatory rev, opc
122	7A	61	3D	boardflags2	Boardflags bits 31-16
124	7C	62	3E	leddc	LED's duty cycle
126	7E	63	3F	Rev/CRC8	xx03 Rev 3