SROM fields					Boardname	
Byte (Offset		Offset	Description	Value	Explanation
0	0	0	0	functionEnabled		
2	2	1	1	commonPower		
4	4	2	2	SubsystemID		
6	6	3	3	SubVendorID		
8	8	4	4	deviceID		
10	A	5	5	classCode		
12	C	6	6	classCode		
14	E	7	7	PME/Pow		
16 18	10 12	8 9	8 9	b0l/pme/b1En&Sz bar0Value		
20	14	10	A	CISPointer		
22	16	11	В	CISPointer		
24	18	12	C	deviceID		
26	1A	13	D	classCode		
28	1C	14	Е	classCode		
30	1E	15	F	PME/Pow		
32	20	16	10	b0l/pme/b1En&Sz		
34	22	17	11	bar0Value		
36	24	18	12	CISPointer		
38	26	19	13	CISPointer		
40	28	20	14	deviceID		
42	2A	21	15	classCode		
44	2C	22	16	classCode		
46	2E	23	17	PME/Pow		
48	30	24	18	b0l/pme/b1En&Sz		
50	32	25	19	bar0Value		
52 54	34 36	26 27	1A 1B	CISPointer CISPointer		
56	38	28	1C	boardflags2		
58	3A	29	1D	wl1l/1h MaxPower		11a lo/hi maxpowei
60	3C	30	1E	wl1l PA b0		wl1 lo PA parms
62	3E	31	1F	will PA b1		William 7 painte
64	40	32	20	wl1l PA b2		
66	42	33	21	wl1h PA b0		wl1 hi PA parms
68	44	34	22	wl1h PA b1		·
70	46	35	23	wl1h PA b2		
72	48	36	24	mac	0090	machi (single band)
74	4A	37	25	mac	4cxx	macmid
76	4C	38	26	mac	XXXX	maclo
78	4E	39	27	mac	ffff	
80	50	40	28	mac	ffff	
82 84	52 54	41 42	29 2A	mac	ffff	machi (dual band)
84 86	54 56	42 43	2A 2B	wlmac wlmac	0090 4cxx	machi (dual band) macmid
88	58	43 44	2B 2C	wimac wimac	4CXX XXXX	macmid
90	5A	45	2D	enetphy	ffff	madio
92	5C	46	2E	aa1/0,cc,BdRev		Ant avail, in/out, bdrev
94	5E	47	2F	wl0 PA b0		wl0 PA parms
96	60	48	30	wl0 PA b1		
98	62	49	31	wl0 PA b2		
100	64	50	32	gpio10		
102	66	51	33	gpio32		
104	68	52	34	wl1/0 MaxPower		maxp
106	6A	53	35	wl1m PA b0		wl1 mid PA parms
108	6C	54	36	wl1m PA b1		
110	6E	55	37	wl1m PA b2		
112	70	56	38	itt		Boardflogs
114	72 74	57	39	boardflags		Boardflags
116 118	74 76	58 59	3A 3B	ag0/1		AntennaGain
120	78	60	3C	ccode reserverd/ofdmof		000
120	78 7A	61	3D	reserverd/ordinor	ffff	оро
124	7A 7C	62	3E	reserved	ffff	
126	7E	63	3F	Rev/CRC8	xx02	Rev 2
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