## 01\_data-import

## Downloading the Standard Operating Procedure example data

wget https://mothur.s3.us-east-2.amazonaws.com/wiki/miseqsopdata.zip

```
## --2020-11-24 21:10:19-- https://mothur.s3.us-east-2.amazonaws.com/wiki/miseqsopdata.zip
## Resolving mothur.s3.us-east-2.amazonaws.com (mothur.s3.us-east-2.amazonaws.com)... 52.219.80.160
## Connecting to mothur.s3.us-east-2.amazonaws.com (mothur.s3.us-east-2.amazonaws.com)|52.219.80.160|:4
## HTTP request sent, awaiting response... 200 OK
 Length: 36910055 (35M) [application/zip]
 Saving to: 'miseqsopdata.zip.2'
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  1% 58.7M 33s
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                           2% 54.3M 27s
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##	16350K		 	 45%	31.4M	2s
##	16400K		 	 45%	92.2M	2s
##	16450K		 	 45%	91.8M	2s
##	16500K		 	 45%	775K	2s
##	16550K		 	 46%	73.9M	2s
##	16600K		 	 46%	66.2M	2s
##	16650K		 	 46%	80.9M	2s
##	16700K		 	 46%	54.3M	2s
##	16750K		 	 46%	70.OM	2s
##	16800K		 	 46%	77.9M	2s
##	16850K		 	 46%	79.9M	2s
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##	22150K	 	 	61%	88.3M	1s
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##	23750K	 			66% 6	6.9M	1s
##	23800K	 			66% 3	8.7M	1s
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##	23950K	 			66% 6	4.9M	1s
##	24000K	 			66% 7	4.3M	1s
##	24050K	 			66% 6	4.9M	1s
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##	24800K					108M	
##	24850K					112M	
##	24900K						
##	24950K						
##	25000K						
##	25050K						
##	25100K						
##	25150K						
##	25200K						
##	25250K						
##	25300K						
##	25350K						
##	25400K						
	25450K						
##							
##	25500K						
##	25550K						
##	25600K						
##	25650K						
##	25700K						
##	25750K						
##	25800K						
##	25850K	 •	• • • • • • • • • • • • • • • • • • • •	• • • • • • • • • • •	71% 7	6.1M	1s

##			• • • • • • • • • • • • • • • • • • • •				
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##							
##	26900K	 		 	74%	76.2M	1s
##	26950K	 		 	74%	13.1M	1s
##	27000K	 		 	75%	58.OM	1s
##	27050K	 		 	75%	66.5M	1s
##	27100K	 		 	75%	55.5M	1s
##	27150K	 		 	75%	71.9M	1s
##	27200K	 		 	75%	87.2M	1s
##	27250K	 		 	75%	85.7M	1s
##	27300K	 		 	75%	68.9M	1s
##	27350K	 		 	76%	89.7M	1s
##	27400K	 		 	76%	83.7M	1s
##	27450K	 		 	76%	69.1M	1s
##	27500K	 		 	76%	67.3M	1s
##	27550K	 		 	76%	76.9M	1s
##	27600K	 		 	76%	86.1M	1s
##	27650K	 		 	76%	62.5M	1s
##	27700K	 		 	76%	36.2M	1s
##	27750K	 		 	77%	69.3M	1s
##	27800K	 		 	77%	77.7M	1s
##	27850K	 		 	77%	93.6M	1s
##	27900K	 		 	77%	14.7M	1s
##	27950K	 		 	77%	80.8M	1s
##							
##	28050K	 		 	77%	76.3M	1s
##	28100K	 		 	78%	65.6M	1s
##	28150K	 		 	78%	79.1M	1s
##							
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##	28600K							
##	28650K							
##	28700K							
##	28750K							
##	28800K							
##	28850K							
##	28900K							
##	28950K					80%	80.5M	0s
##	29000K							
##	29050K							
##	29100K					80%	75.5M	0s
##	29150K					81%	92.5M	0s
##	29200K					81%	76.8M	0s
##	29250K					81%	86.7M	0s
##	29300K					81%	97.OM	0s
##	29350K					81%	20.1M	0s
##	29400K					81%	91.8M	0s
##	29450K					81%	46.4M	0s
##	29500K					81%	26.7M	0s
##	29550K					82%	78.4M	0s
##	29600K					82%	83.1M	0s
##	29650K					82%	98.2M	0s
##	29700K					82%	41.8M	0s
##	29750K					82%	78.9M	0s
##	29800K					82%	78.3M	0s
##	29850K					82%	91.7M	0s
##	29900K							
##	29950K							
##	30000K					83%	76.3M	0s
##	30050K							
##	30100K							
##	30150K					83%	69.7M	0s
##	30200K							
##	30250K							
##	30300K							
##	30350K							
##	30400K							0s
##	30450K							
##	30500K							
##	30550K							
##	30600K							
##	30650K							
##	30700K							
##	30750K							
##	30800K							
##	30850K							
##	30900K							
##	30950K							
##	31000K							
##	31050K						101M	
##	31100K							
##	31150K							
##	31200K							
##	31250K							
##	01700V	• • • • • • • • • •	• • • • • • • • • • •	• • • • • • • • • •	• • • • • • • • • •	00%	الاق. ∠ن	OB.

##	31300K					
##	31350K					
##	31400K					
##	31450K	 	 	87% 6	39.OM	0s
##	31500K	 	 	87% 6	31.3M	0s
##	31550K	 	 	87% 5	58.9M	0s
##	31600K	 	 	87%	76.5M	0s
##	31650K	 	 	87% 6	38.5M	0s
##	31700K	 	 	88% 8	31.3M	0s
##	31750K	 	 	88% 3	34.1M	0s
##	31800K	 	 	88% 7	77.OM	0s
##	31850K	 	 	88% 3	32.OM	0s
##	31900K	 	 	88% 6	34.2M	0s
##	31950K	 	 	88% 9	90.5M	0s
##	32000K					
##	32050K					
##	32100K					
##	32150K					
##	32200K					
##	32250K					
##	32300K					
##	32350K					
##	32400K					
##	32450K					
##	32500K					
##	32550K					
##	32600K					
##	32650K					
##	32700K					
##	32750K					
##	32800K					
##	32850K					
##	32900K					
##	32950K					
##	33000K					
##	33050K	 	 	91%	72.1M	0s
##	33100K	 	 	91%	71.8M	0s
##	33150K	 	 	92% 5	58.5M	0s
##	33200K	 	 	92%	113M	0s
##	33250K	 	 	92% 7	77.2M	0s
##	33300K	 	 	92% 8	39.2M	0s
##	33350K	 	 	92% 8	36.4M	0s
##	33400K	 	 	92% 9	97.9M	0s
##	33450K	 	 	92% 7	77.7M	0s
##	33500K	 	 	93% 6	31.4M	0s
##	33550K	 	 	93%	72.OM	0s
##	33600K					
##	33650K					
##	33700K					
##	33750K					
##	33800K					
##	33850K					
##	33900K					
##	33950K					
11		 	 	O ±/0		OD.

```
34000K ...... 94% 85.1M Os
 34050K ...... 94% 90.6M Os
##
##
 34100K ...... 94% 83.8M Os
 34150K ...... 94%
##
##
 34200K ...... 95% 81.7M Os
 34250K ...... 95%
##
                       129M Os
 34300K ...... 95% 1.18M Os
##
 34350K ...... 95% 37.3M Os
##
##
 34450K ...... 95% 57.5M Os
##
 34500K ...... 95% 63.5M Os
 ##
##
 34600K ...... 96% 87.5M Os
 ##
##
 34700K ...... 96% 62.5M Os
##
 34750K ...... 96% 88.3M Os
 34800K ...... 96% 93.9M Os
##
##
 34850K ...... 96%
 34900K ...... 96% 80.8M Os
##
##
 34950K ..... 97% 23.7M Os
##
 35000K ...... 97% 63.4M Os
 35050K ...... 97% 85.1M Os
##
 35100K ...... 97% 69.4M Os
##
 35150K ...... 97% 78.6M Os
##
 35200K ...... 97% 71.1M Os
##
##
 ##
 35300K ...... 98% 68.6M Os
 35350K ...... 98% 85.1M Os
##
 35400K ...... 98% 65.5M Os
##
##
 ##
 35500K ...... 98% 81.1M Os
##
 ##
 35600K ...... 98% 44.5M Os
 ##
##
 35700K ...... 99% 67.2M Os
 35750K ...... 99% 84.7M Os
##
##
 35800K ...... 99% 27.0M Os
##
 35850K ...... 99% 87.3M Os
 35900K ...... 99% 67.4M Os
##
##
 35950K ...... 99% 17.6M Os
 ##
                      100% 99.3M=2.1s
##
## 2020-11-24 21:10:22 (16.7 MB/s) - 'miseqsopdata.zip.2' saved [36910055/36910055]
```

#decompress the data

## unzip miseqsopdata.zip

```
## Archive: miseqsopdata.zip
## creating: MiSeq_SOP/
## inflating: MiSeq_SOP/F3DO_S188_L001_R1_001.fastq
## creating: __MACOSX/
## creating: __MACOSX/MiSeq_SOP/
```

```
inflating: MACOSX/MiSeq SOP/. F3D0 S188 L001 R1 001.fastq
##
##
     inflating: MiSeq_SOP/F3D0_S188_L001_R2_001.fastq
     inflating: MACOSX/MiSeq SOP/. F3D0 S188 L001 R2 001.fastq
##
##
     inflating: MiSeq_SOP/F3D141_S207_L001_R1_001.fastq
     inflating: __MACOSX/MiSeq_SOP/._F3D141_S207_L001_R1_001.fastq
##
##
     inflating: MiSeq SOP/F3D141 S207 L001 R2 001.fastq
##
     inflating: MACOSX/MiSeq SOP/. F3D141 S207 L001 R2 001.fastq
     inflating: MiSeq SOP/F3D142 S208 L001 R1 001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D142_S208_L001_R1_001.fastq
##
     inflating: MiSeq_SOP/F3D142_S208_L001_R2_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D142_S208_L001_R2_001.fastq
     inflating: MiSeq_SOP/F3D143_S209_L001_R1_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D143_S209_L001_R1_001.fastq
##
##
     inflating: MiSeq_SOP/F3D143_S209_L001_R2_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D143_S209_L001_R2_001.fastq
     inflating: MiSeq_SOP/F3D144_S210_L001_R1_001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D144_S210_L001_R1_001.fastq
     inflating: MiSeg SOP/F3D144 S210 L001 R2 001.fastg
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D144_S210_L001_R2_001.fastq
     inflating: MiSeq SOP/F3D145 S211 L001 R1 001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D145_S211_L001_R1_001.fastq
##
     inflating: MiSeq SOP/F3D145 S211 L001 R2 001.fastq
     inflating: __MACOSX/MiSeq_SOP/._F3D145_S211_L001_R2_001.fastq
##
##
     inflating: MiSeq SOP/F3D146 S212 L001 R1 001.fastq
     inflating: __MACOSX/MiSeq_SOP/._F3D146_S212_L001_R1_001.fastq
##
##
     inflating: MiSeq SOP/F3D146 S212 L001 R2 001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D146_S212_L001_R2_001.fastq
##
     inflating: MiSeq_SOP/F3D147_S213_L001_R1_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D147_S213_L001_R1_001.fastq
     inflating: MiSeq_SOP/F3D147_S213_L001_R2_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D147_S213_L001_R2_001.fastq
##
##
     inflating: MiSeq_SOP/F3D148_S214_L001_R1_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D148_S214_L001_R1_001.fastq
##
     inflating: MiSeq_SOP/F3D148_S214_L001_R2_001.fastq
     inflating: __MACOSX/MiSeq_SOP/._F3D148_S214_L001_R2_001.fastq
##
##
     inflating: MiSeq_SOP/F3D149_S215_L001_R1_001.fastq
##
     inflating: MACOSX/MiSeq SOP/. F3D149 S215 L001 R1 001.fastq
##
     inflating: MiSeq_SOP/F3D149_S215_L001_R2_001.fastq
     inflating: __MACOSX/MiSeq_SOP/._F3D149_S215_L001_R2_001.fastq
##
##
     inflating: MiSeq_SOP/F3D150_S216_L001_R1_001.fastq
##
     inflating: MACOSX/MiSeq SOP/. F3D150 S216 L001 R1 001.fastq
     inflating: MiSeq SOP/F3D150 S216 L001 R2 001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D150_S216_L001_R2_001.fastq
##
     inflating: MiSeq_SOP/F3D1_S189_L001_R1_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D1_S189_L001_R1_001.fastq
##
     inflating: MiSeq_SOP/F3D1_S189_L001_R2_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D1_S189_L001_R2_001.fastq
##
     inflating: MiSeq_SOP/F3D2_S190_L001_R1_001.fastq
     inflating: __MACOSX/MiSeq_SOP/._F3D2_S190_L001_R1_001.fastq
##
##
     inflating: MiSeq_SOP/F3D2_S190_L001_R2_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D2_S190_L001_R2_001.fastq
##
     inflating: MiSeq_SOP/F3D3_S191_L001_R1_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D3_S191_L001_R1_001.fastq
     inflating: MiSeq SOP/F3D3 S191 L001 R2 001.fastq
##
```

```
inflating: MACOSX/MiSeq SOP/. F3D3 S191 L001 R2 001.fastq
##
##
     inflating: MiSeq SOP/F3D5 S193 L001 R1 001.fastq
     inflating: MACOSX/MiSeq SOP/. F3D5 S193 L001 R1 001.fastq
##
##
     inflating: MiSeq_SOP/F3D5_S193_L001_R2_001.fastq
     inflating: __MACOSX/MiSeq_SOP/._F3D5_S193_L001_R2_001.fastq
##
##
     inflating: MiSeq SOP/F3D6 S194 L001 R1 001.fastq
##
     inflating: MACOSX/MiSea SOP/. F3D6 S194 L001 R1 001.fasta
     inflating: MiSeq SOP/F3D6 S194 L001 R2 001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D6_S194_L001_R2_001.fastq
##
     inflating: MiSeq_SOP/F3D7_S195_L001_R1_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D7_S195_L001_R1_001.fastq
     inflating: MiSeq_SOP/F3D7_S195_L001_R2_001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._F3D7_S195_L001_R2_001.fastq
##
     inflating: MiSeq_SOP/F3D8_S196_L001_R1_001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D8_S196_L001_R1_001.fastq
     inflating: MiSeq_SOP/F3D8_S196_L001_R2_001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D8_S196_L001_R2_001.fastq
     inflating: MiSeq SOP/F3D9 S197 L001 R1 001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D9_S197_L001_R1_001.fastq
     inflating: MiSeq SOP/F3D9 S197 L001 R2 001.fastq
##
##
     inflating: __MACOSX/MiSeq_SOP/._F3D9_S197_L001_R2_001.fastq
##
     inflating: MiSeq SOP/HMP MOCK.v35.fasta
     inflating: __MACOSX/MiSeq_SOP/._HMP_MOCK.v35.fasta
##
##
     inflating: MiSeq SOP/Mock S280 L001 R1 001.fastq
     inflating: __MACOSX/MiSeq_SOP/._Mock_S280_L001_R1_001.fastq
##
##
     inflating: MiSeq SOP/Mock S280 L001 R2 001.fastq
##
     inflating: __MACOSX/MiSeq_SOP/._Mock_S280_L001_R2_001.fastq
     inflating: MiSeq_SOP/mouse.dpw.metadata
##
##
     inflating: __MACOSX/MiSeq_SOP/._mouse.dpw.metadata
     inflating: MiSeq_SOP/mouse.time.design
##
     inflating: __MACOSX/MiSeq_SOP/._mouse.time.design
##
##
     inflating: MiSeq_SOP/stability.batch
##
     inflating: __MACOSX/MiSeq_SOP/._stability.batch
##
     inflating: MiSeq_SOP/stability.files
     inflating: __MACOSX/MiSeq_SOP/._stability.files
##
```