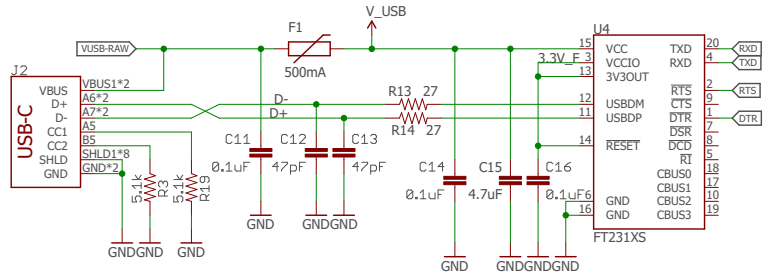
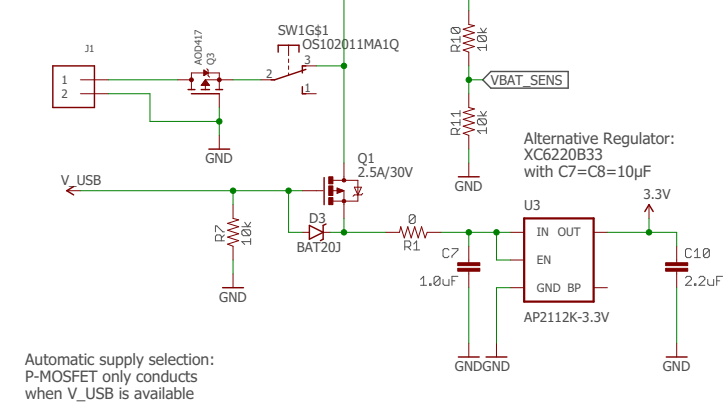


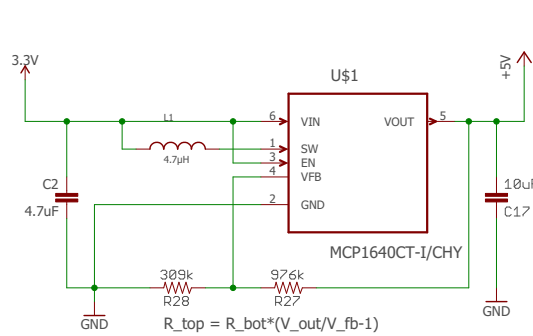
FTDI Adapter



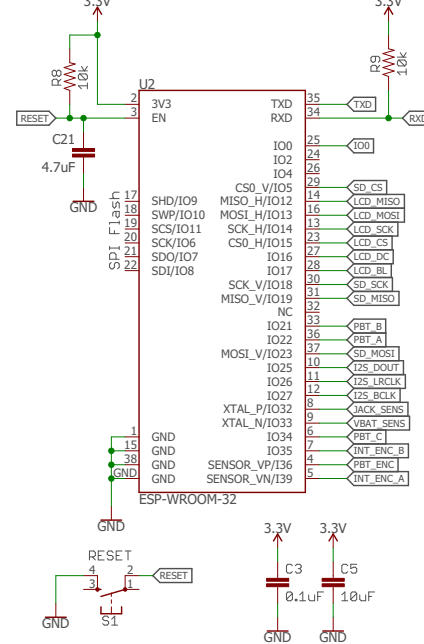
Power Supply



LCD Charge Pump

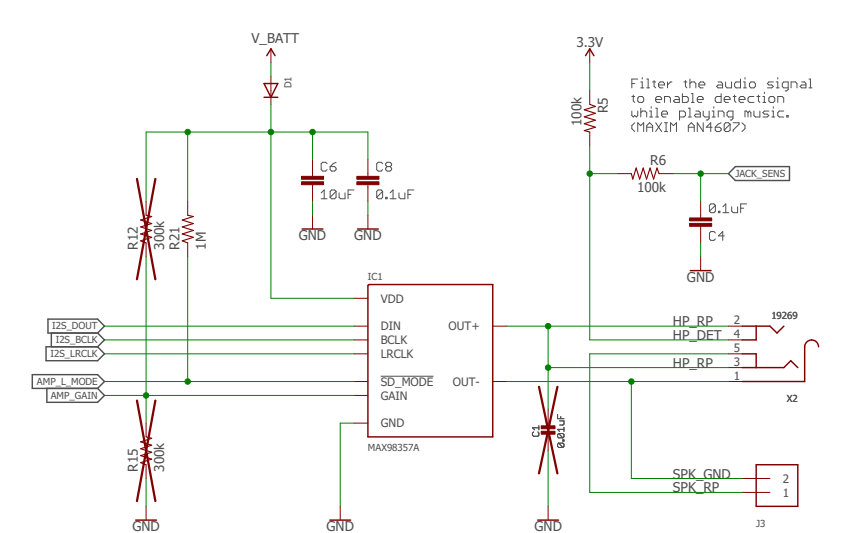


ESP32 WROOM

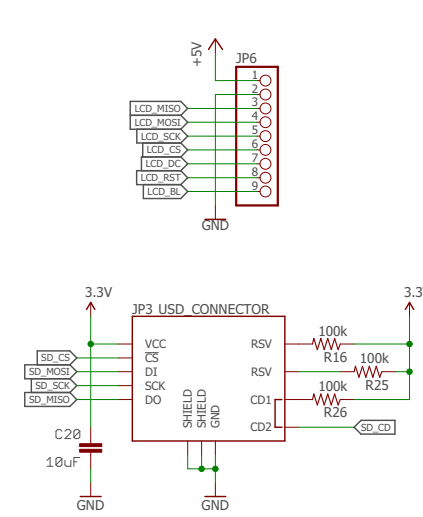


RTC GPIOs: 0,2,4,12-15,25-27,32-39
usable by ULP, internal Pullups /Pulldowns
ex10 for one pin as wakeup source
ex11 for multiple pins as wakeup source
only for GPIOs: 32-39?
no internal Pullups /Pulldowns

Audio Output Stage

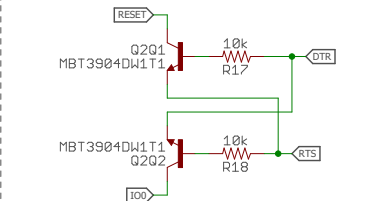


LCD / uSD Connectors



Pin	Default	Boot	Download
GPIO0	1	1	0
U0TXD	1	1	x
GPIO2	0	x	0
GPIO4	0	x	x
MTD0	1	x	x
GPIO5	1	1	x

If U0TXD, GPIO2, GPIO5 are floating,
GPIO0 determines boot mode



If DTR is LOW, toggling RTS from
HIGH to LOW resets to run mode.
If RTS is HIGH, toggling DTR from
LOW to HIGH resets to bootloader.