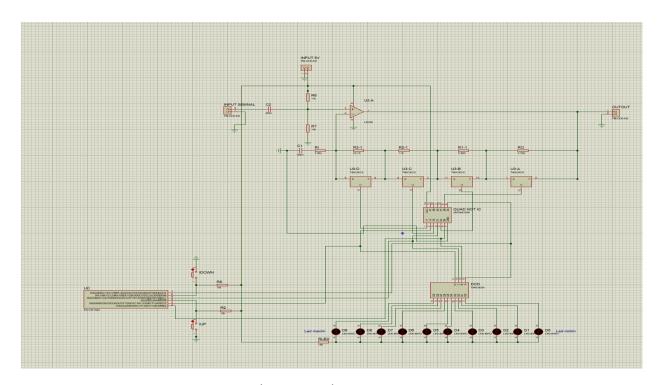
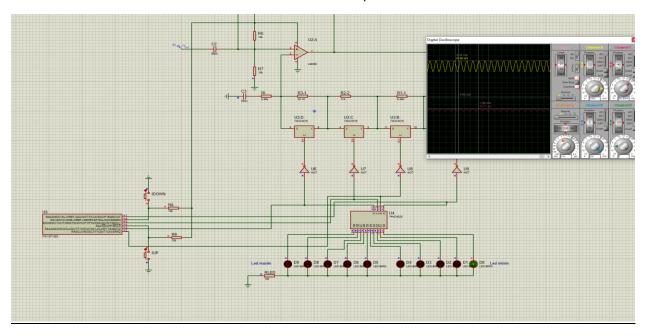
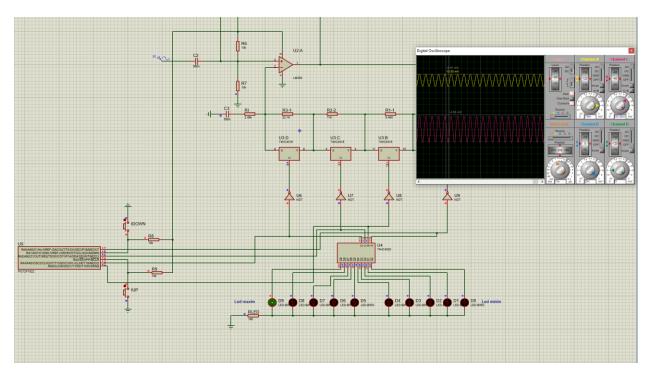
<u>Proiect pre-amplificator digital de reglare a</u> <u>intensitatii sonore in trepte</u>



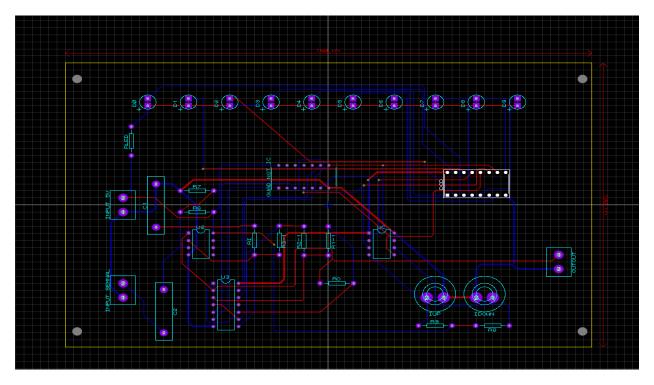
Schematic realizat in proteus



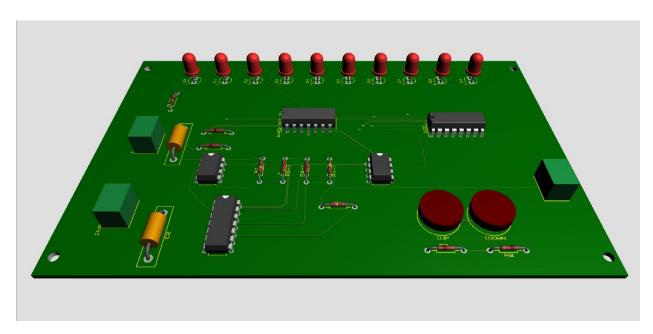
Amplificare minima(toate comutatoarele electronice au iesire 5v-1 logic)



Amplificare maxima(toate comutatoarele electronice au iesire 0v-0 logic)



PCB layout realizat in proteus



Vizualizare 3d a PCB-ului