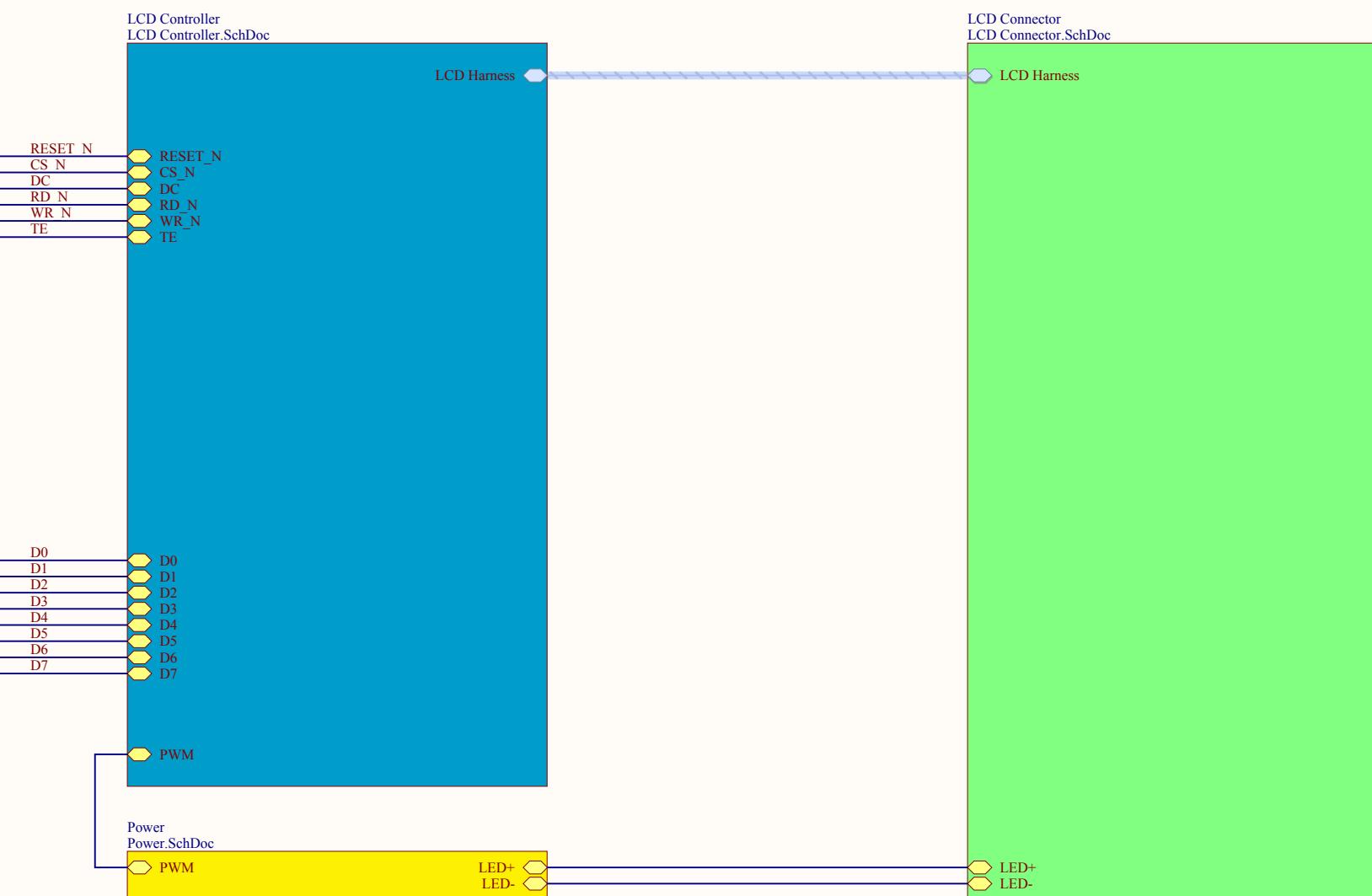
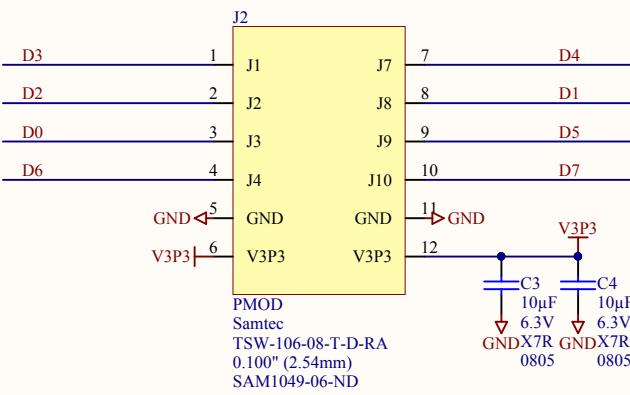
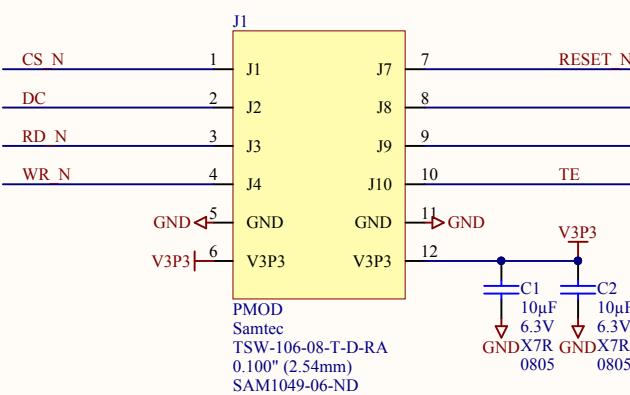


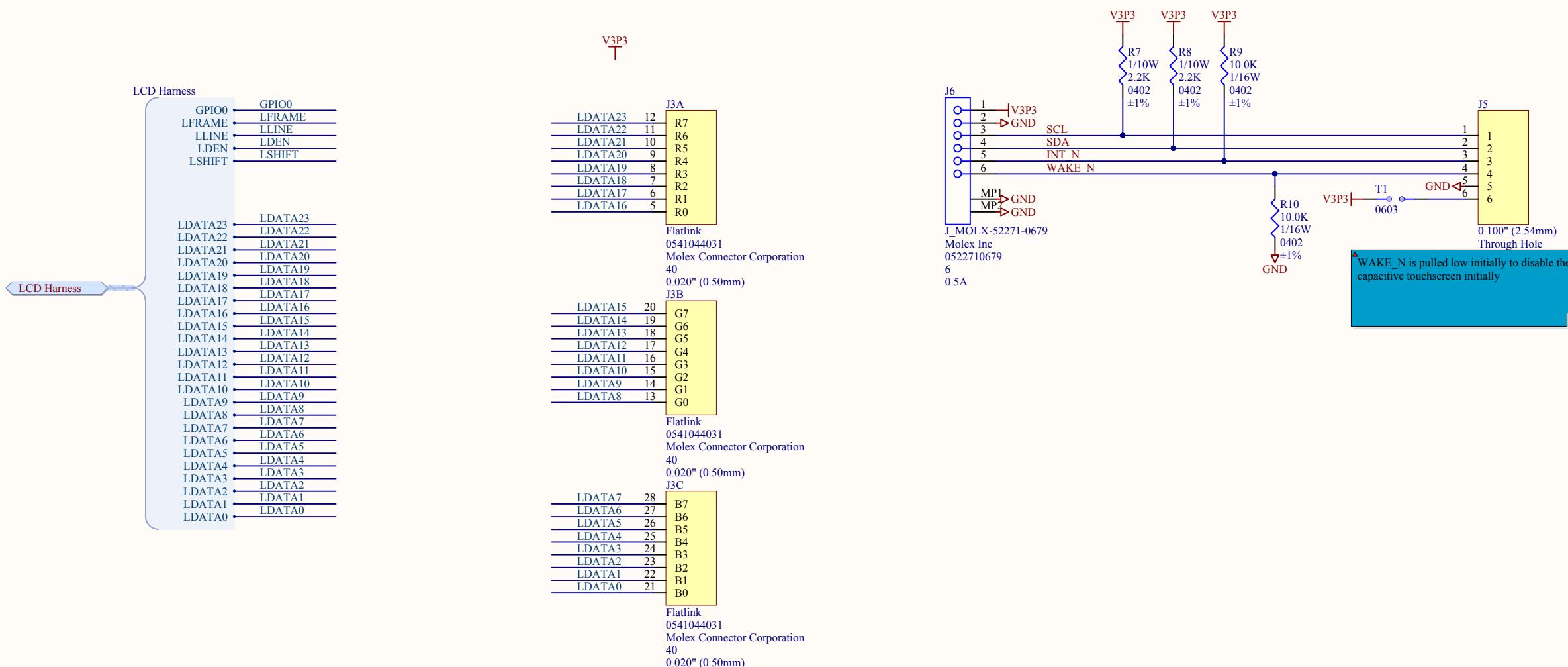
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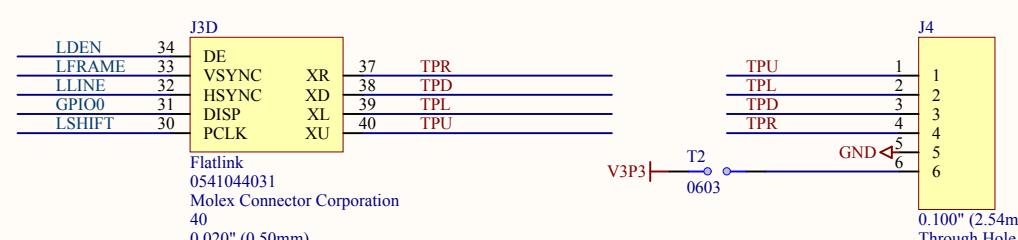
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Project:	Dionysus LCD PMOD.PrjPCB	
Document:	Block Diagram.SchDoc	
Engineer:	*	
Drawn By:	*	
Approved By:	*	Revision: *
Date:	*	Number: * of *



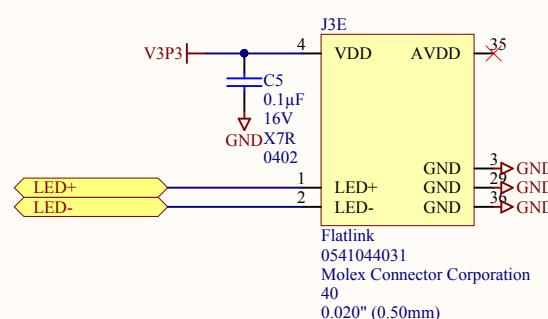
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C

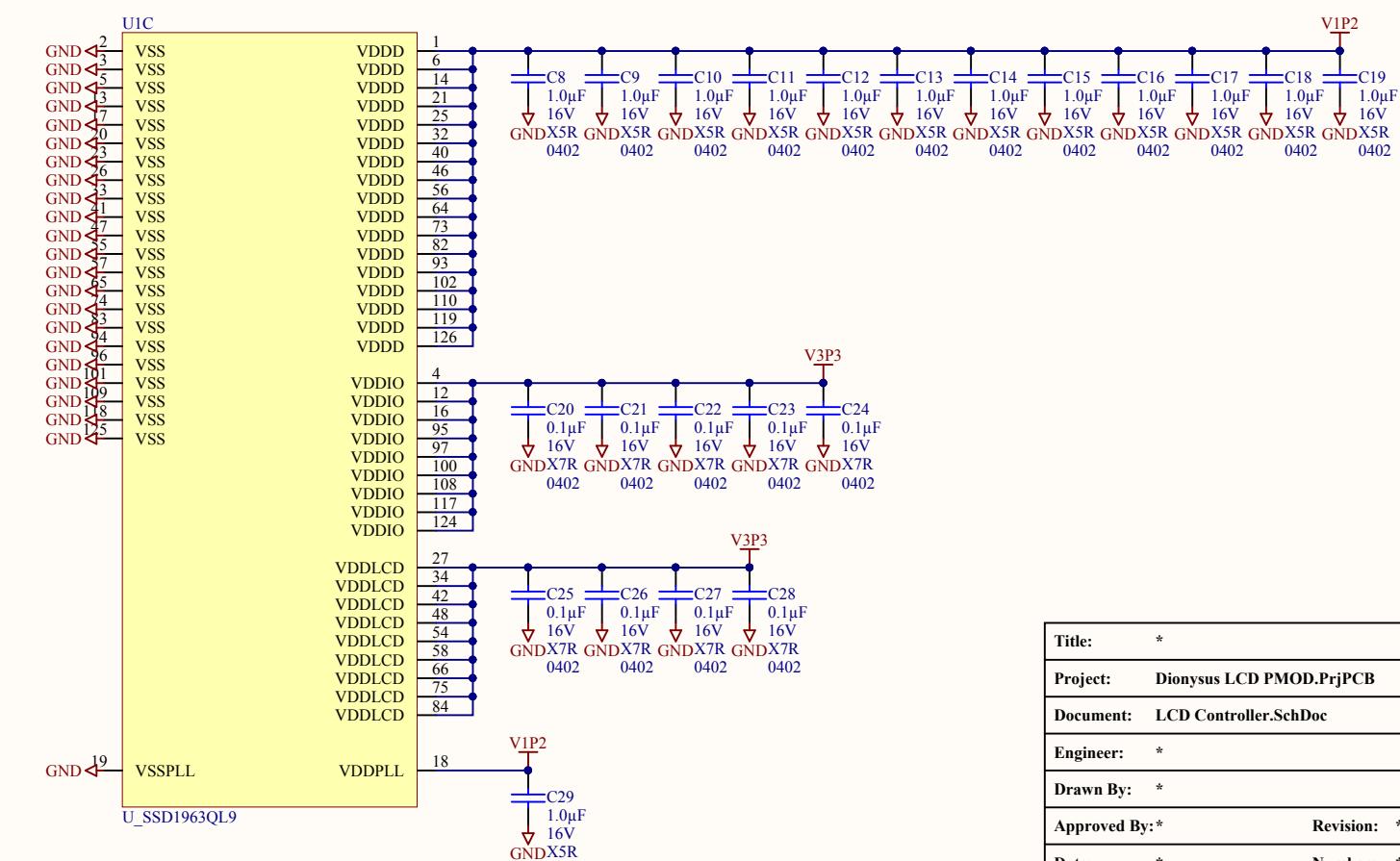
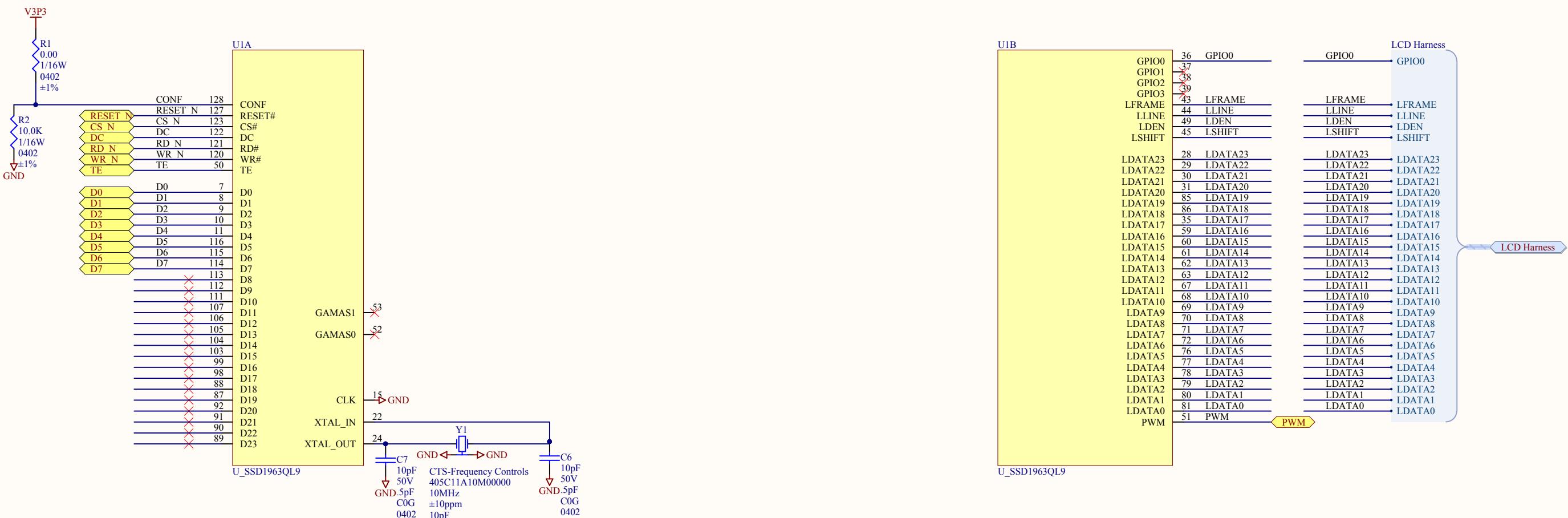


D



Title:	*	Cospan Design <a href="http://www.cospandesign.com">http://www.cospandesign.com</a>
Project:	Dionysus LCD PMOD.PrbPCB	
Document:	LCD Connector.SchDoc	
Engineer:	*	
Drawn By:	*	
Approved By:	*	Revision: *
Date:	*	Number: * of *





Title:	*	Cospan Design <a href="http://www.cospandesign.com">http://www.cospandesign.com</a>
Project:	Dionysus LCD PMOD.PrjPCB	
Document:	LCD Controller.SchDoc	
Engineer:	*	
Drawn By:	*	
Approved By:	*	Revision: *
Date:	*	Number: * of *



A

A

B

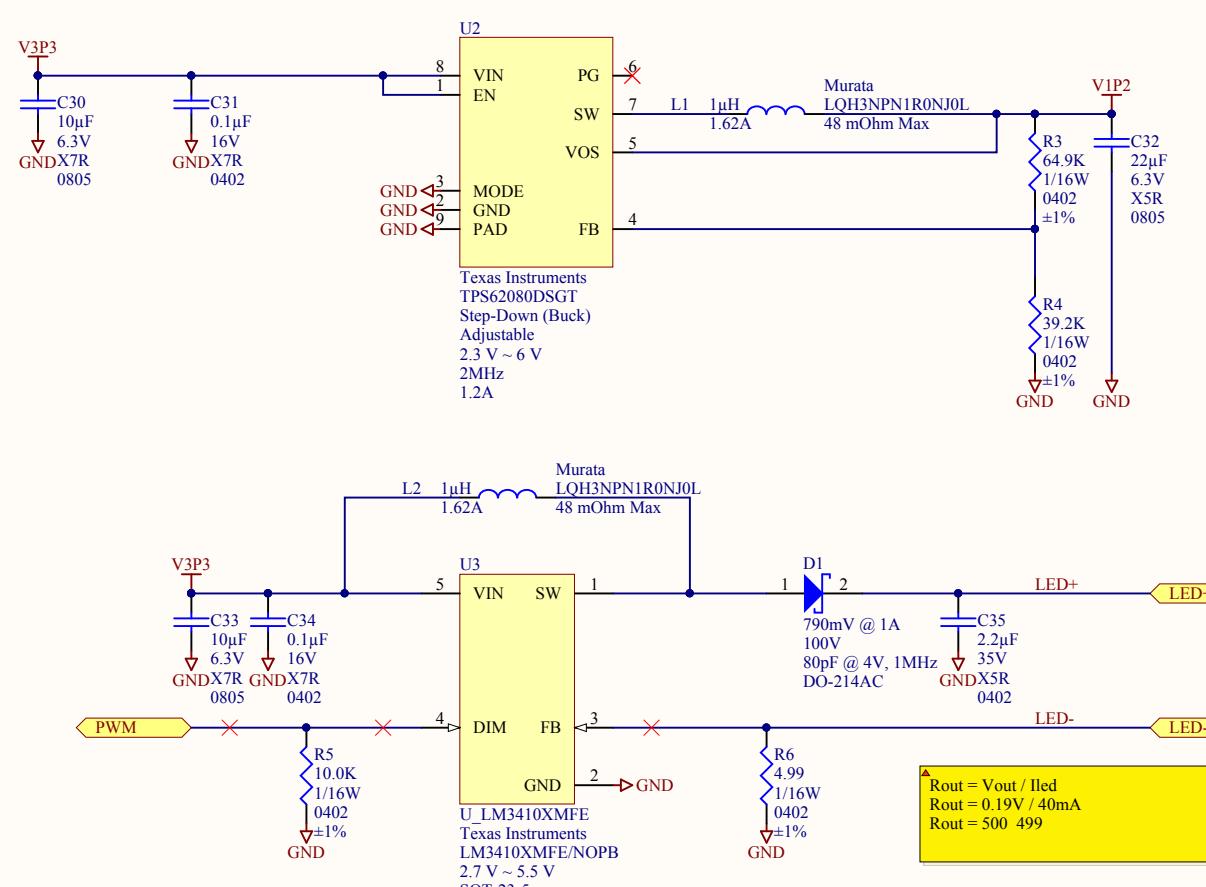
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C

C

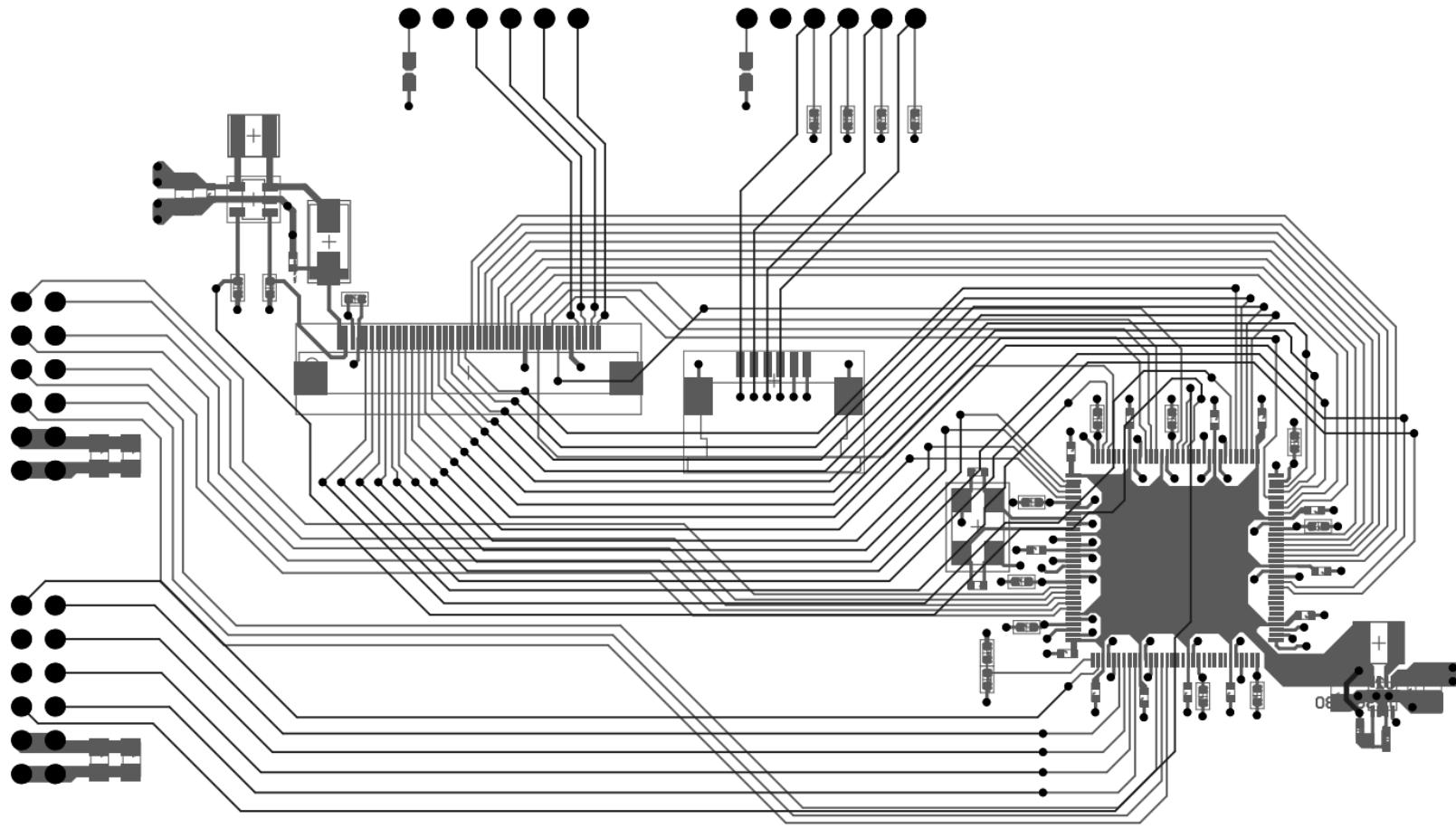
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D

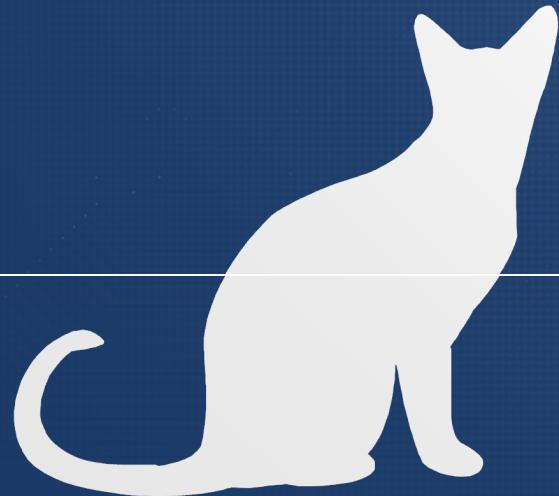
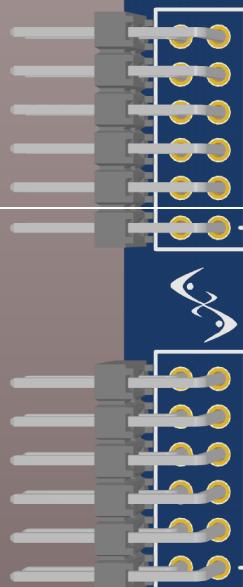


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Document: Power.SchDoc			
Engineer: *			
Drawn By: *			
Approved By: *	Revision: *		
Date: *	Number: * of *		





Cospan Design  
TFT PMOD  
Rev: A  
SN: 1



J2

J1

