



NHD-4.3-480272EF-ATXL#

TFT (Thin-Film-Transistor) Color Liquid Crystal Display Module

NHD- Newhaven Display
4.3- 4.3" Diagonal
480272- 480xRGBx272 Pixels

EF- Model

A- Built-in Driver / No Controller

T- White LED Backlight

X- TFT

L- 6:00 Optimal View, Wide Temperature

#- RoHS Compliant

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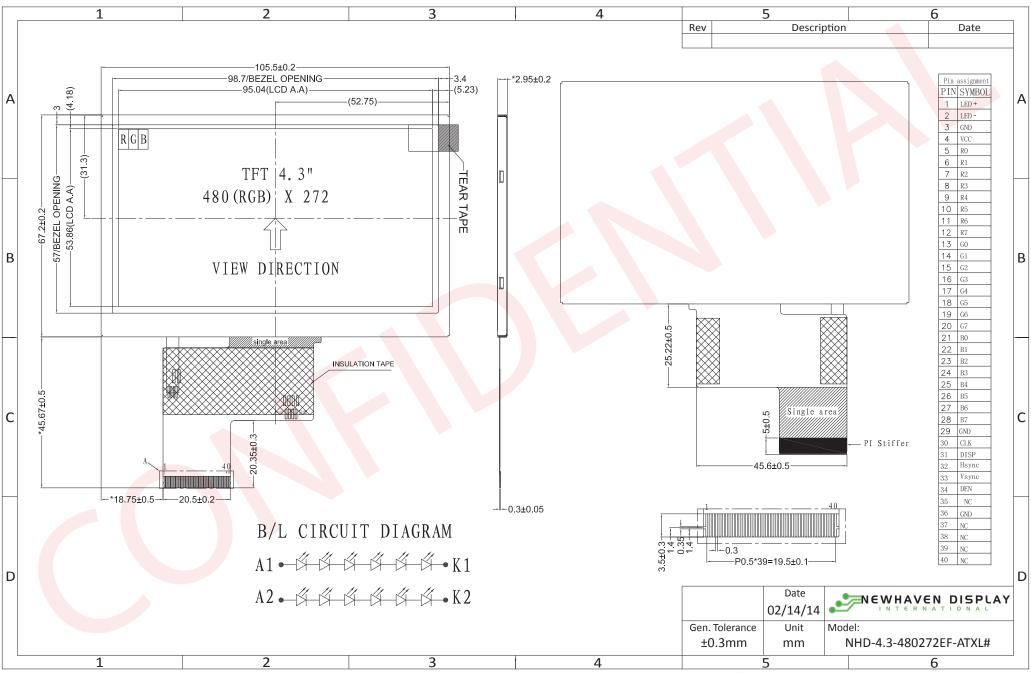
Document Revision History

Revision	Date	Description	Changed by
0	8/29/2012	Initial Release	AK
1	7/11/2013	Mechanical and Optical characteristic updated	KA
2	2/14/2014	Mechanical drawing updated	KA
3	6/24/2014	Timing characteristics updated	ML

Functions and Features

- 480xRGBx272 resolution, up to 16.7M colors
- 12-LED backlight
- 24 bit RGB interface
- Resistive and Capacitive touch panel available

Mechanical Drawing



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Pin Description

Pin No.	Symbol	External	Function Description		
		Connection			
1	LED-	Power Supply	Backlight Cathode (Ground)		
2	LED+	Power Supply	Backlight Anode (40mA @ 19.2V)		
3	GND	Power Supply	Ground		
4	VDD	Power Supply	Supply Voltage for LCD and logic (3.3V)		
5-12	[R0-R7]	MPU	Red Data signals		
13-20	[G0-G7]	MPU	Green Data signals		
21-28	[B0-B7]	MPU	Blue Data signals		
29	GND	Power Supply	Ground		
30	CLK	MPU	Data sample Clock signal		
31	DISP	MPU	Display ON/OFF signal		
32	HSYNC	MPU	Line synchronization signal		
33	VSYNC	MPU	Frame synchronization signal		
34	DE	MPU	Data Enable signal		
35	NC	-	No Connect		
36	GND	Power Supply	Ground		
37	NC	-	No Connect		
38	NC	-	No Connect		
39	NC	-	No Connect		
40	NC	-	No Connect		

Recommended LCD connector: 0.5mm pitch 40-Conductor FFC. Molex p/n: 54132-4062

Backlight connector: on LCD connector Mates with: ---

Electrical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	Тор	Absolute Max	-20	1	+70	°C
Storage Temperature Range	Tst	Absolute Max	-30	-	+80	°C
Supply Voltage	VDD		3.0	3.3	3.6	V
Supply Current (White screen)	IDD		-	24.24	28.78	mA
Supply Current (Black screen)	IDD		-	25.76	30.30	mA
"H" level input	Vih		0.8*VDD	-	VDD	V
"L" level input	Vil		GND	-	0.2*VDD	V
Backlight Supply Voltage	VLED		-	19.2	22	V
Backlight Supply Current	ILED		-	40	-	mA

Optical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Viewing Angle – Top	-		-	70	-	0
Viewing Angle – Bottom	-	Cr ≥ 10	-	50	-	0
Viewing Angle – Left	-	Cr ≥ 10	-	70	-	0
Viewing Angle – Right	-		-	70	-	0
Contrast Ratio	Cr	-	400	500	-	-
Luminance	Lv	-	320	400	-	cd/m ²
Response Time (rise)	Tr	-	-	25	30	ms
Response Time (fall)	Tf	-	-	25	30	ms

Viewing angles based on 12:00 grayscale inversion

Driver Information

Built-in Himax HX8257-A driver.

Please download specification at http://www.newhavendisplay.com/app notes/HX8257.pdf

Timing Characteristics

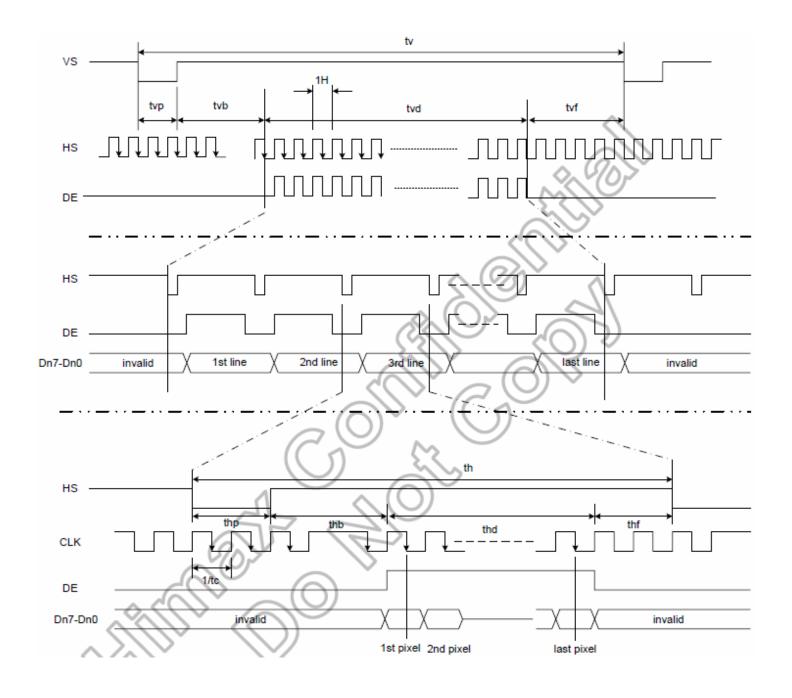
Parallel RGB input timing requirement

(480RGBx272, T_A=25°C, VDDIO=1.8V to 3.6V, DVSS= 0V)

Parameter	Symbol		Unit		
Faranietei	•	Min. Typ.		Max.	Offic
Clock cycle	f _{CLK} ⁽¹⁾	-	9	15	MHz
Hsync cycle	1/th	-	17.14	-	KHz
Vsync cycle	1/tv	-	59.94	-	Hz
Horizontal Signal					
Horizontal cycle	th	525	525	605	CLK
Horizontal display period	thd	480	480	480	CLK
Horizontal front porch	thf	2	2	82	CLK
Horizontal pulse width	thp ⁽²⁾	2	41	41	CLK
Horizontal back porch	thb ⁽²⁾	2	2	41	CLK
Vertical Signal					
Vertical cycle	tv	285	286	399	H ⁽¹⁾
Vertical display period	tvd	272	272	272	H ⁽¹⁾
Vertical front porch	tvf	1	2	227	H ⁽¹⁾
Vertical pulse width	tvp ⁽²⁾	1	10	11	H ⁽¹⁾
Vertical back porch	tvb ⁽²⁾	1	2	11	H ⁽¹⁾

Note: (1) Unit: CLK=1/ f_{CLK}, H= th,

⁽²⁾ It is necessary to keep tvp+tvb=12 and thp+thb=43 in sync mode. DE mode is unnecessary to keep it.

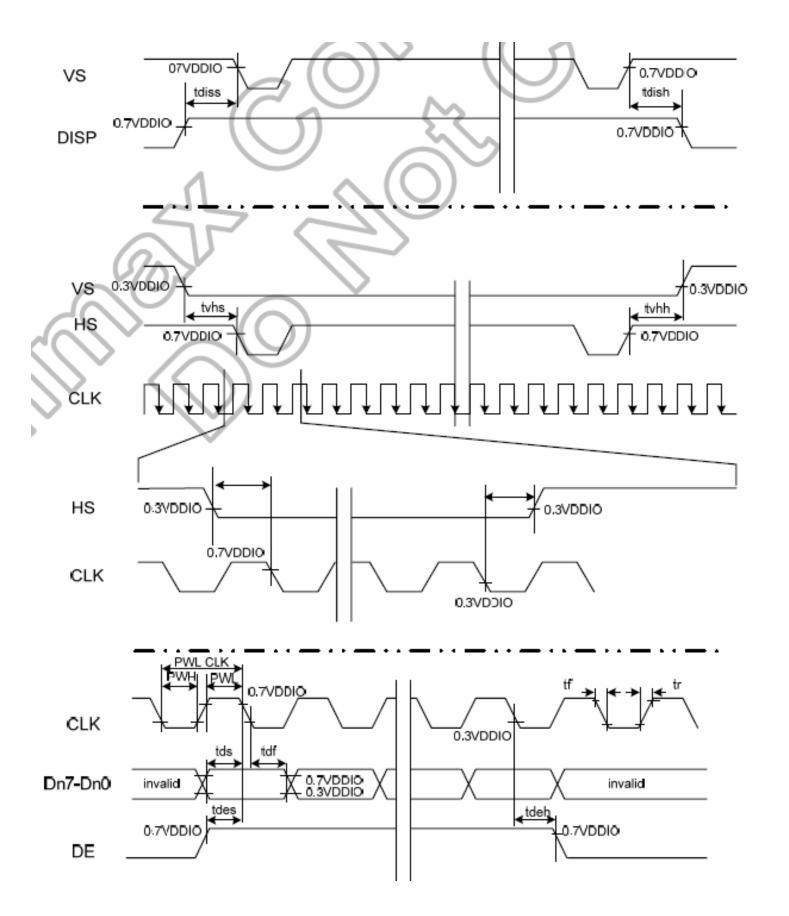


Input setup timing requirement

 $(T_A=25^{\circ}C, VDDIO=1.8V to 3.6V, DVSS=0V, tr^{(1)}=tf^{(1)}=2ns)$

Parameter	Symbol		Unit		
Farameter	Syllibol	Min.	Тур.	Max.	Ullit
DISP setup time	t _{diss}	10	-	-	ns
DISP hold time	t _{dish}	10	-	-	ns
Clock period	PW _{CLK} ⁽²⁾	66.7	-	-	ns
Clock pulse high period	PWH ⁽²⁾	26.7	-	O₂ (ns
Clock pulse low period	PWL ⁽²⁾	26.7	-	\-\ <u>-</u> \	ns
Hsync setup time	t _{hs}	10	-	2-()	ns
Hsync hold time	t _{hh}	10	-	<u> </u>	ns
Data setup time	t _{ds}	10	- <	(-)	ns
Data hold time	t _{dh}	10	-	<u> </u>	ns
DE setup time	t _{des}	10	√) - /	ns
DE hold time	t _{deh}	10		- ~	ns
Vsync setup time	t _{vhs}	10			ns
Vsync hold time	t _{vhh}	10	\bigcirc		ns

Note: (1) tr, tf is defined 10% to 90% of signal amplitude.
(2) For parallel interface, maximum clock frequency is 15MHz.



Quality Information

Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high	+80°C , 96hrs	2
	storage temperature for a long time.		
Low Temperature storage	Endurance test applying the low storage	-30°C , 96hrs	1,2
	temperature for a long time.		
High Temperature	Endurance test applying the electric stress	+70°C, 96hrs	2
Operation	(voltage & current) and the high thermal		
	stress for a long time.		
Low Temperature	Endurance test applying the electric stress	-20°C , 96hrs	1,2
Operation	(voltage & current) and the low thermal		
	stress for a long time.		
High Temperature /	Endurance test applying the electric stress	+60°C, 90% RH, 96hrs	1,2
Humidity Operation	(voltage & current) and the high thermal		
	with high humidity stress for a long time.		
Thermal Shock resistance	Endurance test applying the electric stress	-20°C,30min -> 25°C,5min -	
	(voltage & current) during a cycle of low	>70°C,30min = 1 cycle	
	and high thermal stress.	10 cycles	
Vibration test	Endurance test applying vibration to	10-55Hz , 15mm amplitude.	3
	simulate transportation and use.	60 sec in each of 3 directions	
		X,Y,Z	
		For 15 minutes	
Static electricity test	Endurance test applying electric static	VS=800V, RS=1.5kΩ, CS=100pF	
	discharge.	One time	

Note 1: No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

Note 3: Test performed on product itself, not inside a container.

Precautions for using LCDs/LCMs

See Precautions at www.newhavendisplay.com/specs/precautions.pdf

Warranty Information

See Terms & Conditions at http://www.newhavendisplay.com/index.php?main_page=terms