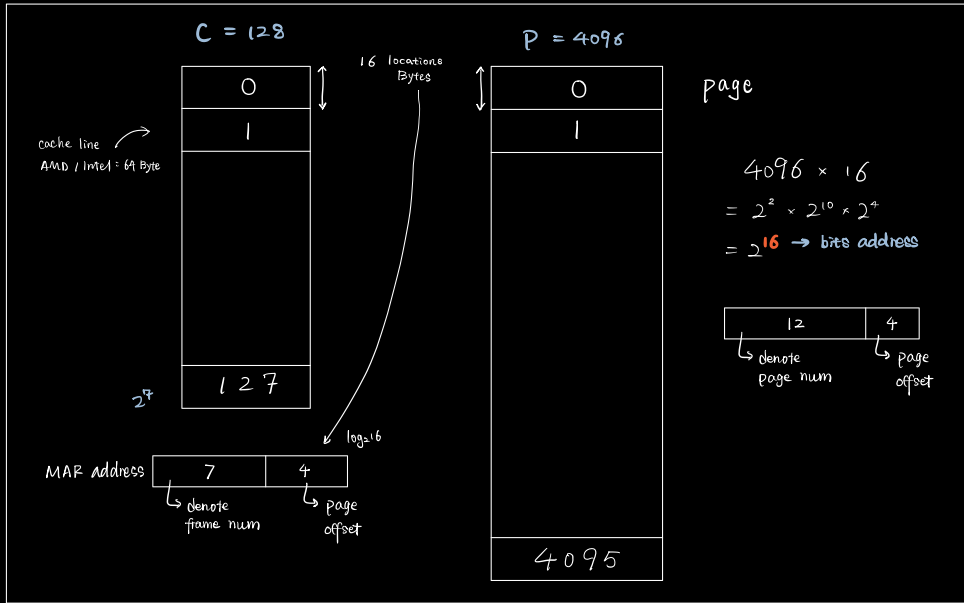


Memory Mapping dump the memory replica into cache



Direct Mapping

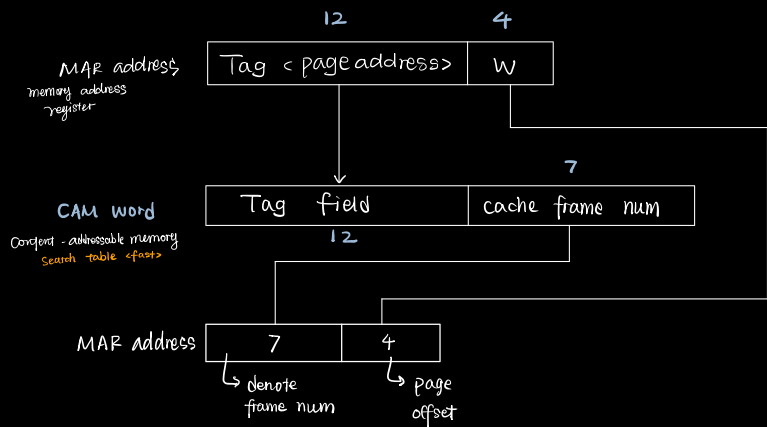
no page replacement algorithm
<full seated>



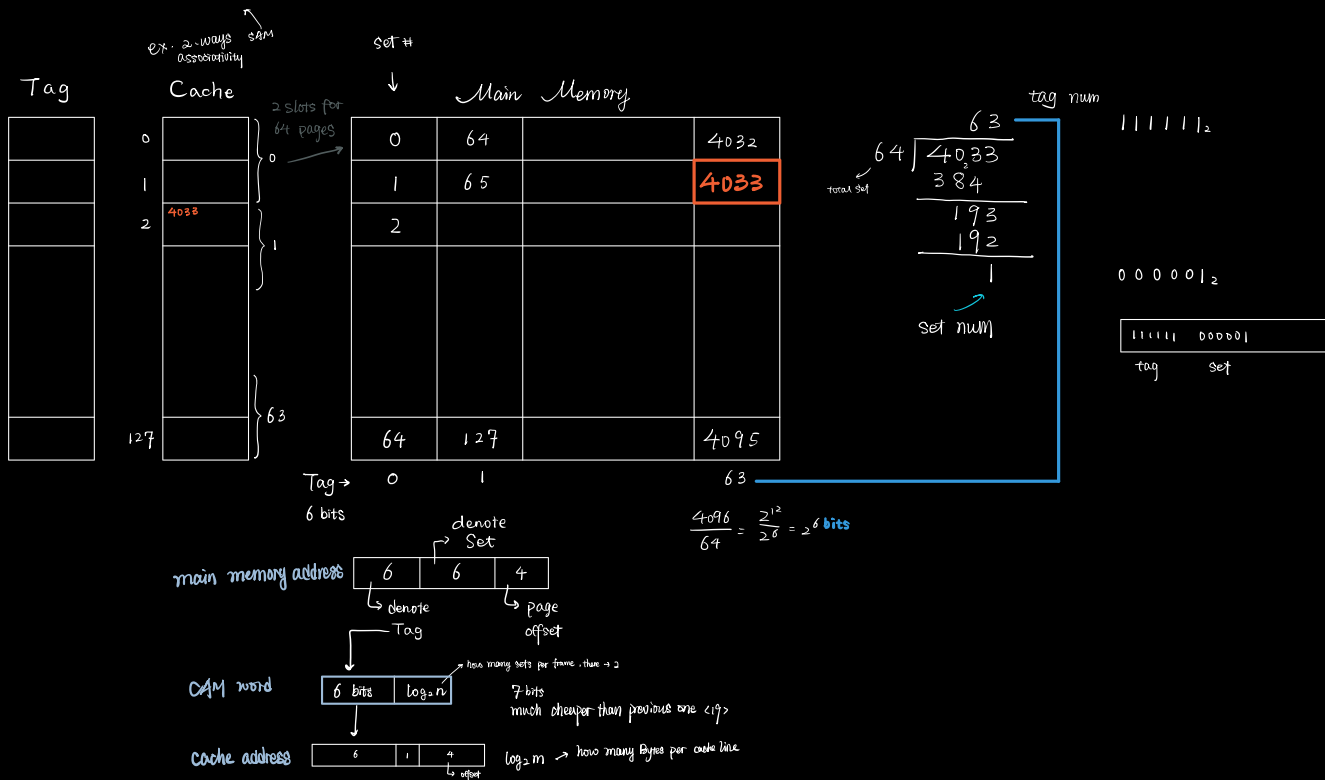
$$\frac{4096}{128} = \frac{2^{12}}{2^7} = 2^5 = 32$$

Associative Mapping <fully>

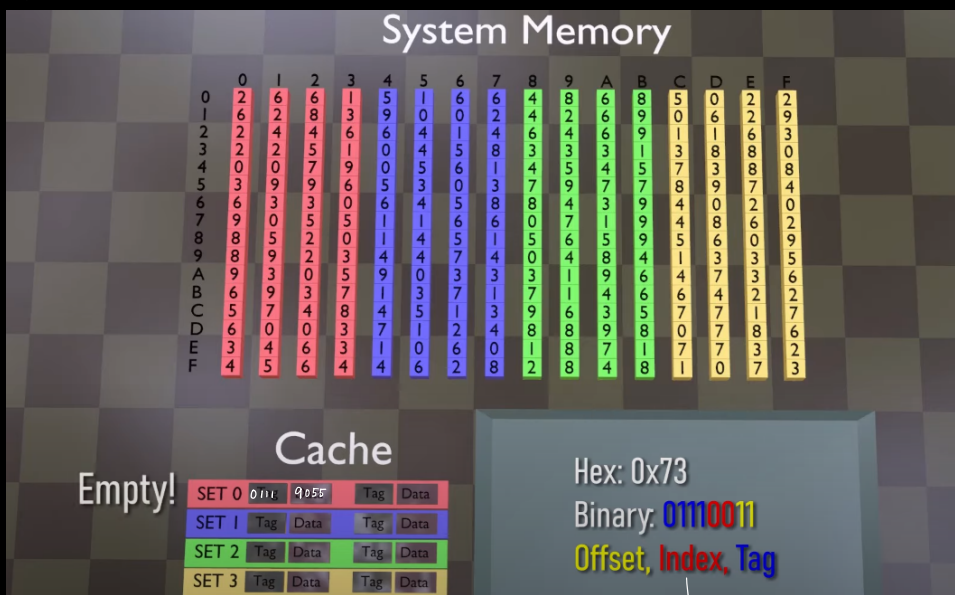
↓ speed & space overhead
 ↑ flexibility - any page can be loaded into any frame



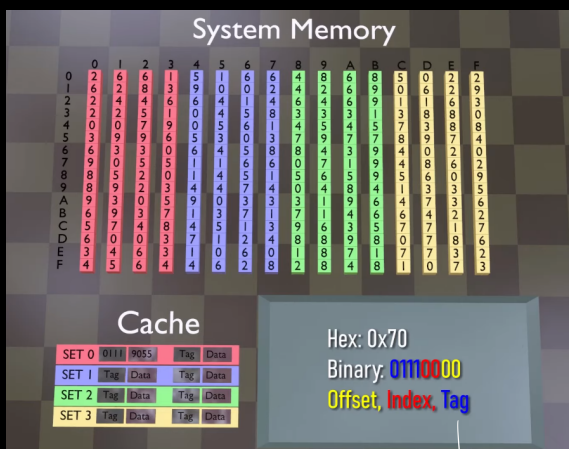
• Set Associative Mapping



cache miss

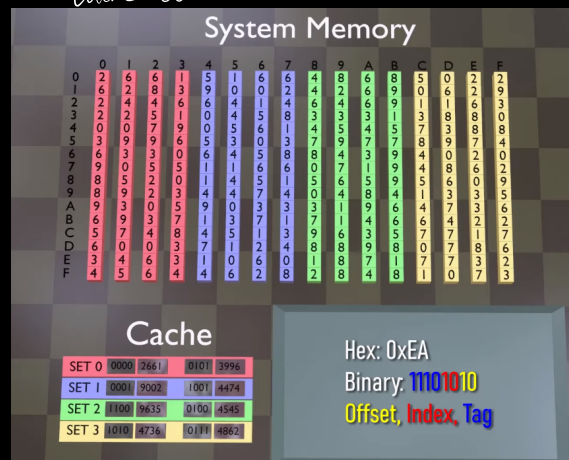


cache hit



which set

cache evict



eviction policy

LRU: least recently used

