

A
B
C
D

A
B
C
D

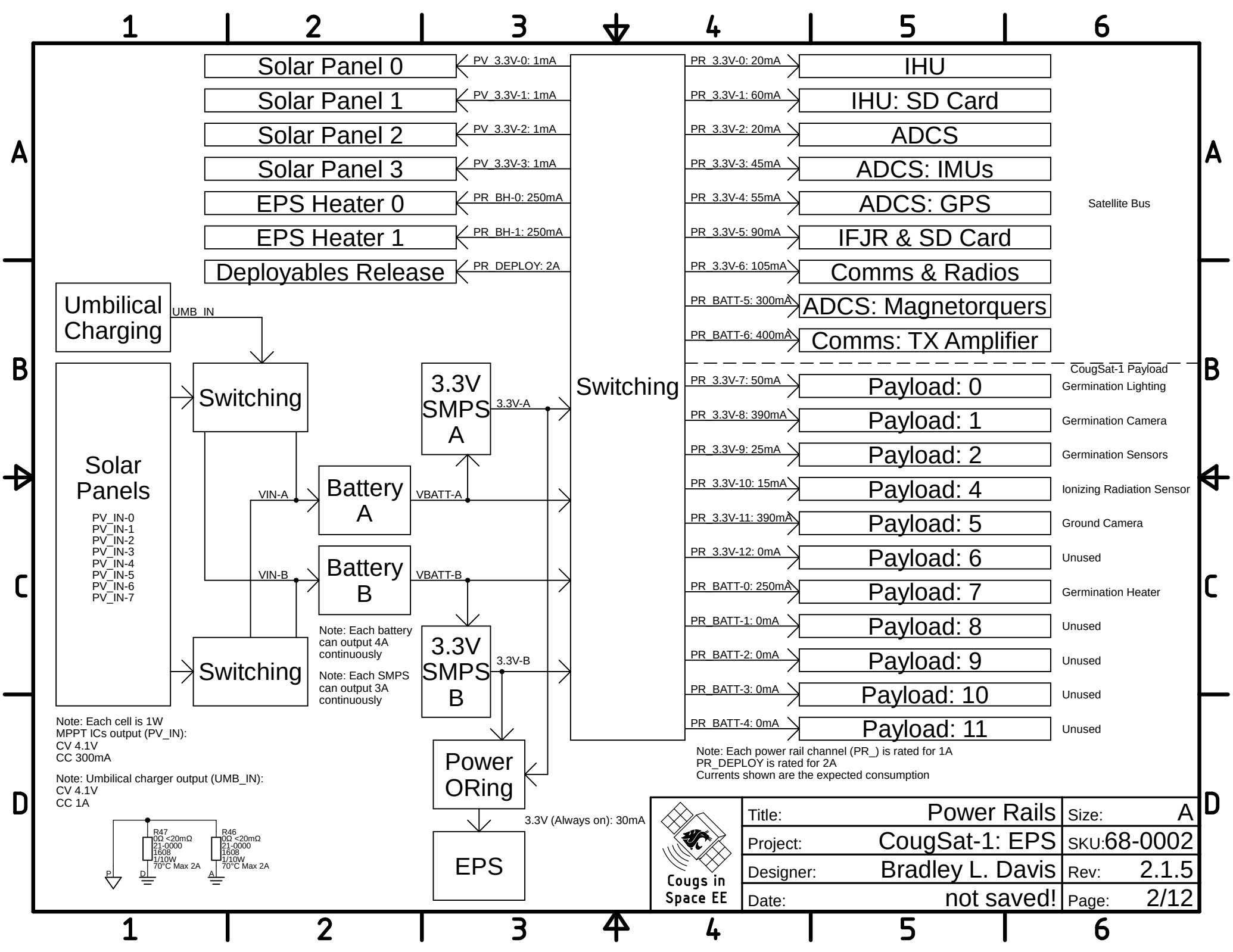
Page 1: Block Diagram
Page 2: Power Rails
Page 3: Input Switching Matrix
Page 4: Batteries
Page 5: 3.3V Regulation
Page 6: Output Switching Matrix
Page 7: Output Switching Matrix
Page 8: Switching Default States
Page 9: PMIC

Page 10: Backplane Digital & ADCs
Page 11: ADCs
Page 12: Mechanical Layout



Title:	Block Diagram
Project:	CougSat-1: EPS
Designer:	Bradley L. Davis
Date:	not saved!

Size:	A
SKU:	68-0002
Rev:	2.1.5
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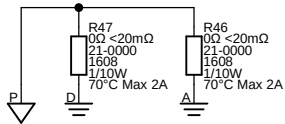
Umbilical
Charging

Solar
Panels

PV_IN-0
PV_IN-1
PV_IN-2
PV_IN-3
PV_IN-4
PV_IN-5
PV_IN-6
PV_IN-7

Note: Each cell is 1W
MPPT ICs output (PV_IN):
CV 4.1V
CC 300mA

Note: Umbilical charger output (UMB_IN):
CV 4.1V
CC 1A



Solar Panel 0 < PV 3.3V-0: 1mA
Solar Panel 1 < PV 3.3V-1: 1mA
Solar Panel 2 < PV 3.3V-2: 1mA
Solar Panel 3 < PV 3.3V-3: 1mA
EPS Heater 0 < PR BH-0: 250mA
EPS Heater 1 < PR BH-1: 250mA
Deployables Release < PR DEPLOY: 2A

Switching

Switching

Battery
A

Battery
B

Note: Each battery
can output 4A
continuously

Note: Each SMPS
can output 3A
continuously

3.3V
SMPS
A

3.3V
SMPS
B

Power
ORing

EPS

Switching

PR 3.3V-0: 20mA > IHU
PR 3.3V-1: 60mA > IHU: SD Card
PR 3.3V-2: 20mA > ADCS
PR 3.3V-3: 45mA > ADCS: IMUs
PR 3.3V-4: 55mA > ADCS: GPS
PR 3.3V-5: 90mA > IFJR & SD Card
PR 3.3V-6: 105mA > Comms & Radios
PR BATT-5: 300mA > ADCS: Magnetorquers
PR BATT-6: 400mA > Comms: TX Amplifier
PR 3.3V-7: 50mA > Payload: 0
PR 3.3V-8: 390mA > Payload: 1
PR 3.3V-9: 25mA > Payload: 2
PR 3.3V-10: 15mA > Payload: 4
PR 3.3V-11: 390mA > Payload: 5
PR 3.3V-12: 0mA > Payload: 6
PR BATT-0: 250mA > Payload: 7
PR BATT-1: 0mA > Payload: 8
PR BATT-2: 0mA > Payload: 9
PR BATT-3: 0mA > Payload: 10
PR BATT-4: 0mA > Payload: 11

Note: Each power rail channel (PR_) is rated for 1A
PR_DEPLOY is rated for 2A
Currents shown are the expected consumption

Satellite Bus

CougSat-1 Payload
Germination Lighting

Germination Camera

Germination Sensors

Ionizing Radiation Sensor

Ground Camera

Unused

Germination Heater

Unused

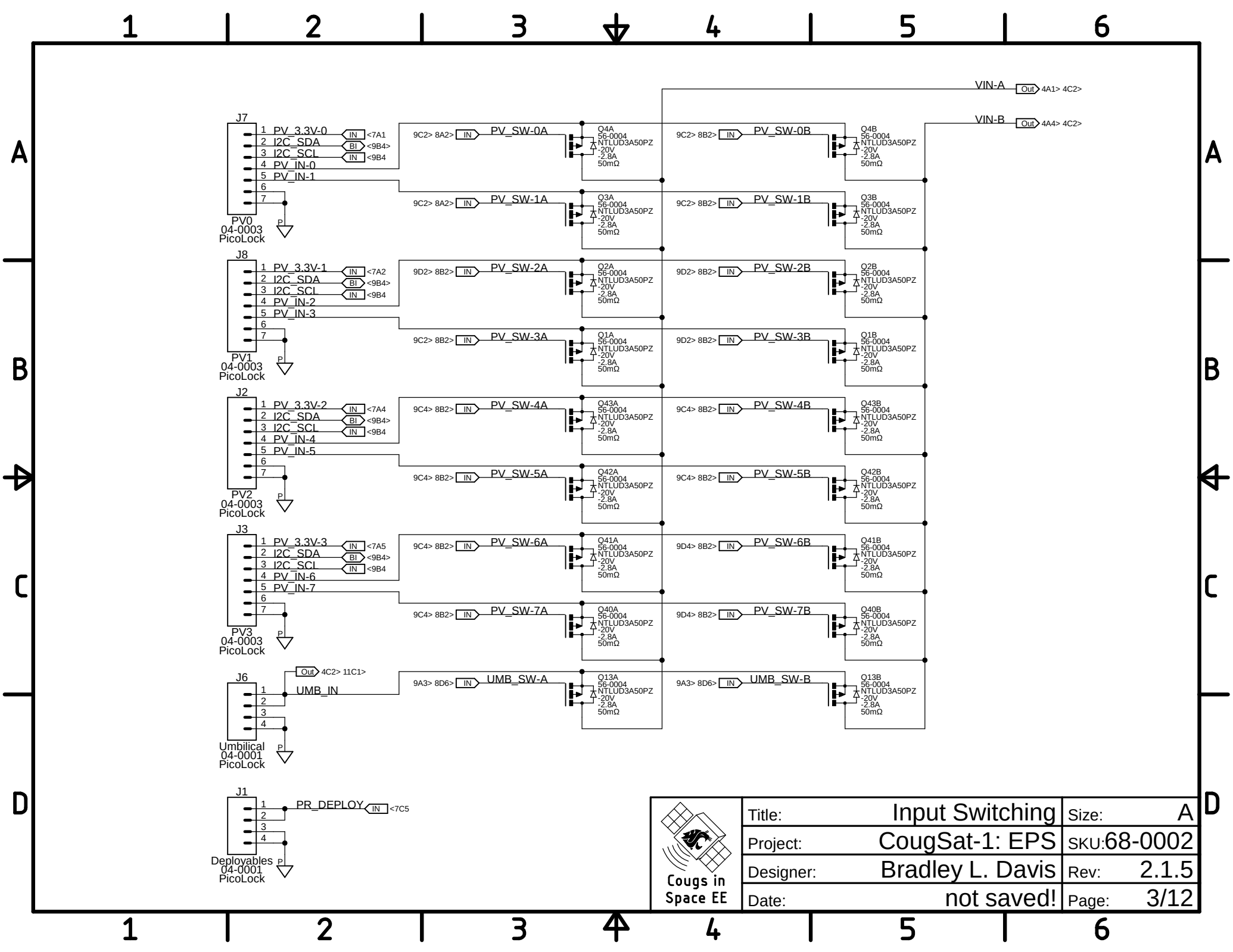
Unused

Unused

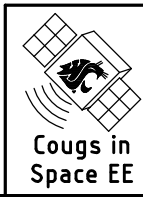
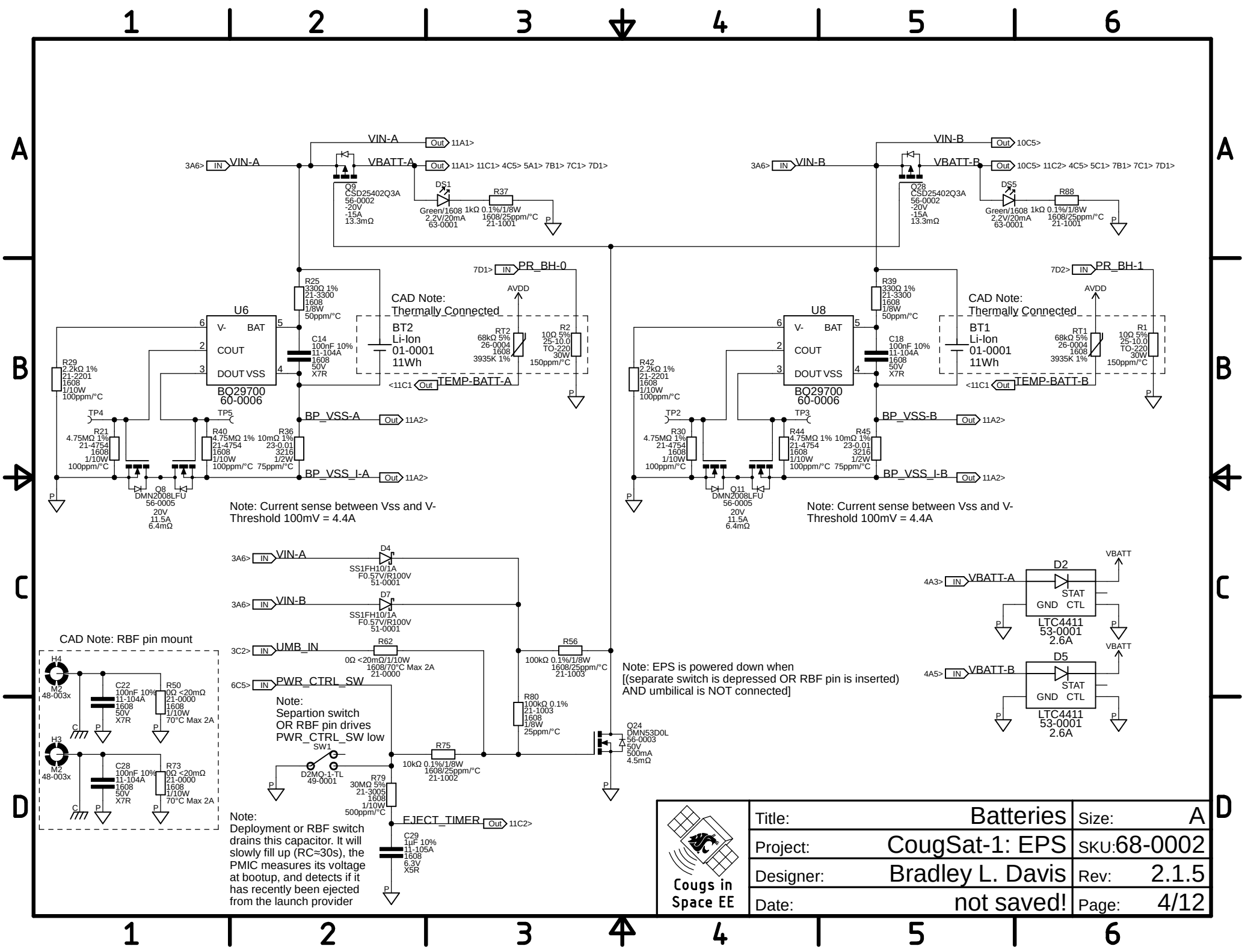
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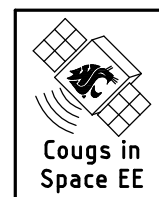
Title:	Power Rails	Size:	A
Project:	CougSat-1: EPS	SKU:	68-0002
Designer:	Bradley L. Davis	Rev:	2.1.5
Date:	not saved!	Page:	2/12



Title:	Input Switching	Size:	A
Project:	CougSat-1: EPS	SKU:	68-0002
Designer:	Bradley L. Davis	Rev:	2.1.5
Date:	not saved!	Page:	3/12



Title:	Batteries		Size:	A
Project:	CougSat-1: EPS		SKU:	68-0002
Designer:	Bradley L. Davis		Rev:	2.1.5
Date:	not saved!		Page:	4/12



Title:	3.3V Regulation	Size:	A
Project:	CougSat-1: EPS	SKU:	68-0002
Designer:	Bradley L. Davis	Rev:	2.1.5
Date:	not saved!	Page:	5/12

1

2

3

4

5

6

A

A

B

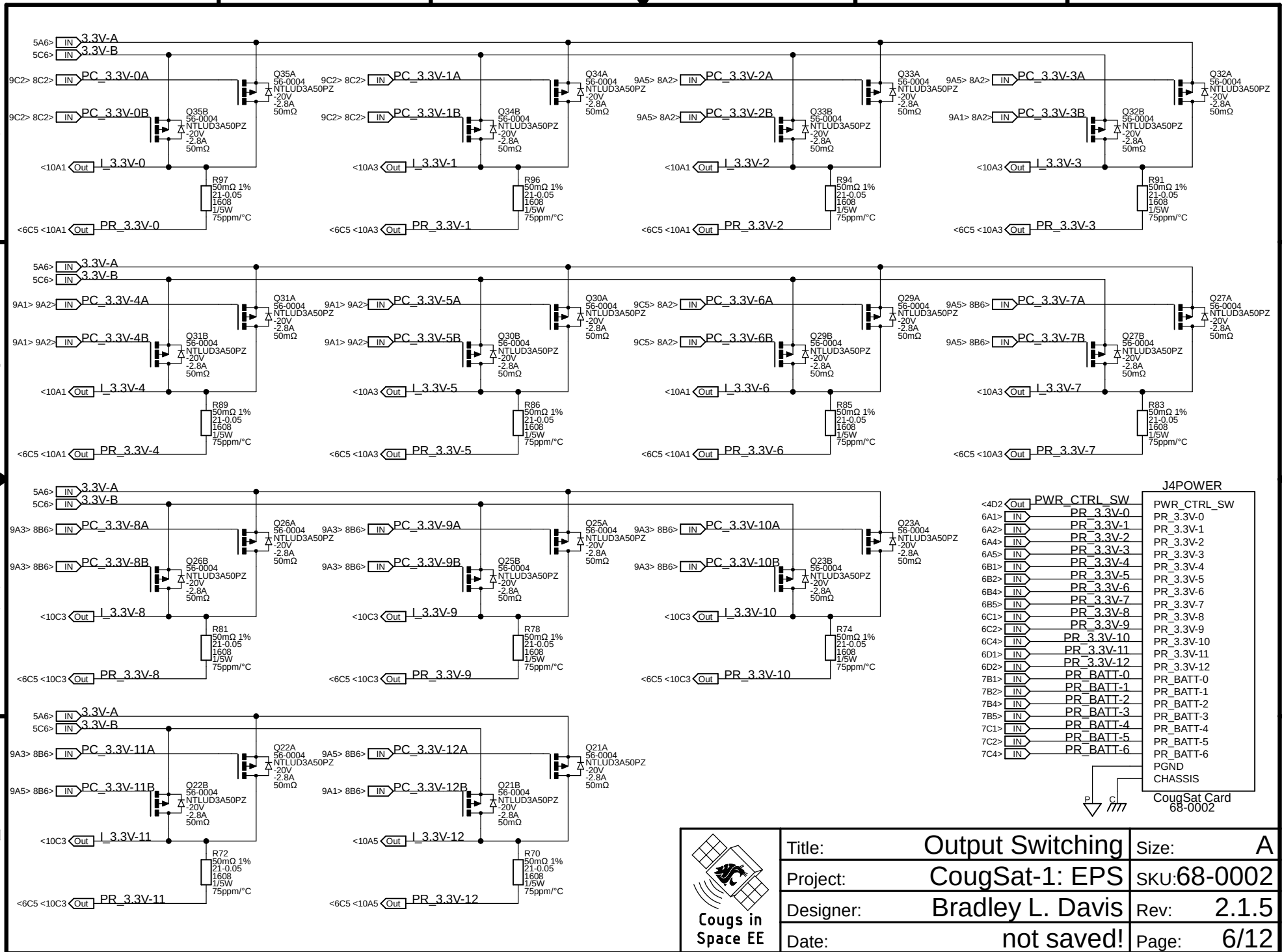
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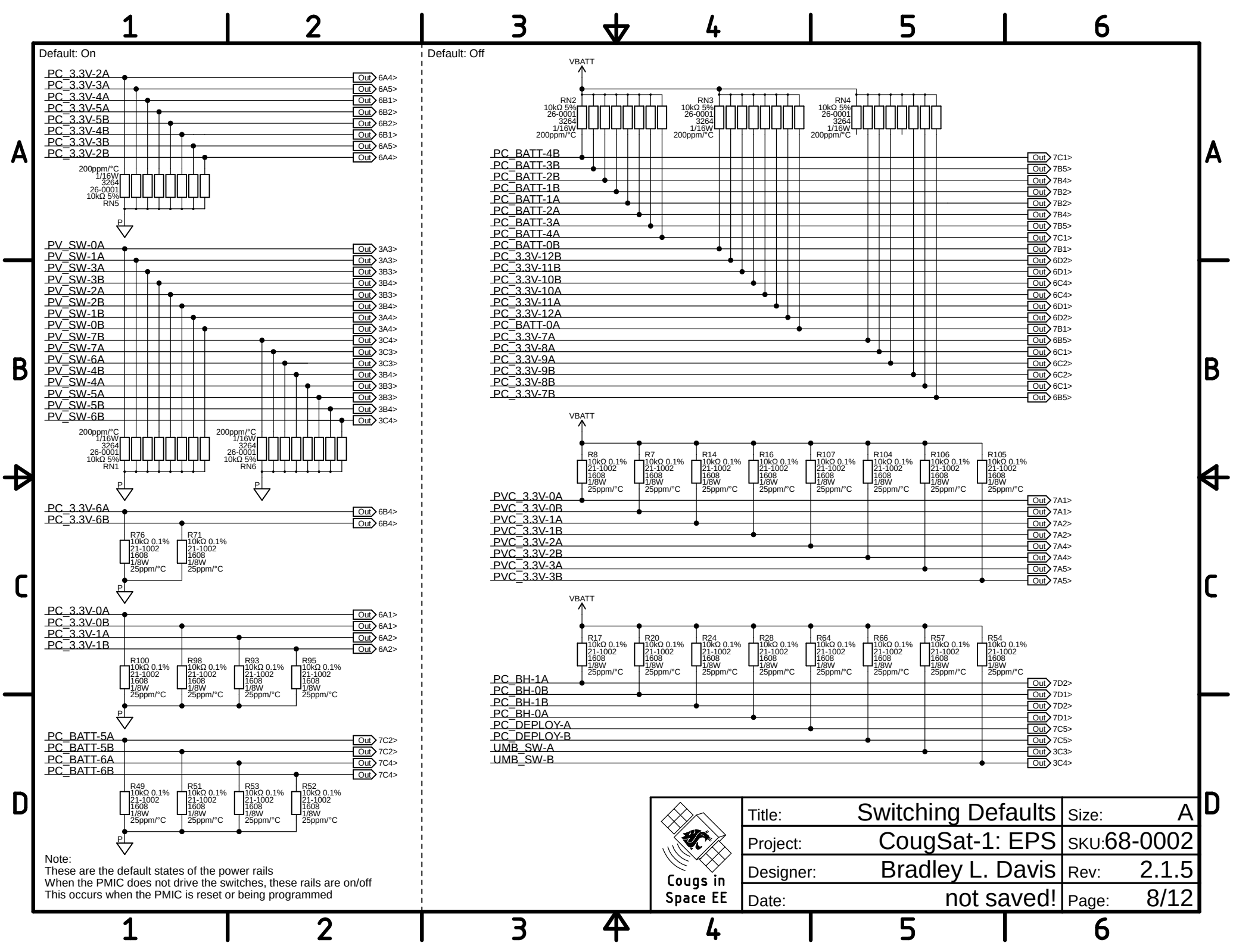
C

C

D

D





Default: On

PC_3.3V-2A
PC_3.3V-3A
PC_3.3V-4A
PC_3.3V-5A
PC_3.3V-5B
PC_3.3V-4B
PC_3.3V-3B
PC_3.3V-2B

Out 6A4>
Out 6A5>
Out 6B1>
Out 6B2>
Out 6B1>
Out 6A5>
Out 6A4>

PV_SW-0A
PV_SW-1A
PV_SW-3A
PV_SW-3B
PV_SW-2A
PV_SW-2B
PV_SW-1B
PV_SW-0B
PV_SW-7B
PV_SW-7A
PV_SW-6A
PV_SW-4B
PV_SW-4A
PV_SW-5A
PV_SW-5B
PV_SW-6B

Out 3A3>
Out 3A3>
Out 3B3>
Out 3B4>
Out 3B3>
Out 3B4>
Out 3A4>
Out 3A4>
Out 3C4>
Out 3C3>
Out 3C3>
Out 3B4>
Out 3B3>
Out 3B3>
Out 3B4>
Out 3C4>

PC_3.3V-6A
PC_3.3V-6B

Out 6B4>
Out 6B4>

PC_3.3V-0A
PC_3.3V-0B
PC_3.3V-1A
PC_3.3V-1B

Out 6A1>
Out 6A1>
Out 6A2>
Out 6A2>

PC_BATT-5A
PC_BATT-5B
PC_BATT-6A
PC_BATT-6B

Out 7C2>
Out 7C2>
Out 7C4>
Out 7C4>

Note:
These are the default states of the power rails
When the PMIC does not drive the switches, these rails are on/off
This occurs when the PMIC is reset or being programmed

Default: Off

PC_BATT-4B
PC_BATT-3B
PC_BATT-2B
PC_BATT-1B
PC_BATT-1A
PC_BATT-2A
PC_BATT-3A
PC_BATT-4A
PC_BATT-0B
PC_3.3V-12B
PC_3.3V-11B
PC_3.3V-10B
PC_3.3V-10A
PC_3.3V-11A
PC_3.3V-12A
PC_BATT-0A
PC_3.3V-7A
PC_3.3V-8A
PC_3.3V-9A
PC_3.3V-9B
PC_3.3V-8B
PC_3.3V-7B


Out 7C1>
Out 7B5>
Out 7B4>
Out 7B2>
Out 7B4>
Out 7B5>
Out 7C1>
Out 7B1>
Out 6D2>
Out 6D1>
Out 6C4>
Out 6C4>
Out 6D1>
Out 6D2>
Out 7B1>
Out 6B5>
Out 6C1>
Out 6C2>
Out 6C2>
Out 6C1>
Out 6B5>

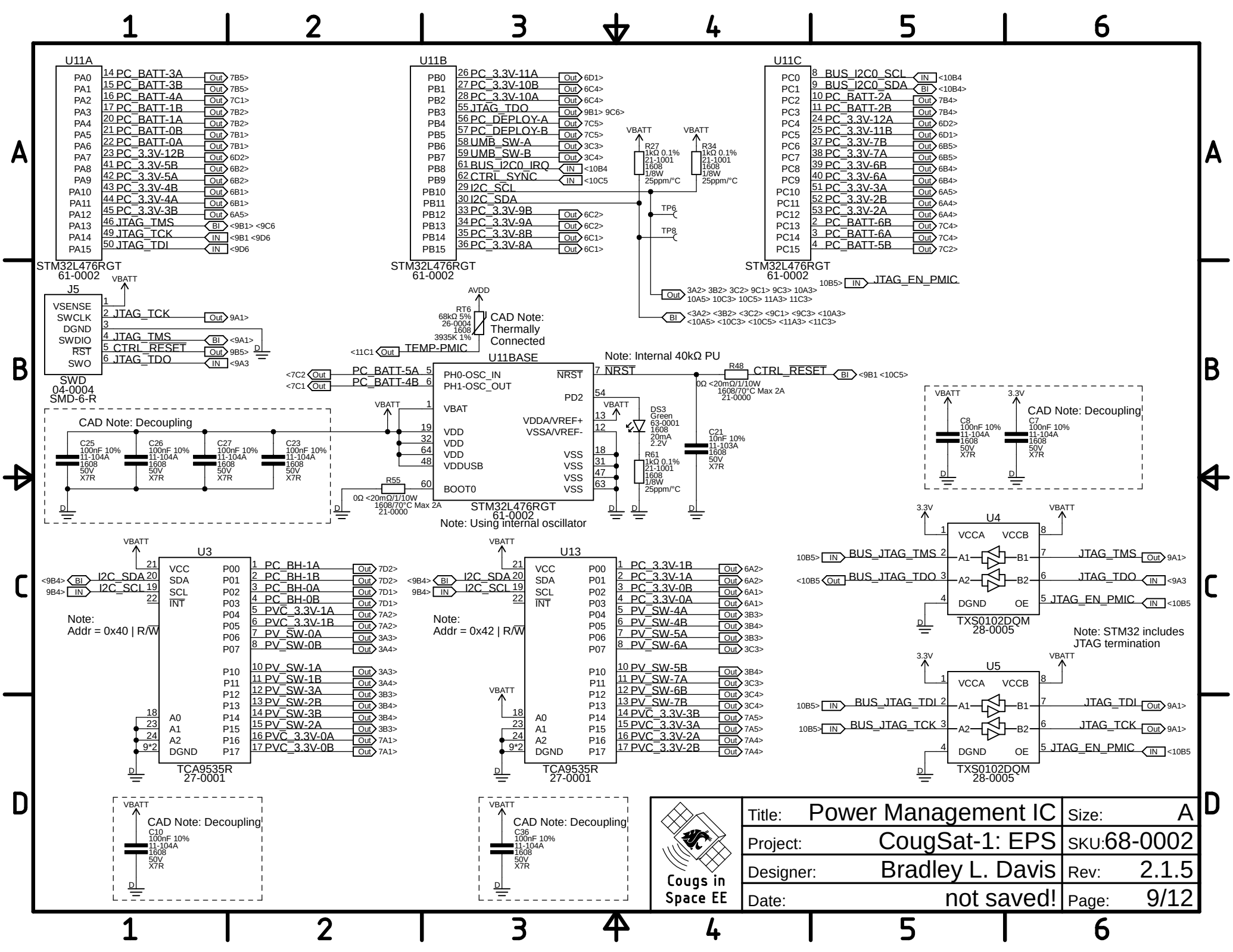
PVC_3.3V-0A
PVC_3.3V-0B
PVC_3.3V-1A
PVC_3.3V-1B
PVC_3.3V-2A
PVC_3.3V-2B
PVC_3.3V-3A
PVC_3.3V-3B

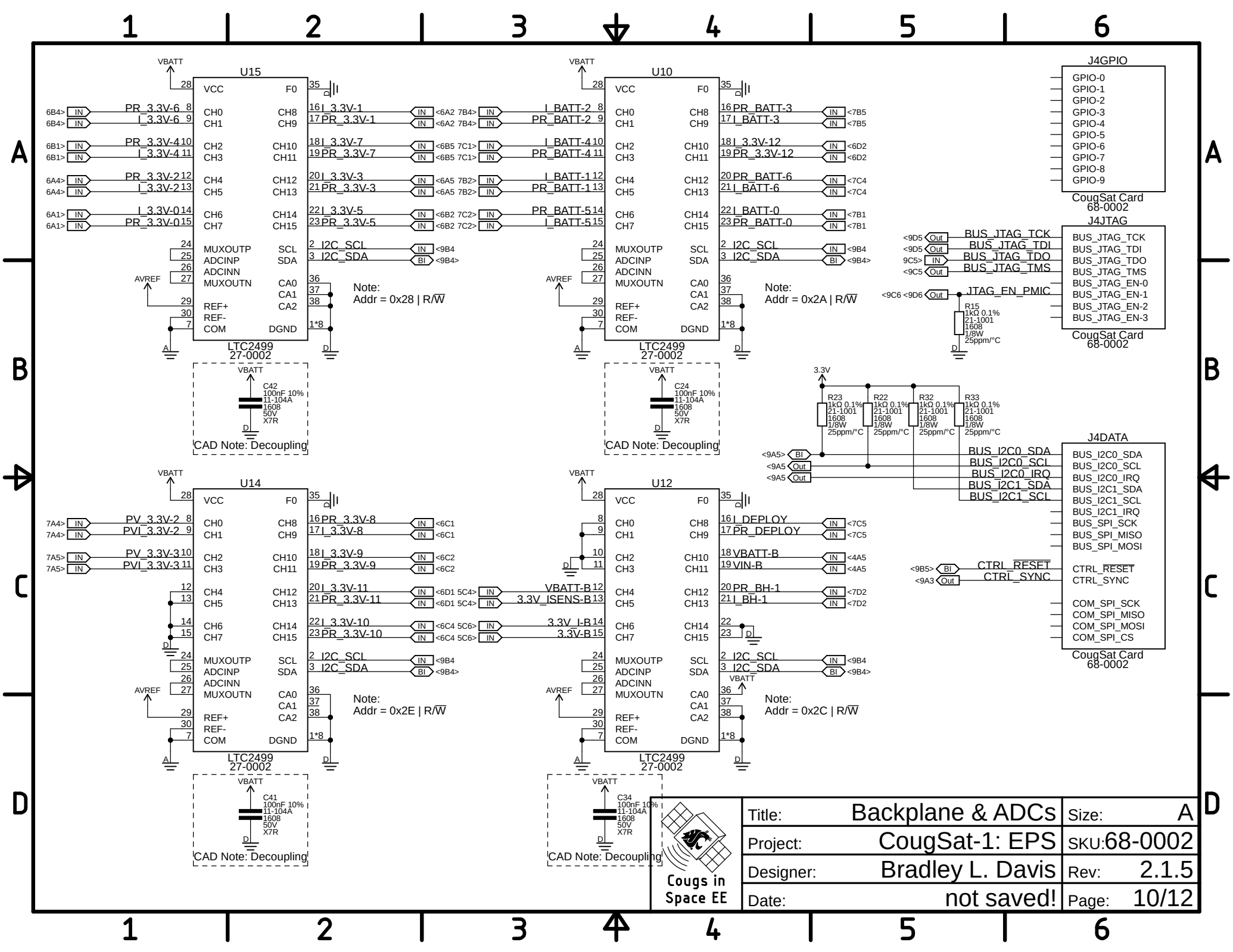
Out 7A1>
Out 7A1>
Out 7A2>
Out 7A2>
Out 7A4>
Out 7A4>
Out 7A5>
Out 7A5>

PC_BH-1A
PC_BH-0B
PC_BH-1B
PC_BH-0A
PC_DEPLOY-A
PC_DEPLOY-B
UMB_SW-A
UMB_SW-B

Out 7D2>
Out 7D1>
Out 7D2>
Out 7D1>
Out 7C5>
Out 7C5>
Out 3C3>
Out 3C4>

 Cougs in Space EE	Title: Switching Defaults	Size: A
	Project: CougSat-1: EPS	SKU:68-0002
	Designer: Bradley L. Davis	Rev: 2.1.5
	Date: not saved!	Page: 8/12





Title:	Backplane & ADCs	Size:	A
Project:	CougsSat-1: EPS	SKU:	68-0002
Designer:	Bradley L. Davis	Rev:	2.1.5
Date:	not saved!	Page:	10/12

1

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4

4

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6

A

A

B

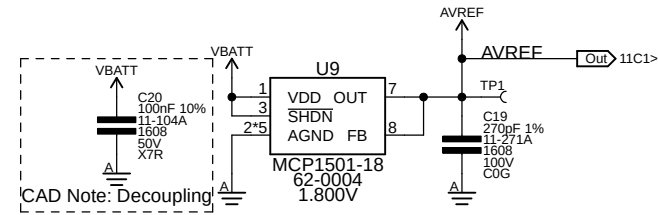
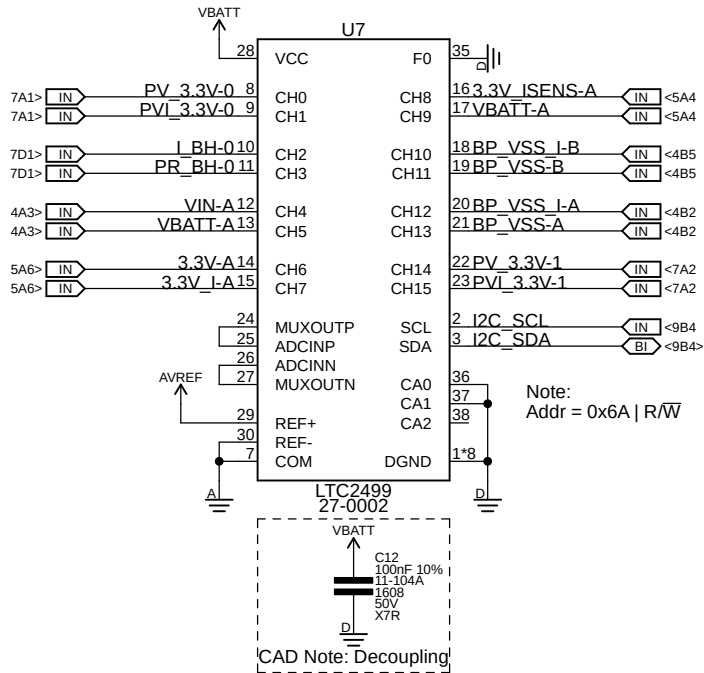
B

C

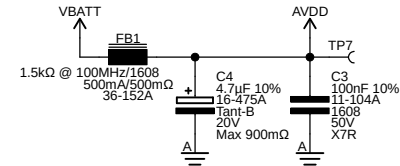
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D

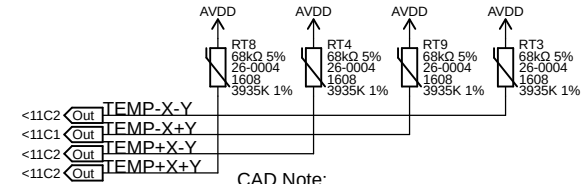
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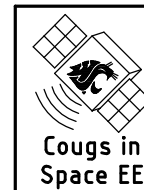
Note:
Differential ADC voltage reference is 1.800V
At 16b, 13.73μV/LSB
Maximum input is ±900mV



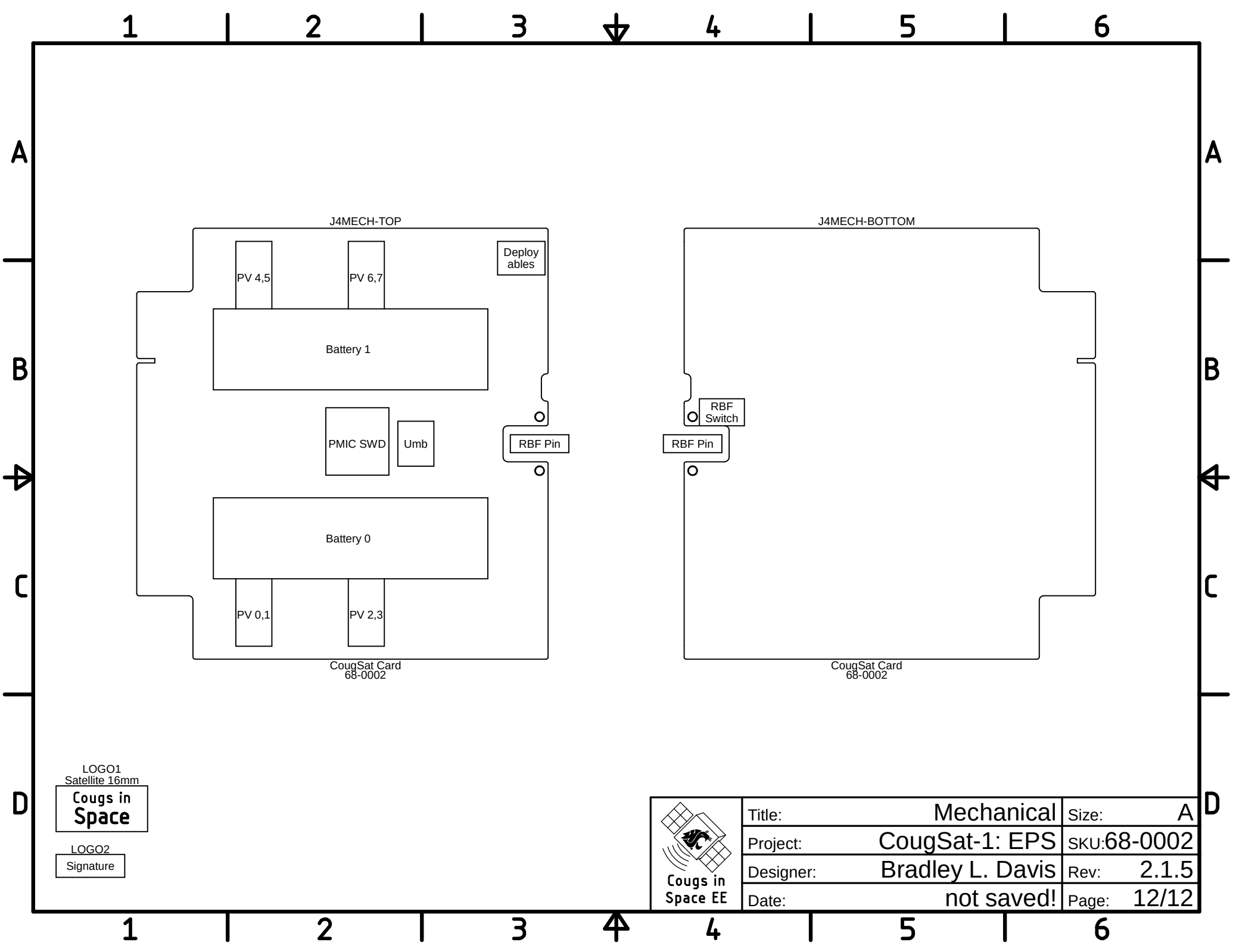
Note:
Single-Ended ADC voltage reference is 3.3V
At 16b, 25.18μV/LSB
Maximum input is ±1.65V



CAD Note:
Place in the corner of the PCB
that matches the connected
signal name, bottom side



Title:	ADCs	Size:	A
Project:	CougsSat-1: EPS	SKU:	68-0002
Designer:	Bradley L. Davis	Rev:	2.1.5
Date:	not saved!	Page:	11/12



Title:	Mechanical	Size:	A
Project:	CougSat-1: EPS	SKU:	68-0002
Designer:	Bradley L. Davis	Rev:	2.1.5
Date:	not saved!	Page:	12/12