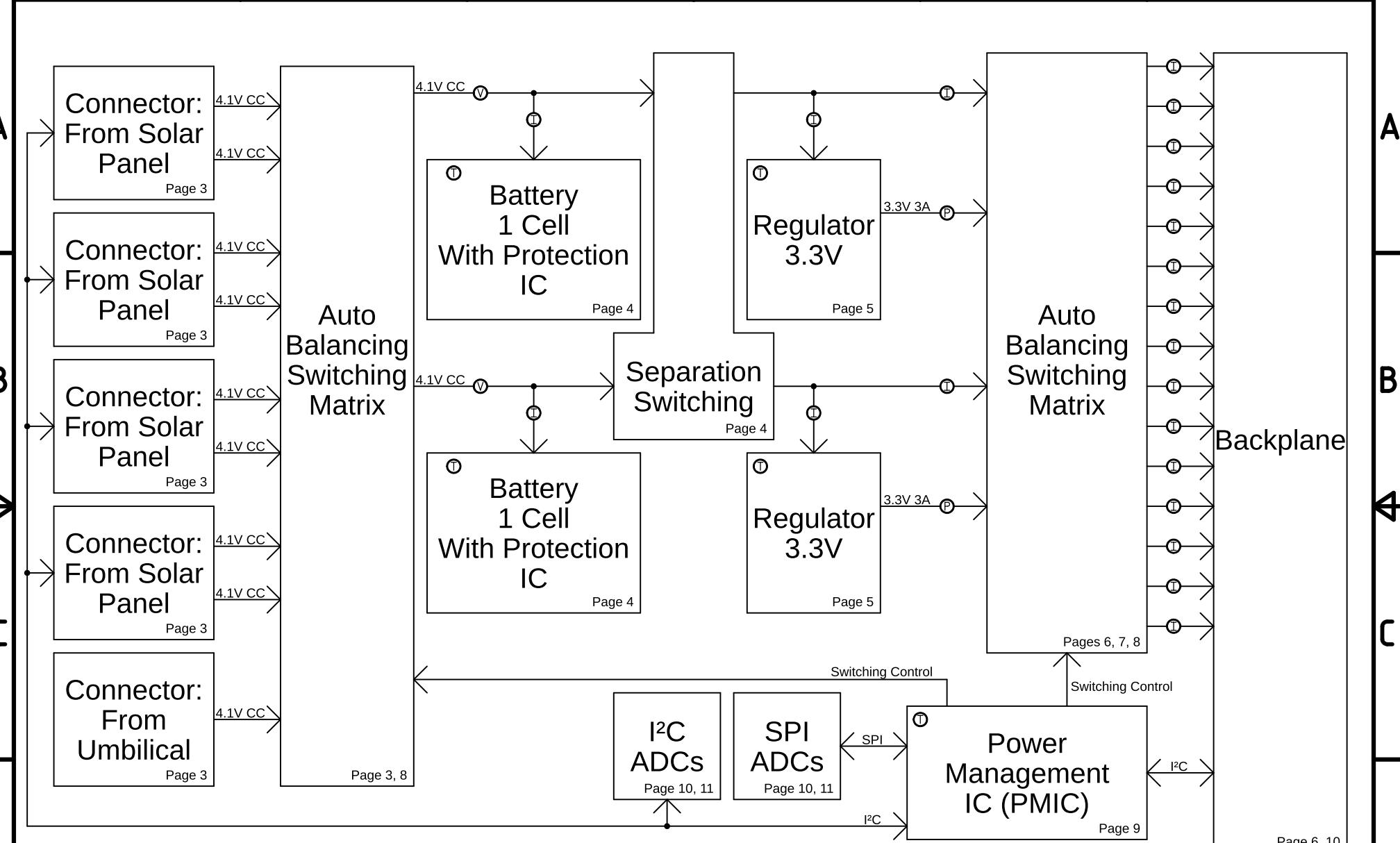


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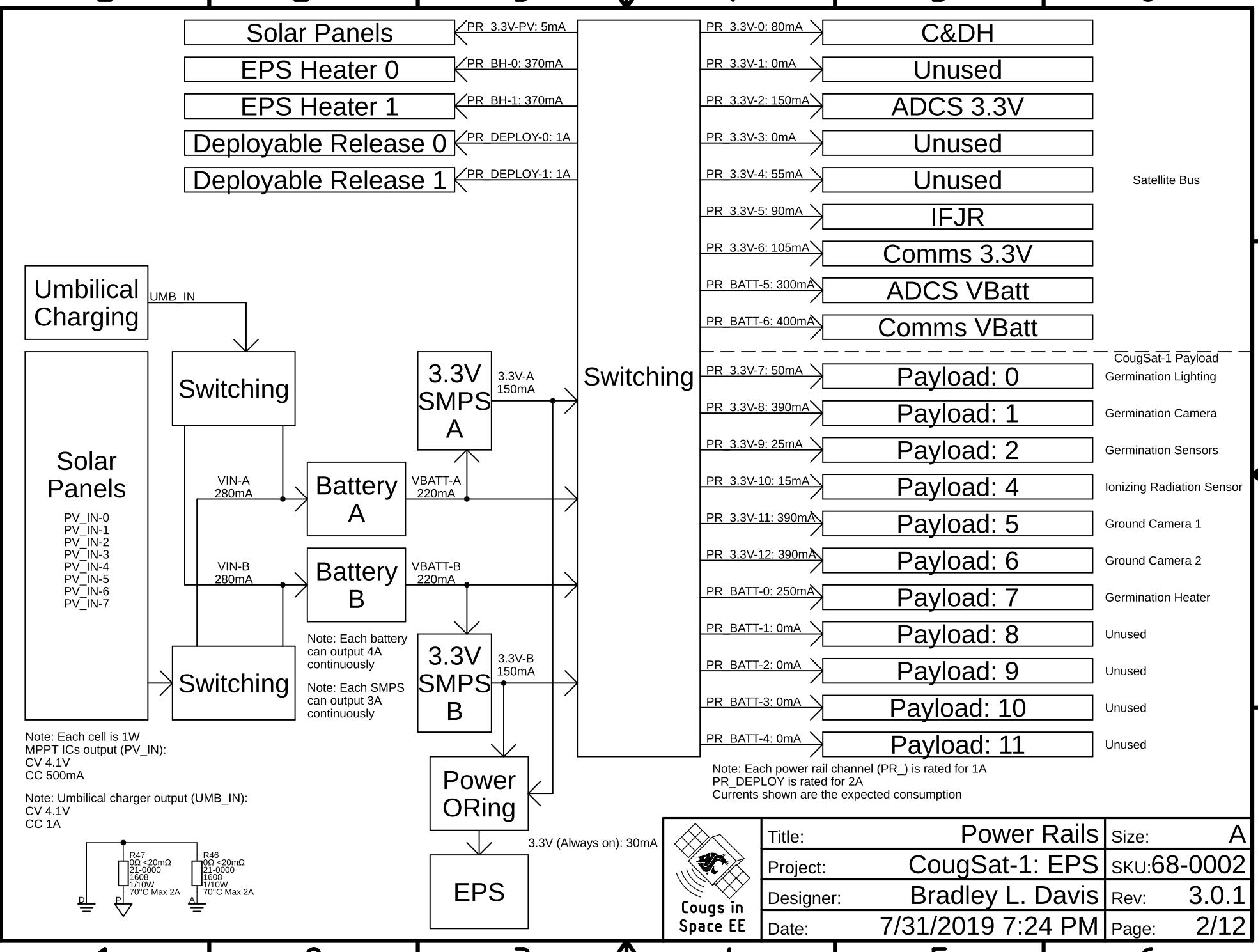
Page 1: Block Diagram
 Page 2: Power Rails
 Page 3: Input Switching Matrix
 Page 4: Batteries
 Page 5: 3.3V Regulation
 Page 6: Output Switching Matrix
 Page 7: Output Switching Matrix
 Page 8: Backplane Connections
 Page 9: Switching Default States

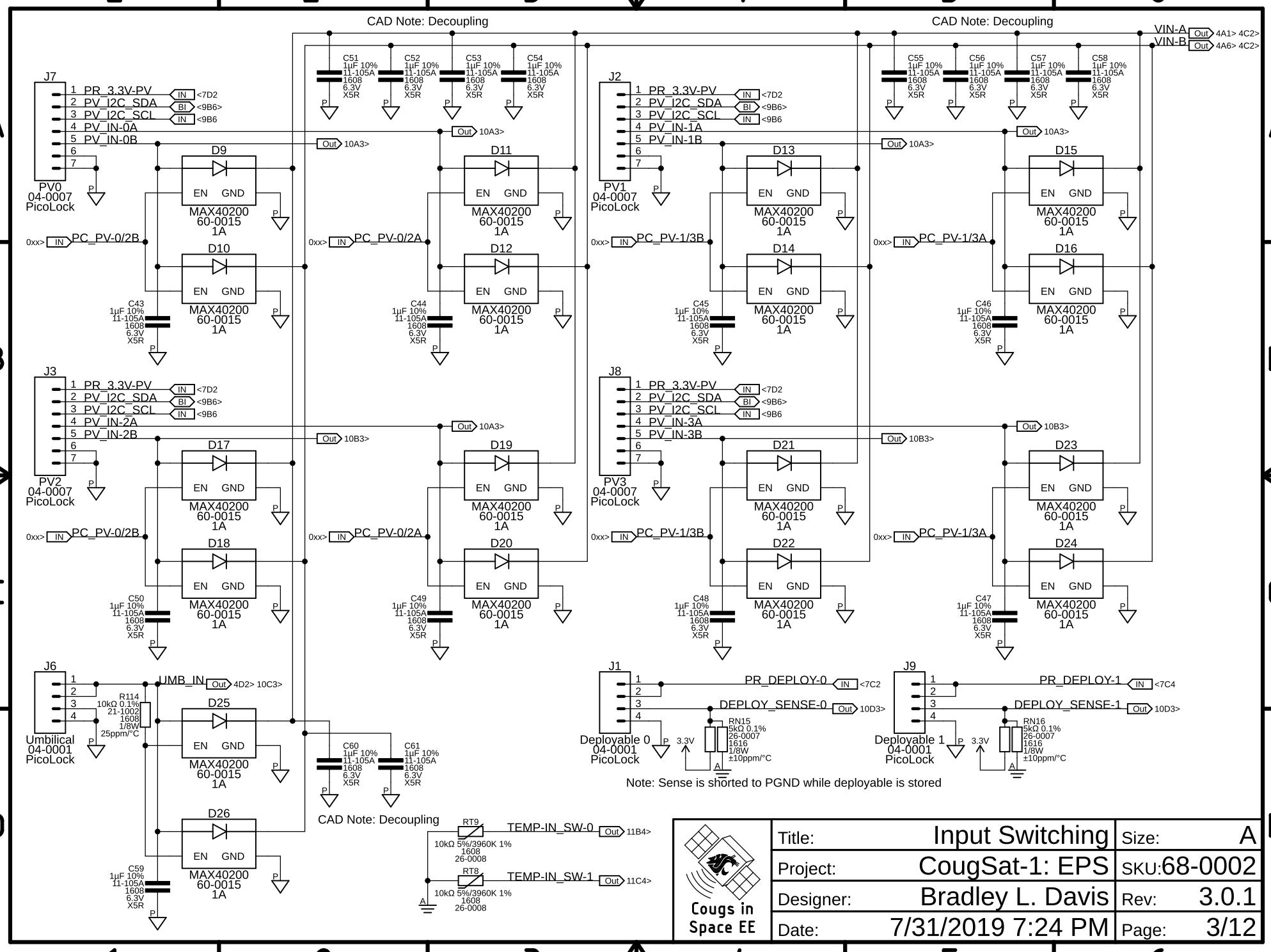
Page 10: PMIC
 Page 11: SPI ADCs
 Page 12: I²C ADCs
 Page 13: Mechanical Layout

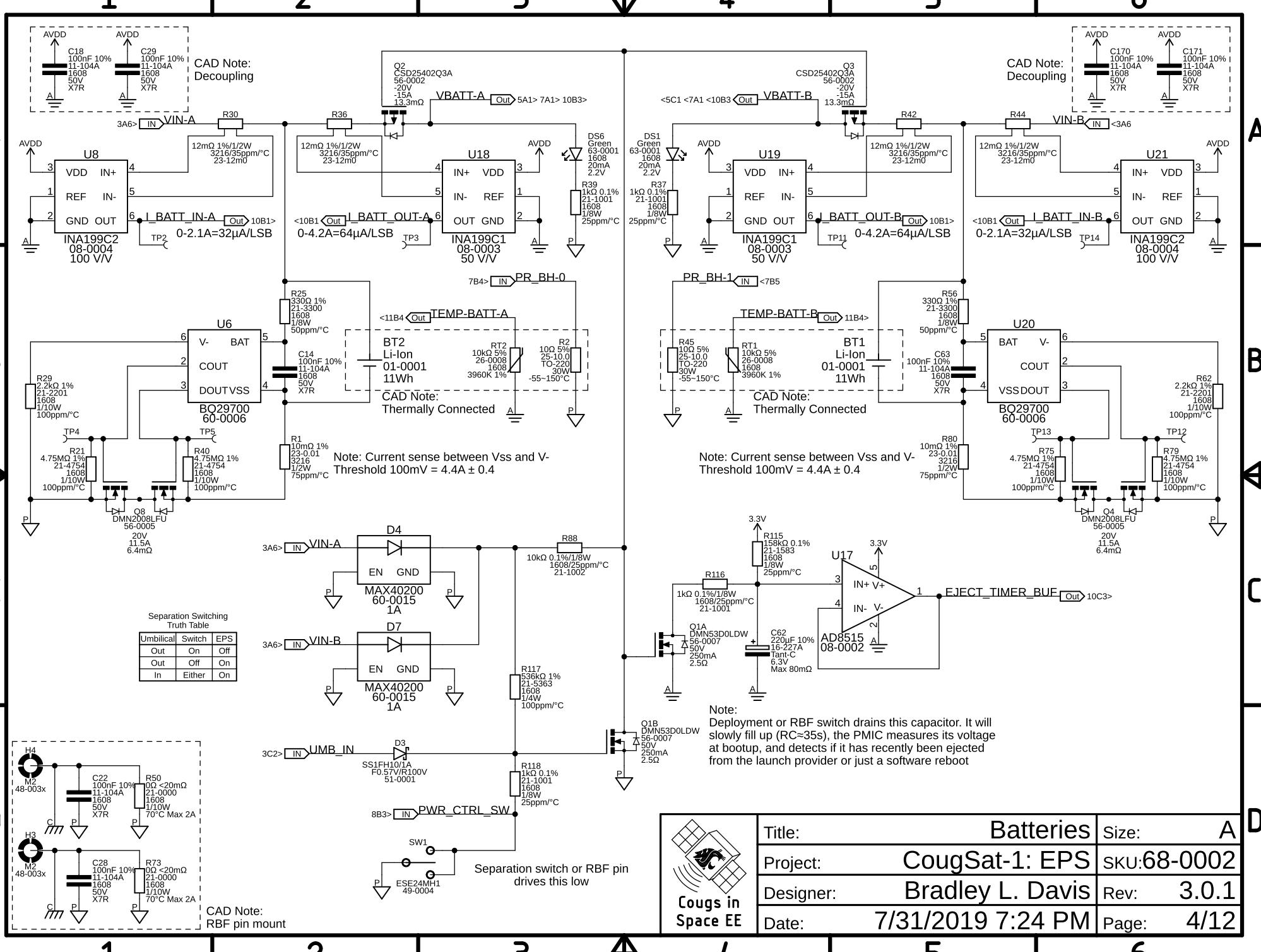


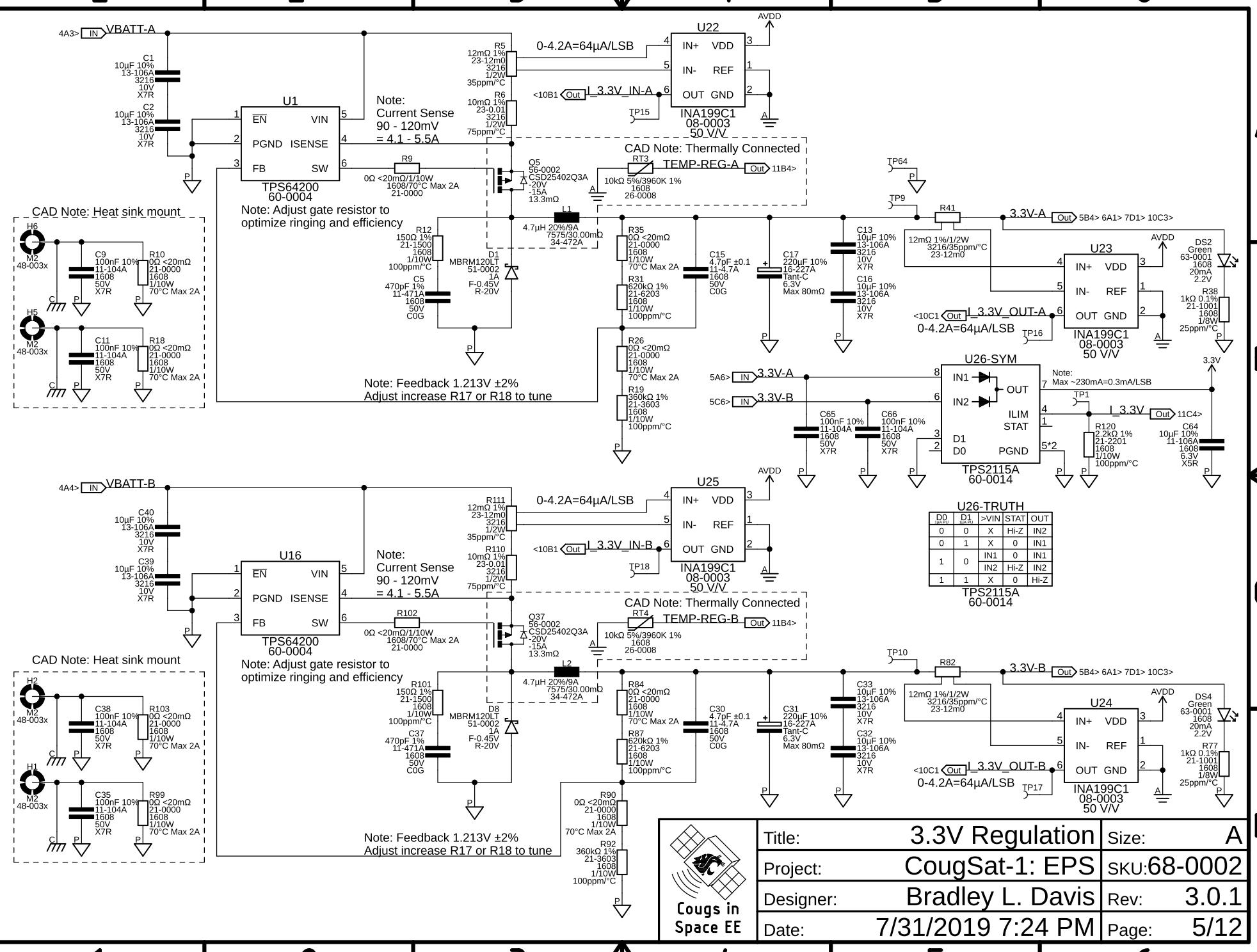
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| Designer: | Bradley L. Davis | Rev: | 3.0.1 |
| Date: | 7/31/2019 7:24 PM | Page: | 1/12 |

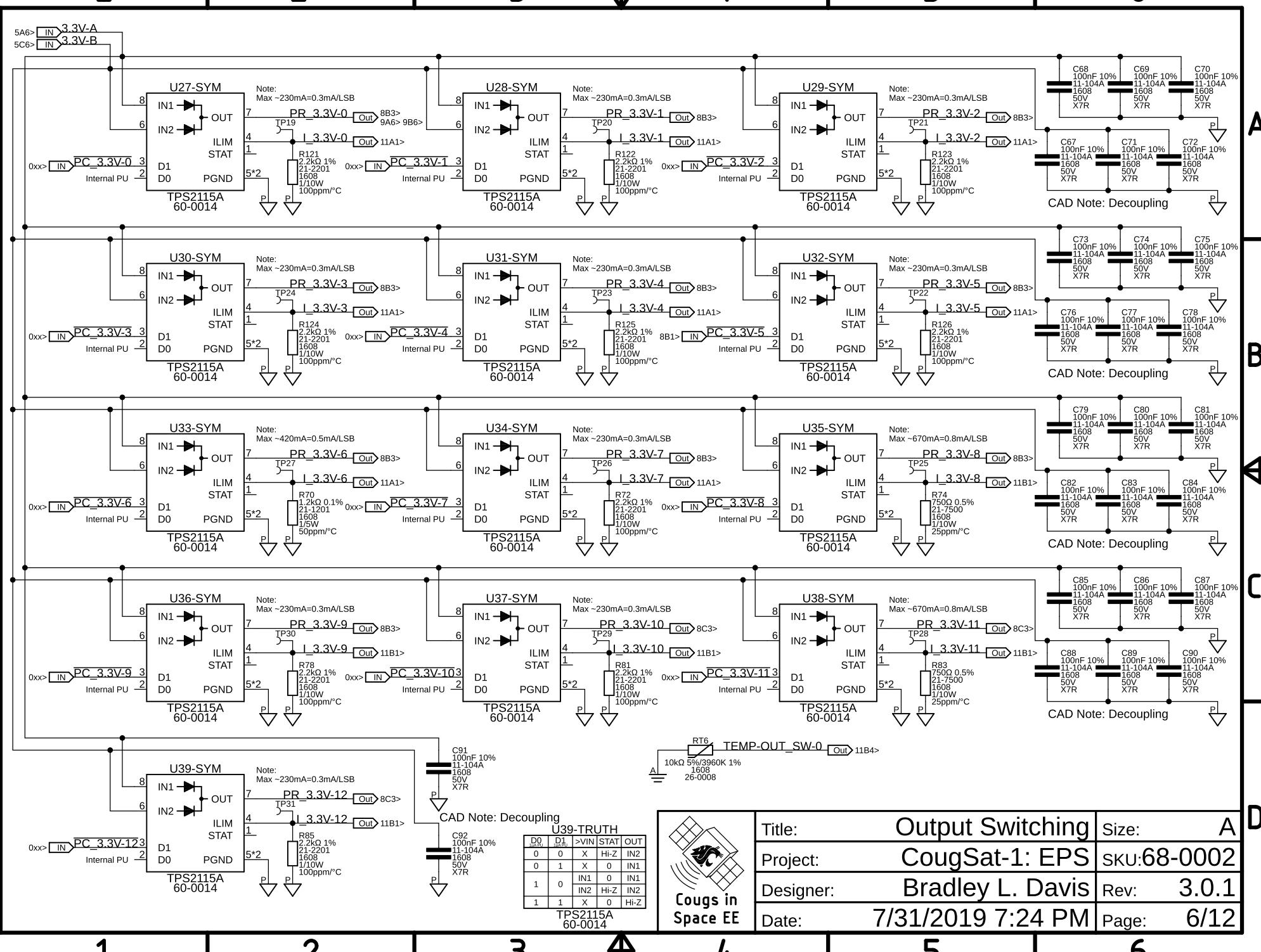
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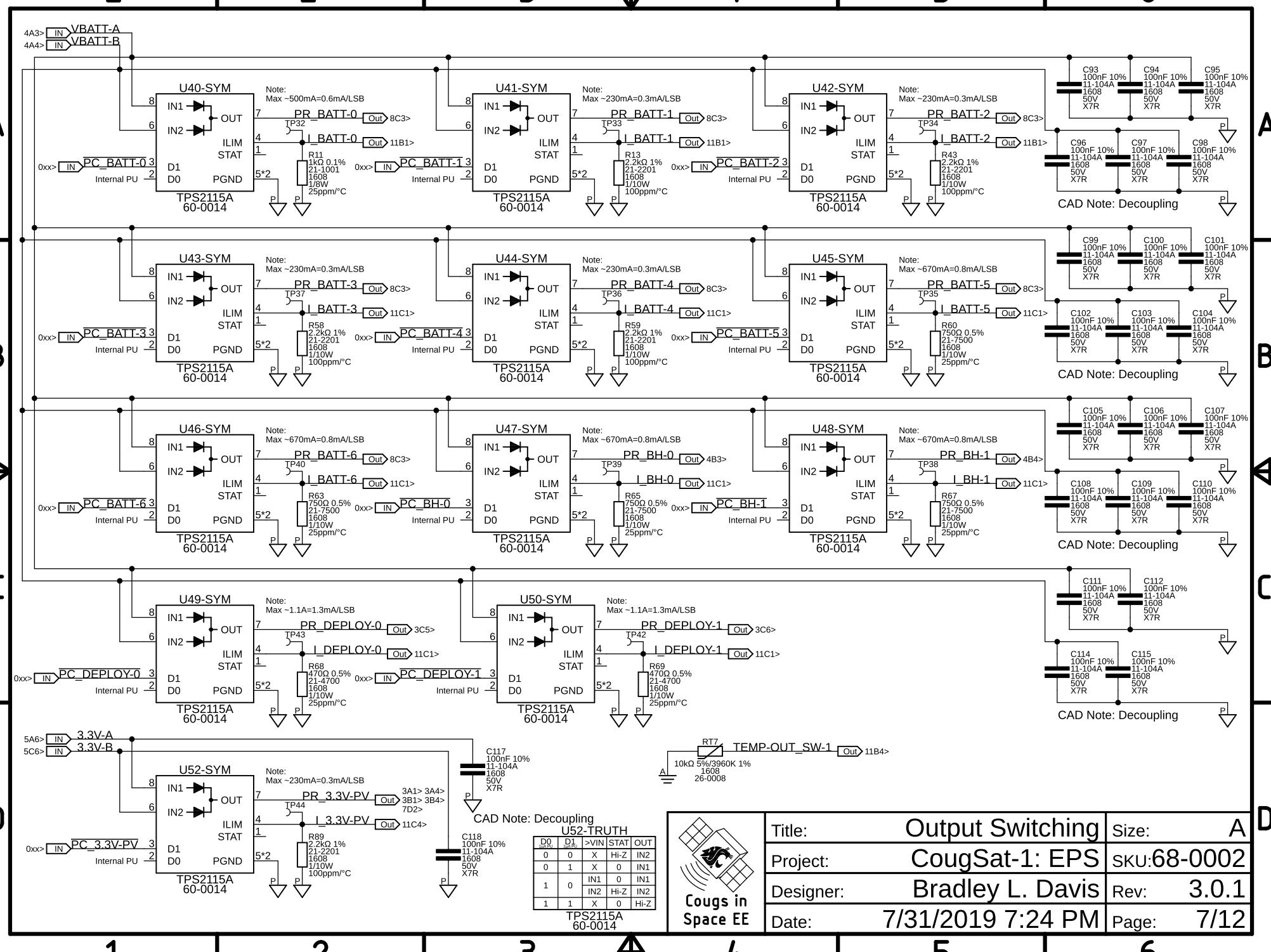




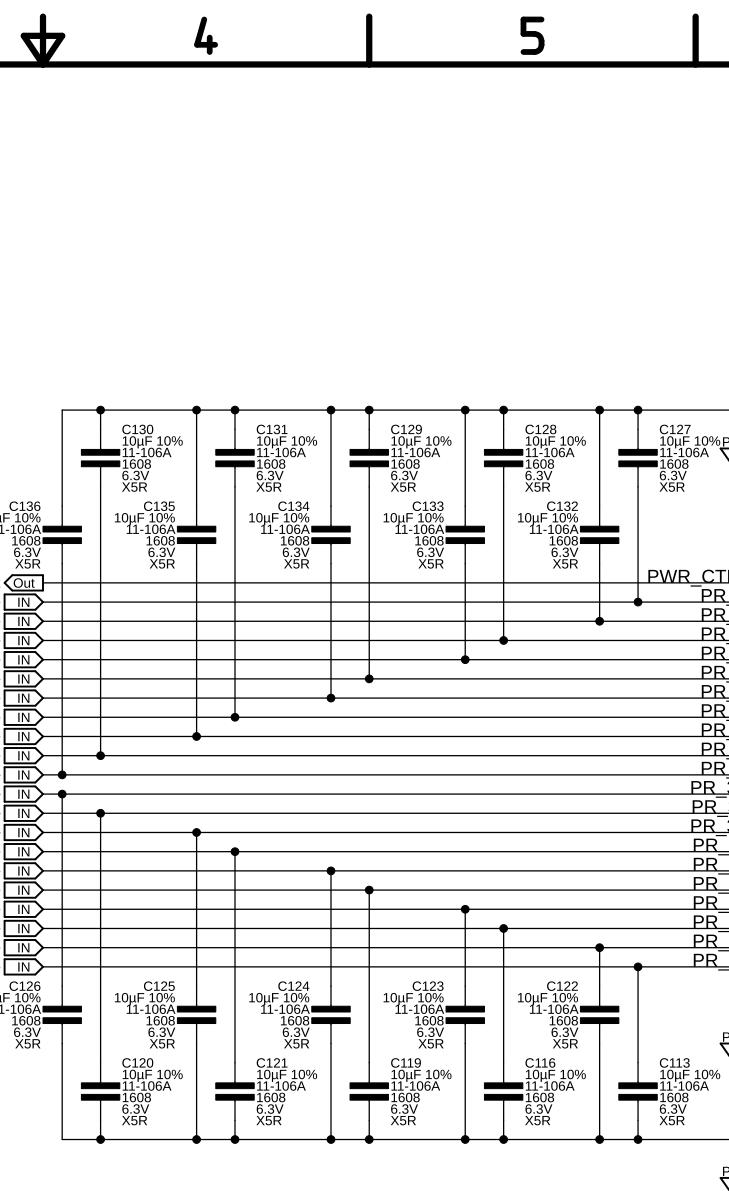
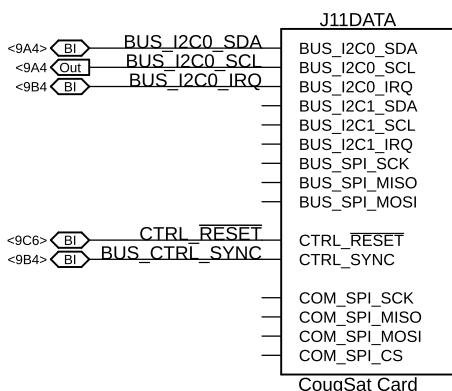
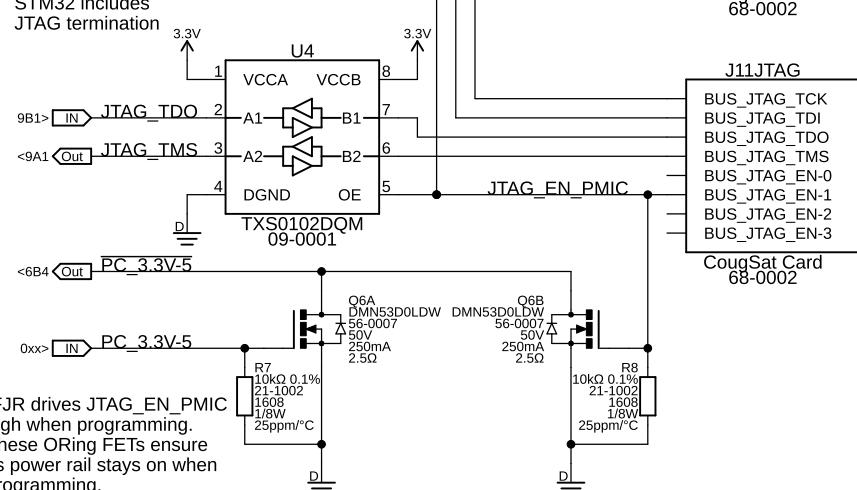
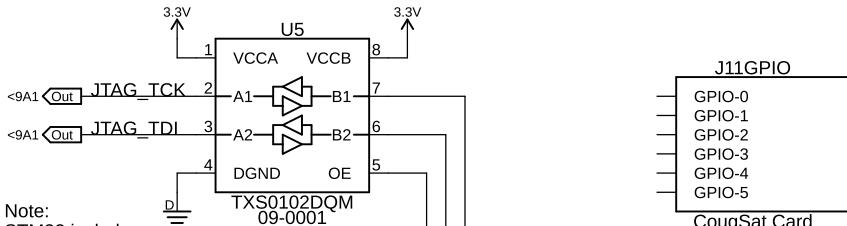
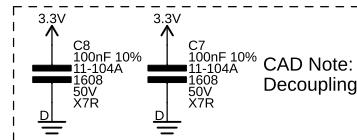








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Title: Backplane Connections

Size: A

Project: CougSat-1: EPS

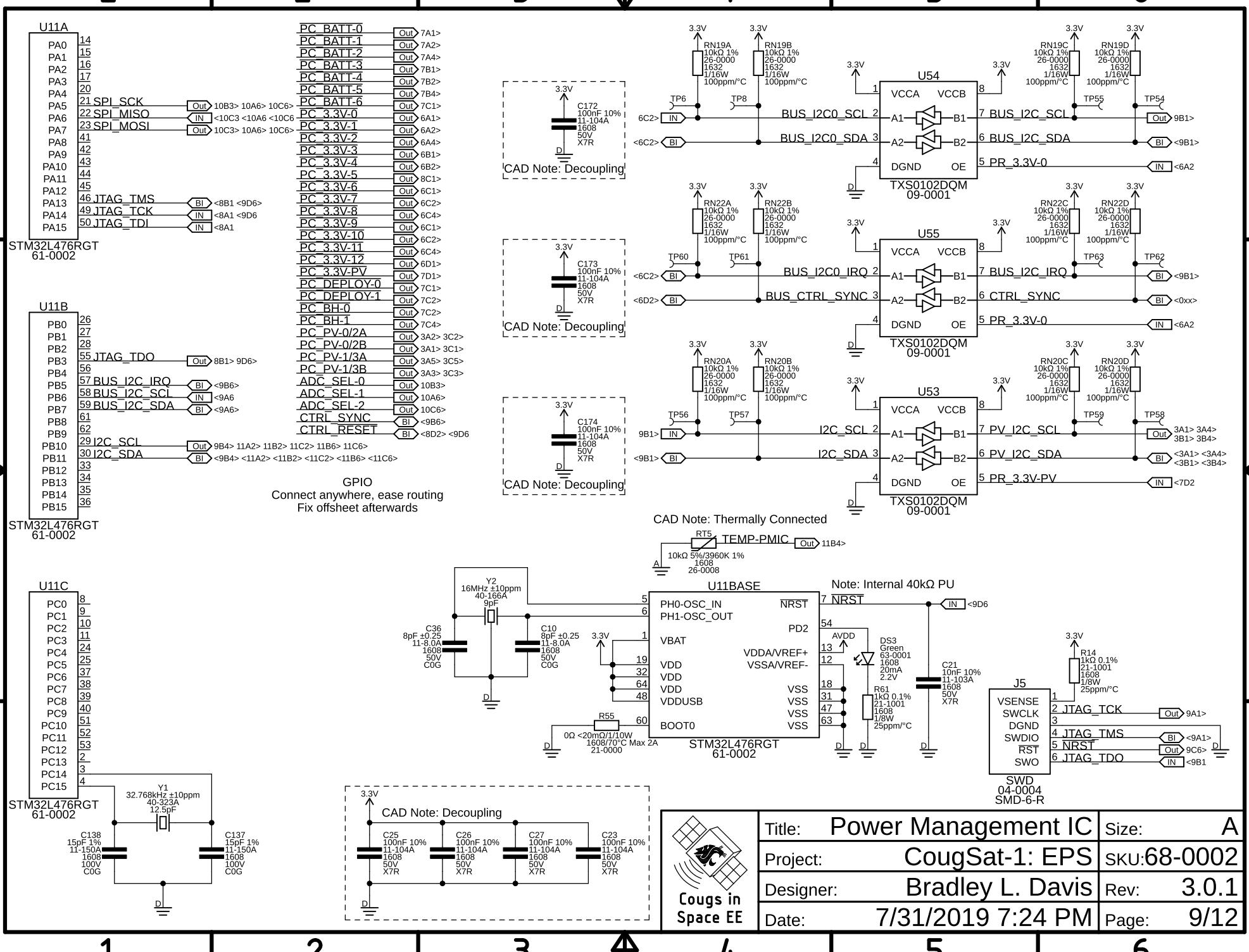
SKU: 68-0002

Designer: Bradley L. Davis

Rev: 3.0.1

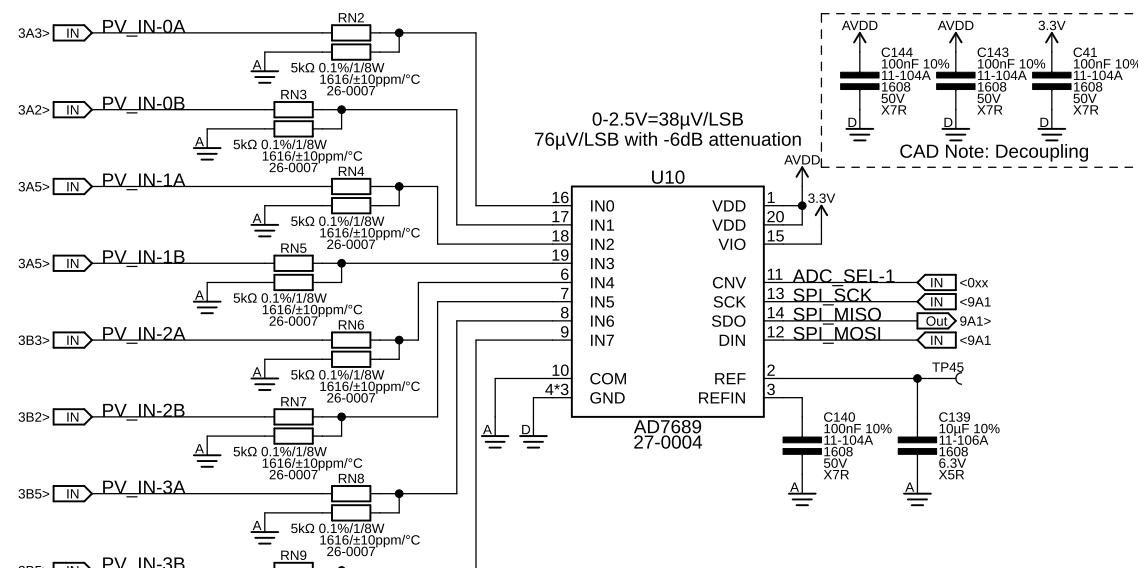
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Page: 8/12

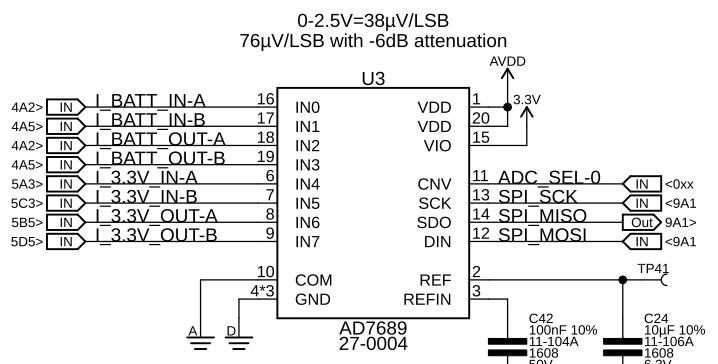


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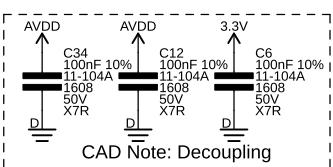
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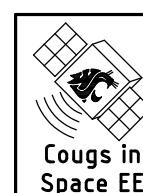
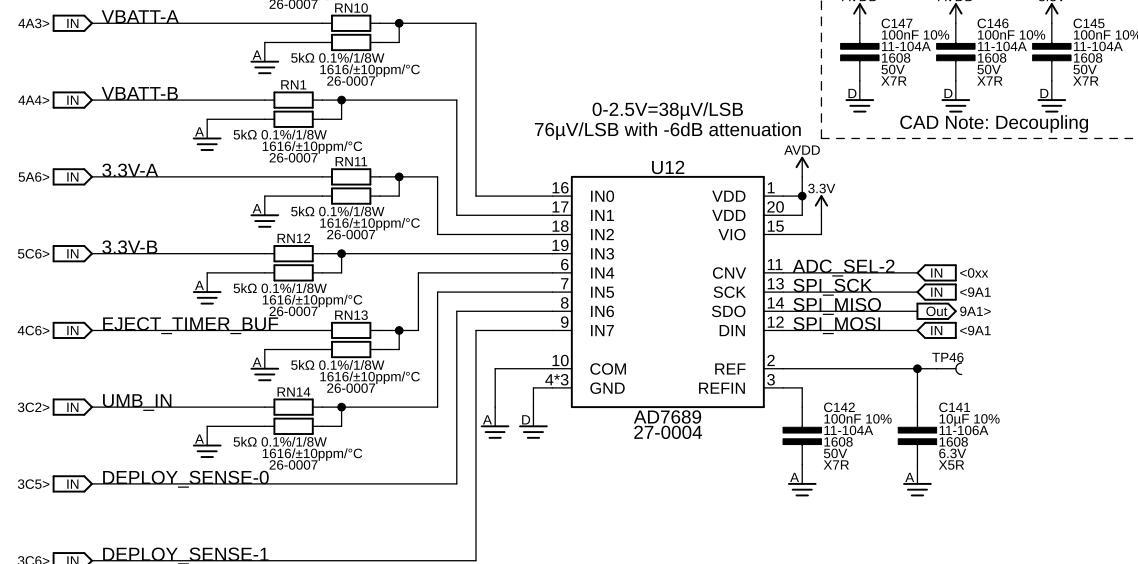
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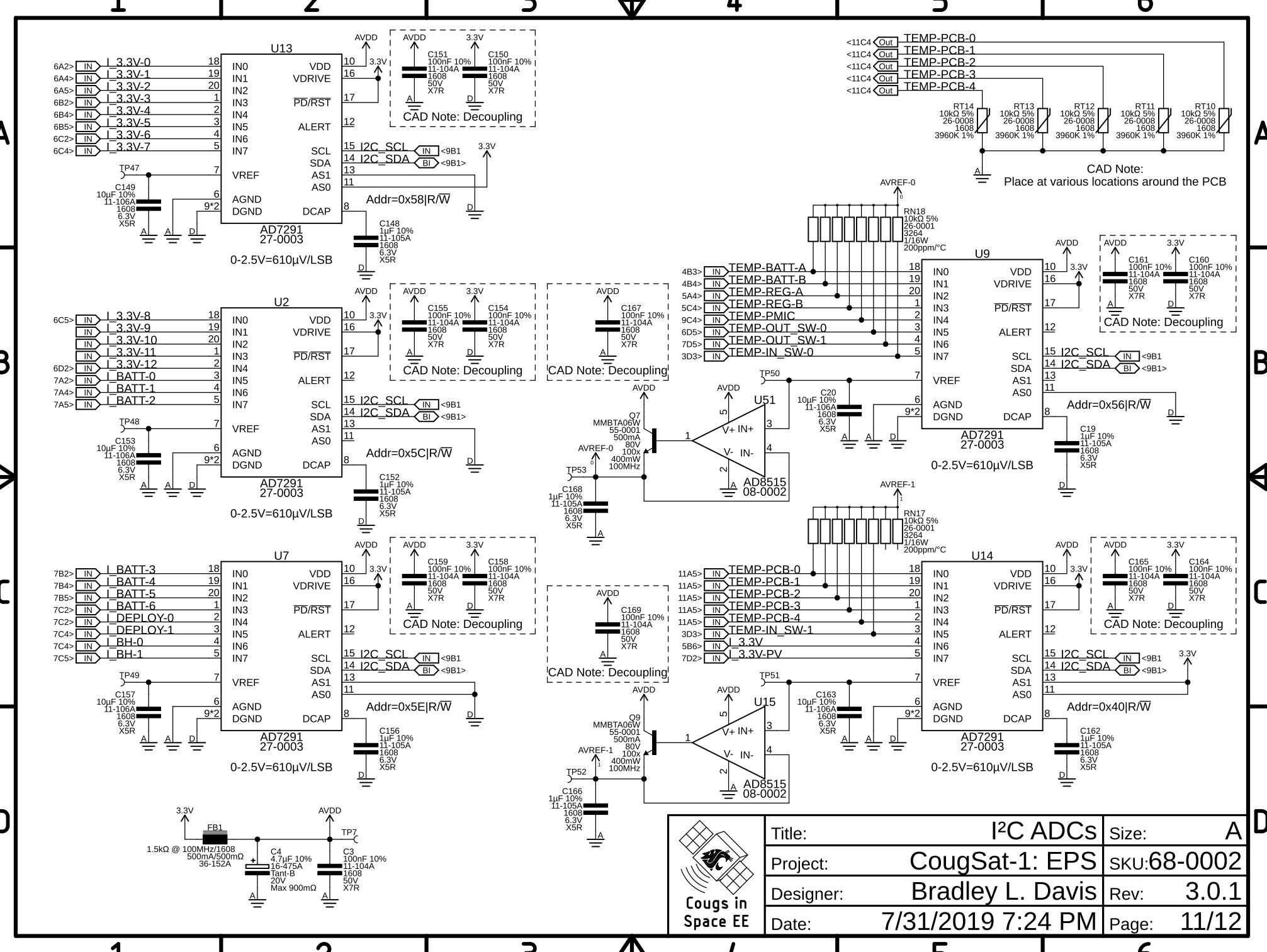
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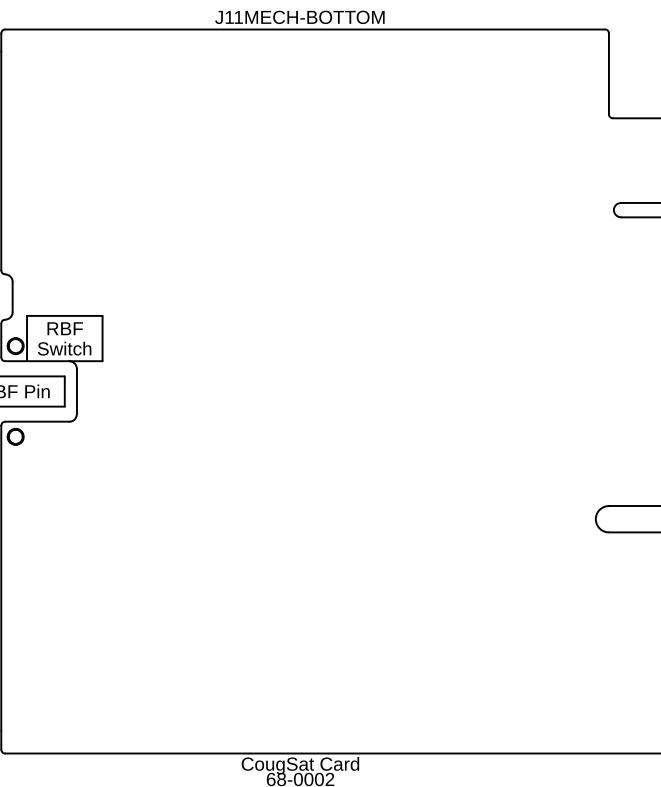
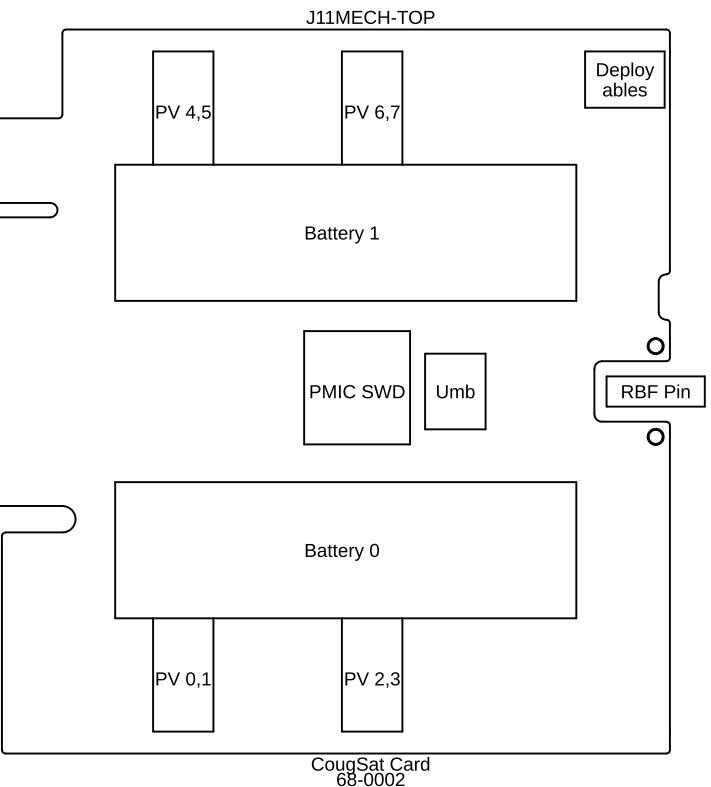
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| Date: | 7/31/2019 7:24 PM | Page: | 10/12 |



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A

A



| | | | |
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