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This document explains the function of the +X Panel, its schematic level design, its board level design, and its functional testing

+X Panel

Exterior PCB Panel Design

Revision: 1.0.7



Table of Contents

[1 Introduction 2](#_Toc14040003)

[1.1 Functions 2](#_Toc14040004)

[1.2 Requirements 2](#_Toc14040005)

[2 Detailed Description 3](#_Toc14040006)

[2.1 Functional Block Diagram 3](#_Toc14040007)

[2.1.1 Low-Gain Antenna 3](#_Toc14040008)

[2.1.2 GPS Patch Antenna 3](#_Toc14040009)

[2.1.3 Photodiode Sensor 3](#_Toc14040010)

[2.2 Schematic 3](#_Toc14040011)

[2.2.1 Photodiode 3](#_Toc14040012)

[2.2.2 Low – Gain Antenna 3](#_Toc14040013)

[2.2.3 GPS Patch Antenna 4](#_Toc14040014)

[2.2.4 Thermistors 4](#_Toc14040015)

[2.3 Board 4](#_Toc14040016)

[2.3.1 Layout Constraints 4](#_Toc14040017)

[3 Testing 6](#_Toc14040018)

# Introduction

This document explains how the +X Panel will fulfil the following Functions and conform to the following Requirements. This document refers to the +X Panel version 1.0.

## Functions

The +X panel is responsible for the following:

* Bi-directional low gain antenna for communicating with the ground station (with deployment mechanism)
* GPS Patch Antenna
* Photodiode sensor for ADCS

## Requirements

The system requirements and Comms design requirements can be viewed [on GitHub](https://github.com/CougsInSpace/CougSat1-Readme/blob/master/CougSat1-Requirements.pdf).

# Detailed Description

This section references the +X Panel [schematic](https://github.com/CougsInSpace/CougSat1-Hardware/blob/master/CougSat1-RadioBoard/Documentation/%2BXPanel.pdf). Page numbers will be listed and may have coordinates listed (number and letter combination found around the frame).

## Functional Block Diagram

The block diagram can be found on the first page of the schematic.

### Low-Gain Antenna

The low-gain antenna is deployed via high-power resistors, thermal knife, burning through a monofilament. Once deployed, the low-gain antenna is used to transmit/receive data to/from the Ground. Transmission from this antenna consists of lower speed data transfers to the ground. Faster data rate transmissions, such as sending images, are handled by the high-gain antenna on the [-Z Panel](https://github.com/CougsInSpace/CougSat1-Hardware/blob/master/CougSat1-RadioBoard/Documentation/-ZPanel-Design.pdf).

### GPS Patch Antenna

The GPS antenna is used to receive GPS information for the attitude determination and control subsystem ([ADCS](https://github.com/CougsInSpace/CougSat1-Hardware/tree/master/CougSat1-AvionicBoard/Documentation)).

### Photodiode Sensor

The +X Panel board has a photodiode which is used to sense light intensity which pertains to the position of the sun. The ADCS uses an I2C ADC to read this intensity.

## Schematic

### Photodiode

The photodiode[[1]](#footnote-1) can be found on page 2 of the schematic. The photodiode is placed near the center of the board facing outward. This particular photodiode has a spectral sensitivity of and the expected illuminance is on the order of . There is a two-stage transimpedance amplifier on the output of the photodiode, which will amplify the input to a output.

### Low – Gain Antenna

The low-gain antenna that can be found on page 3 (B1), of the schematic is a half wave dipole: length of , impedance of , gain of , and linear polarization. It interfaces with the communications board via a coaxial cable (page 3, B2). As the coax is unbalanced and the antenna requires a balanced signal, a balun[[2]](#footnote-2) does this conversion. The antenna is deployed using a thermal knife (page 3, B4:B6) consisting of two resistors. When power is applied, they burn through a monofilament tied to the stowed antenna, releasing the antenna. The power for the thermal knife is provided by the EPS.

While stowed, the antenna is wrapped around metal pegs (page 3, C1:C4) shorting them together. Upon deployment, the short is removed. The EPS senses this change and can determine the deployment state of the antenna.

### GPS Patch Antenna

The GPS patch antenna (page 3, B3) [[3]](#footnote-3) is connected to the avionics board via a coaxial cable (page 3, B3). The antenna has an impedance of , a center frequency of , a bandwidth of for , and right-hand circular polarization. The antenna is compatible with GPS and GALILEO signals.

### Thermistors

The +X Panel contains seven thermistors[[4]](#footnote-4) (page 2, C4:C6). These will be used to record temperature data at various points of the +X Panel for monitoring purposes. The thermistor measurements are passed to the ADC on page 2 of the schematic where they are then read by the ADCS.

## Board

The board shall be double layered with copper and ENIG finish.

### Layout Constraints

Unless specified in the following subsections, all signals shall use the default parameters specified below. Signals in the following subsections do not include their sense signals unless specified. Trace width can be broken if a trace needs to bottleneck down to a pin, the bottleneck shall be minimized.

Trace Width:

Vias: , unlimited count

Separation:

Length: Unlimited

Devices with specific placement and routing considerations are noted in the schematic, see “CAD Note”.

#### I2C – I2C\_[SDA\_ADCS, SCL\_ADCS]

Length: Each node shall be length matched ±

Stubs: <

#### RF Traces – LOW\_GAIN\_[P, N, COAX], GPS\_COAX

Track Width:

Gap Width:

Impedance:

#### Power Resistors

Trace Width:

# Testing

All tests shall be performed at room temperature and not under vacuum unless otherwise specified. If any modifications are performed, take note. Include enough information to understand circuit behavior and for others to replicate the results. Include any software written to execute the test and link it in the test notes section. Save all software, waveforms, etc. in a subfolder of the board’s test folder for each test. All tests shall be performed prior to attaching the solar cells to the solar panel.

* Waveforms shall be captured whenever appropriate.
* Have the event take fill the screen (for fast events, zoom in, for slow events, zoom out)
* Label each channel accurately
* Only have bandwidth limiting if necessary for the test (this applies to the oscilloscope and probe settings)
* If ringing or overshoot occurs, use a ground spring or differential probe

Results location: https://github.com/CougsInSpace/CougSat1-Hardware/tree/master/CougSat1-RadioBoard/Testing/+XPanel.1.0

Common test instructions can be found on the [wiki](https://cis.vcea.wsu.edu/wiki/index.php/Cougs_in_Space_Wiki).

## Before First Power-On Check

This test is required to be executed before any external power is applied to the +x-panel.

### Test Instructions

Measure the resistance of various points in reference to PGND located at the output connector.

### Test Data

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Node | Resistance |  | Node | Resistance |
| AVREF\_UNBUF |  |  | 3.3V |  |
| AVREF |  |  | AVDD |  |
| I2C\_SDA\_ADCS |  |  | SENSE |  |
| I2C\_SCL\_ADCS |  |  | LOW\_GAIN\_COAX |  |
| PHOTODIODE\_IN-1 |  |  | LOW\_GAIN\_P |  |
| OUT1\_IN+2 |  |  | LOW\_GAIN\_N |  |
| PHOTODIODE\_IN-2 |  |  | GPS\_COAX |  |
| PHOTO |  |  | VCC |  |

### Test Notes

Delete me if no notes are required

1. CIS PN: [63-0003](https://github.com/CougsInSpace/Resources/tree/master/SupplierDocuments/66%20-%20Sensor/66-0003) [↑](#footnote-ref-1)
2. CIS PN: [39-0001](https://github.com/CougsInSpace/Resources/tree/cad-libraries/SupplierDocuments/39%20-%20Coils%20Misc/39-0001) [↑](#footnote-ref-2)
3. CIS PN: [65-0010](https://github.com/CougsInSpace/Resources/blob/master/SupplierDocuments/65%20-%20RF/65-0010/TAOGLAS_GP.1575.25.4.A.02.pdf) [↑](#footnote-ref-3)
4. CIS PN: [26-0008](https://github.com/CougsInSpace/Resources/blob/master/SupplierDocuments/26%20-%20Resistor%20Misc/26-0008/VISHAY_ntcs0603e3t.pdf) [↑](#footnote-ref-4)