

Category	Pin	Pin Name	Description
Power	2	SUP_3V3_0	Regulated 3.3V Supply
	12	SUP_3V3_1	
	21	SUP_3V3_2	
	22	SUP_3V3_3	
	42	SUP_3V3_4	
	52	SUP_3V3_5	
	62	SUP_5V0_0	Regulated 5.0V Supply
	71	SUP_5V0_1	
	91	SUP_UNREG_0	Unregulated supply
	111	SUP_UNREG_1	
	182	SUP_5VIN	External 5.0V input for charging via umbilical
	1	GND	Global ground potential
	41	GND	
	82	GND	
	131	GND	
	166	PWR_CTRL_SW	Disconnects battery (active low), driven by deployment switch and RBF pin
	170	PWR_CTRL_UMB	Overrides PWR_CTRL_SW for testing in launch mechanism (active low), driven by umbilical switch
Bus	169	BUS_I2C0_SDA	I2C interfaces for bidirectional data exchange among high priority devices
	173	BUS_I2C0_SCL	
	177	BUS_I2C0_IRQ	
	162	BUS_I2C1_SDA	I2C interfaces for bidirectional data exchange among low priority devices
	162	BUS_I2C1_SCL	
	164	BUS_I2C1_IRQ	
	185	BUS_JTAG_TCK	Shared JTAG interface for subsystem software update via IHU. Max 1 device at a time, normally disconnected
	189	BUS_JTAG_TDI	
	193	BUS_JTAG_TDO	
	197	BUS_JTAG_TMS	
	141	BUS_SPI_MOSI	SPI interface to subsystem or direct access to remote serial device
	145	BUS_SPI_MISO	
	149	BUS_SPI_SCK	
	181	PMIC_I2C_SDA	I2C interfaces for bidirectional data exchange among primary and secondary PMIC
	183	PMIC_I2C_SCL	
Ctrl	174	CTRL_RESET	Global reset (active low), <i>normal high</i>
	178	CTRL_SYNC	Global synchronization signal, driven by IHU
Comms	142	COM_USART_TX	Serial interface for dedicated communication with the RCS
	144	COM_USART_RX	
	150	COM_USART_CLK	
	154	COM_USART_IRQ	
GPIO	153	GPIO_153	
	155	GPIO_155	
	157	GPIO_157	
	158	GPIO_158	
	159	GPIO_159	SPI_EN_CAM0
	161	GPIO_161	JTAG_SEL_RCS
	163	GPIO_163	JTAG_SEL_ADCS
	165	GPIO_165	JTAG_SEL_PMIC0
	167	GPIO_167	JTAG_SEL_PMIC1