## **PH-ATM - ATMS Hardware Design Document**

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	Business Identifier	DTC	Revis
	<u> </u>	DTC	Davida
SNMP	Simple Network Management Protocol		
SNM	Safety NETS Manager		
SPS	Search and Planning System		
SGW	Sensor GateWay		
SFP	Small Form Pluggable		
SATA	Serial Advanced Technology Attachment		
SAS	Serial Attached SCSI		
S			
RTP	Radar Track Processing		
RVM	Requirements Verification Matrix		
R			
Q	. Suprisia Camputor interesting tapic	· <del></del>	
PCIe	Peripheral Computer Interconnect Expre	ess	
PCI	Peripheral Computer Interconnect		
PLC	Planner Controller		
PIL	Pilot		
PFDP	Partition Flight Data Processor		
PDG	Pseudo Data Generator		
<b>P</b>	Special Stopics Conto		
ODS	Operational Display Suite		
0	Notwork Time Flotocol		
NTP	Network Time Protocol		
N/A	Not Applicable		
N NAS	Network Attached Storage		
MTP <b>N</b>	WIGHT-SCHSOL FLACK FLOCESSOL		
MST	Multi-sensor Tracking  Multi-sensor Track Processor		
MON	Multi-Sensor Tracking		
MDPS	Meteorological Data Processing System Monitoring		
MDDC	Motocrological Data Daga and a Conta		
LDR	Leader		
LAN	Local Area Network		
L	Lacal Area Nationali		
KVM	Keyboard Video Mouse		
K	Maybaard Widoo Maysa		
JPDP	Journaling and Playback Data Processing		
JADE	Java Assisted Design Environment		
J	lava Assistad Dasian Environment		
ISR	Integrated Services Router		
ISO	International Standards Organisation		
IPS	Intrusion Protection System		
IP	Internet Protocol		
	Interface Control Document		
ICD	Interfere Control Deciman		

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