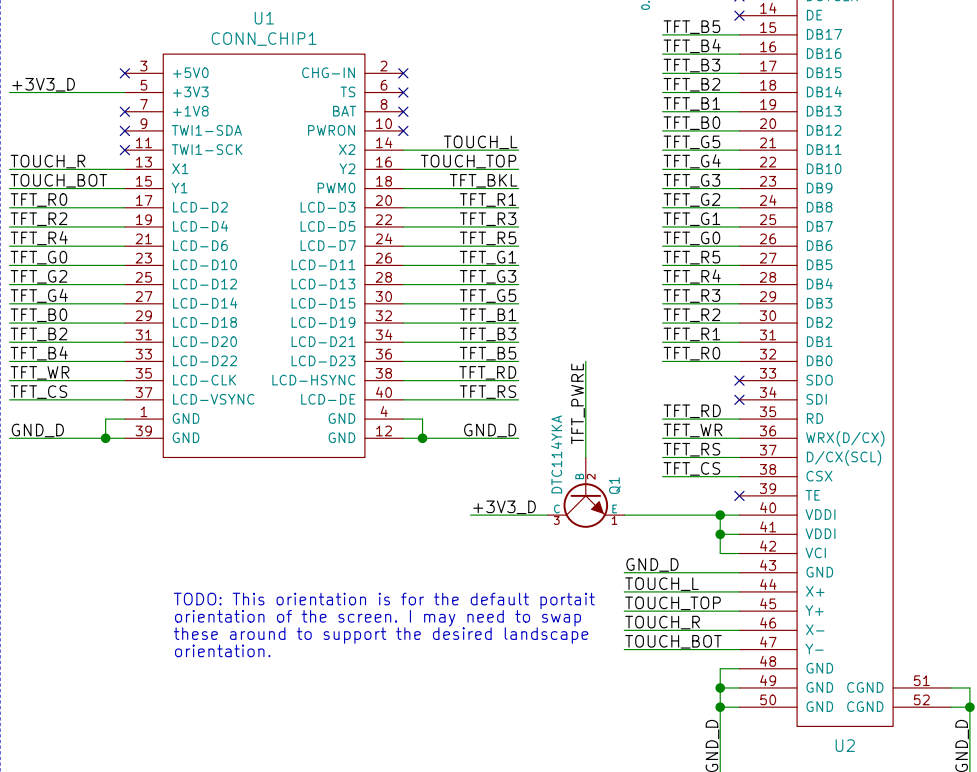


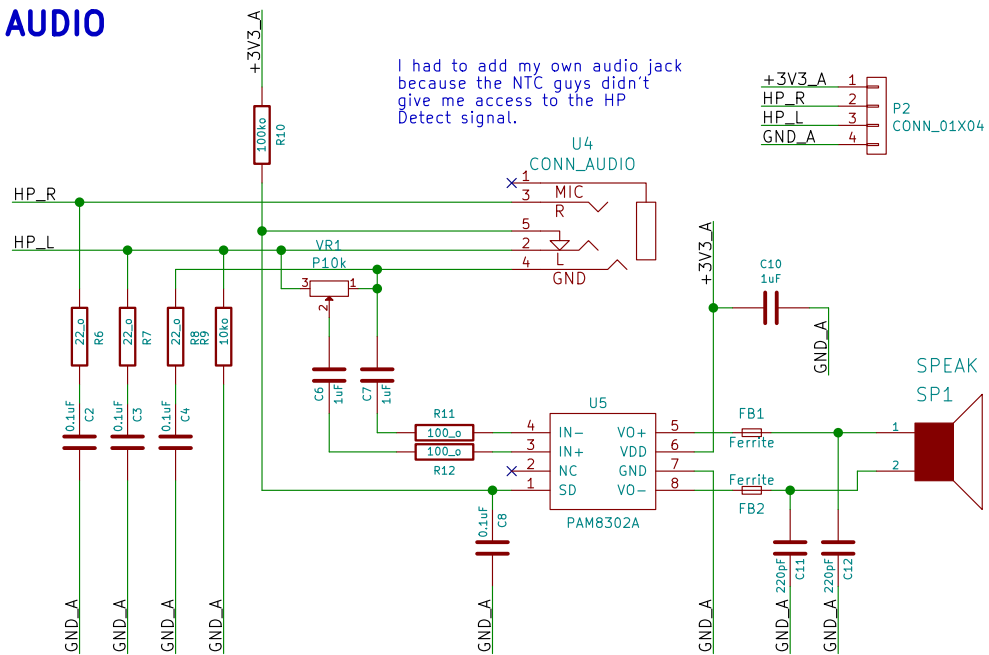
## DISPLAY

Note: I could have used +5V0 here with 100 $\Omega$  resistors for better accuracy, but I went with +3V3 with 4.99 $\Omega$  resistors for increased efficiency. If the +3V3 rail cannot support that much current, then I will change this later.

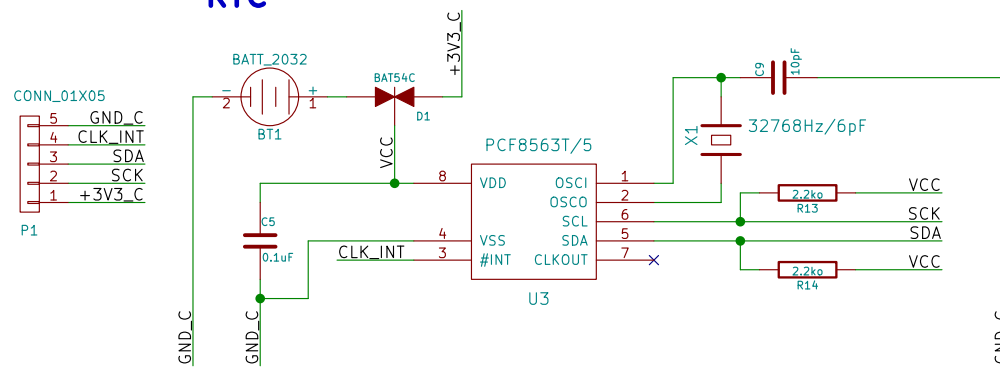
Note: Setting `[IM3,IM0] = 0011` configures the display for 8080 MCU 18-bit interface I. I may need to change this during testing to the 8080 MCU 18-bit interface II but once I have the right interface, it should be hard wired.



## AUDIO



## RTC



Prime8Bit

Sheet: /

File: Marmoset\_Breakout.sch

**Title: Marmoset**

Size: A4

Date: 21 feb 2015

KiCad E.D.A. kicad (2016-05-30 BZR 6864, Git 09dc517)-product

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