

THOMAS SERPINIS
cr0wsplace.com

MATRIX UNLOADED

ESCAPING THE ITAG REALITY

THOMAS SERPINIS
Ecr0wtom

Who am I

- Thomas Serpinis (@cr0wtom)
 - Technical Director by Day @ Auxilium Pentest Labs
 - Security Researcher by Night @ Cr0w's Place
 - Hack Everything, Everywhere, All at Once (and Legally)
- Major infosec complainer
- Movie and pop-culture aficionado
- For more: cr0wsp0lace.com



10 YEARS IN THE INDUSTRY

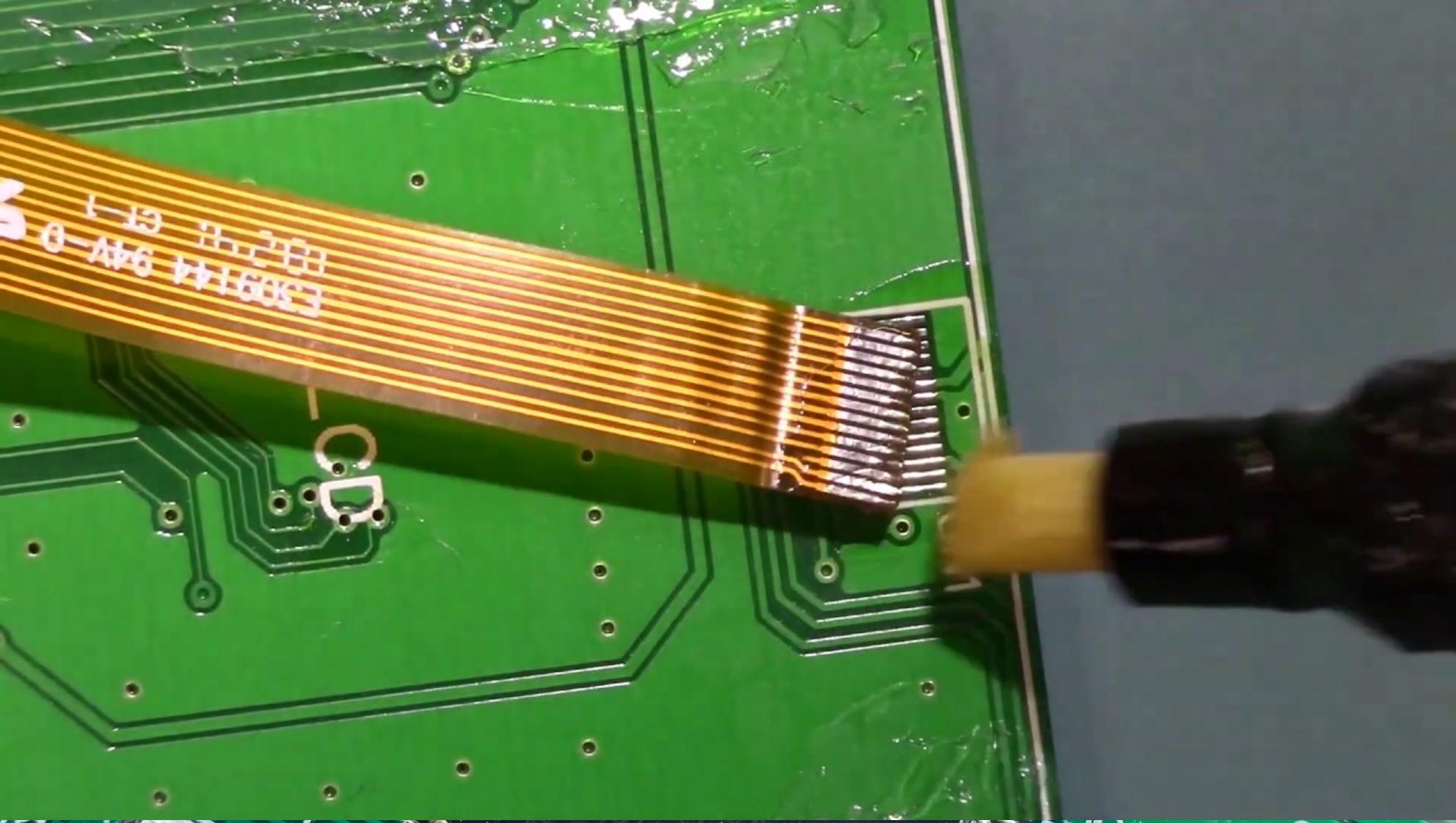
what is this talk about

- Analyze the state of cyber security in the automotive industry
- Talk about all the stupid things the automotive industry is still doing – from a hardware point of view
 - With a sprinkle of forensics in the mix
- Educate the new, the old and the bold
- Lure more hackers to automotive and hardware security
- Raise and highlight the significance of safety related hardware

The State

the state

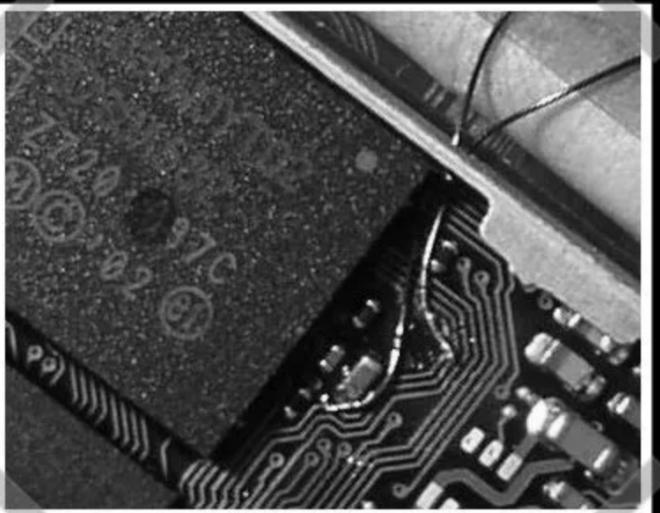




FREE YOUR IPHONE

August 26, 2007 by Will O'Brien

25 Comments



I wasn't going to post [this](#) – it's a freakin phone after all. But I've gotten quite a few tips on it, and I'd like them to end. [George] made a concerted effort to hack the [iPhone](#) – and it paid off. After his crazy [ebay auction](#) that topped out at 99,999,999.99 last time I checked, he ended up trading his first phone for a Nissan 350z and a few more iPhones.

He documented his process, step by step – if you've got the skills, you can probably do it yourself. The soldering work is damn fine work – probably the hardest thing there is. The write up is a little hard to follow, so plan on taking some time to comprehend everything. (Blogging software isn't the best way to organize how-tos, trust me on this.) My hats off to [George], he did some great work. – So, why didn't I want to post it? All this work yielded one thing: carrier choice for the iPhone.

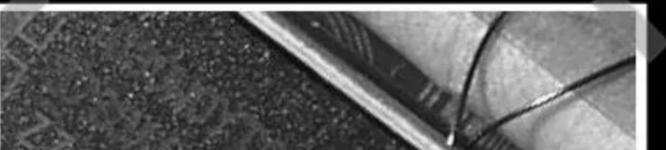
Posted in [handhelds hacks](#), [iphone hacks](#), [ipod hacks](#), [Misc Hacks](#)

Tagged [hack](#), [iphone](#), [jtag](#)

FREE YOUR IPHONE

August 26, 2007 by Will O'Brien

25 Comments



Bypassing the Renesas RH850/P1M-E read protection using fault injection

Nov 8, 2022

Using fault injection to bypass the disabled programmer setting on a Renesas RH850/P1M-E automotive microcontroller and access flash memory contents.



Willem Melching

[Read more →](#)

The documented his process, step by step – if you've got the skills, you can probably do it yourself.

The soldering work is damn fine work – probably the hardest thing there is. The write up is a little hard to follow, so plan on taking some time to comprehend everything. (Blogging software isn't the best way to organize how-tos, trust me on this.) My hats off to [George], he did some great work. – So, why didn't I want to post it? All this work yielded one thing: carrier choice for the iPhone.

Posted in [handhelds hacks](#), [iphone hacks](#), [ipod hacks](#), [Misc Hacks](#)

Tagged [hack](#), [iphone](#), [jtag](#)



~~Overall~~ The Problem

Ubiquitous Hardware Vulnerabilities in Automotive



January 22-24, 2025
Tokyo, Japan

REGISTER NOW

The Automotive Cybersecurity Event
at the Forefront of Zero-Day Vulnerability Discovery



Target	Cash Prize	Master of Pwn Points
ChargePoint Home Flex (Model CPH50)	\$50,000	5
Phoenix Contact CHARX SEC-3150	\$50,000	5
WOLFBOX Level 2 EV Charger	\$50,000	5
EMPORIA EV Charger Level 2	\$50,000	5
Tesla Wall Connector	\$50,000	5
Autel MaxiCharger AC Wallbox Commercial (MAXI US AC W12-L-4G)	\$50,000	5
Ubiquiti Connect EV Station	\$50,000	5





Hardware Debugging Interfaces

- Direct, low-level access to processor and memory

Hardware Debugging Interfaces

- Direct, low-level access to processor and memory

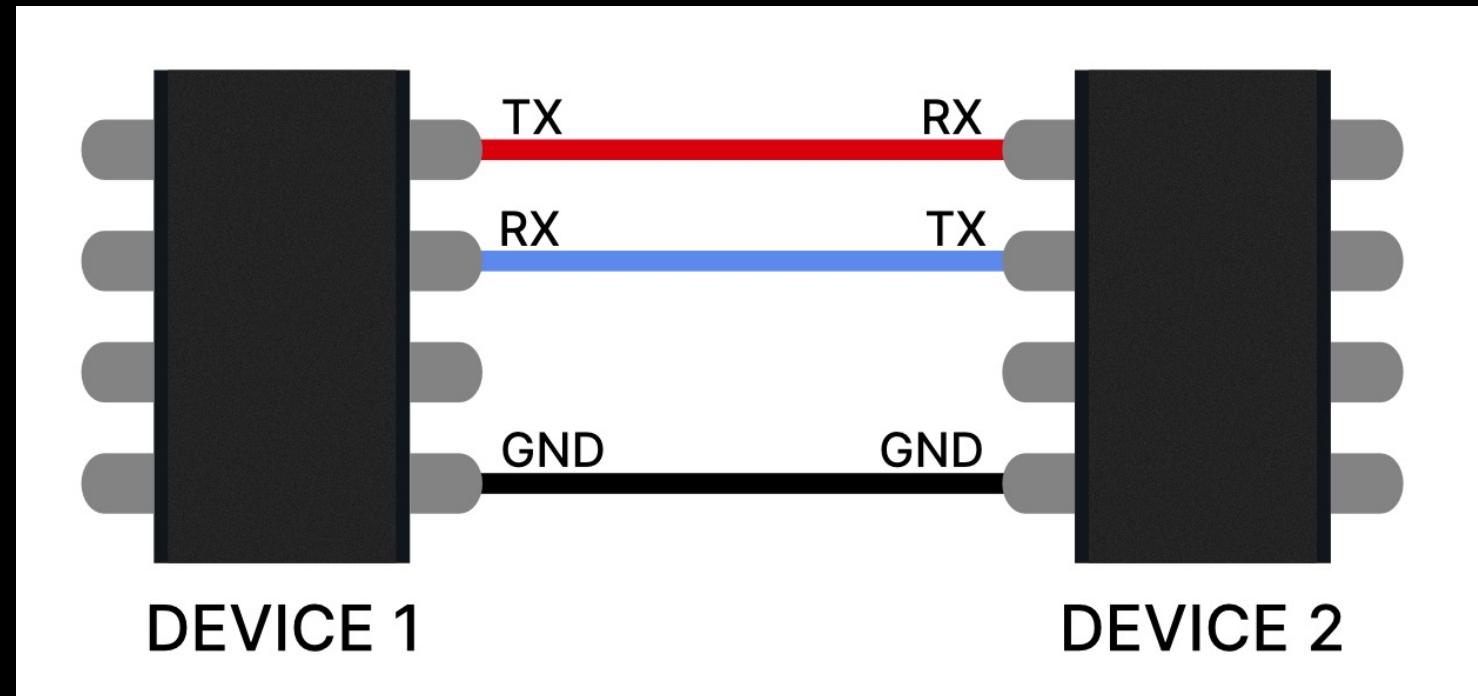
Interface	Purpose
JTAG	Full debug access
SWD	ARM-specific, lightweight JTAG alternative
UART	Serial console
SPI/I ² C	Internal buses

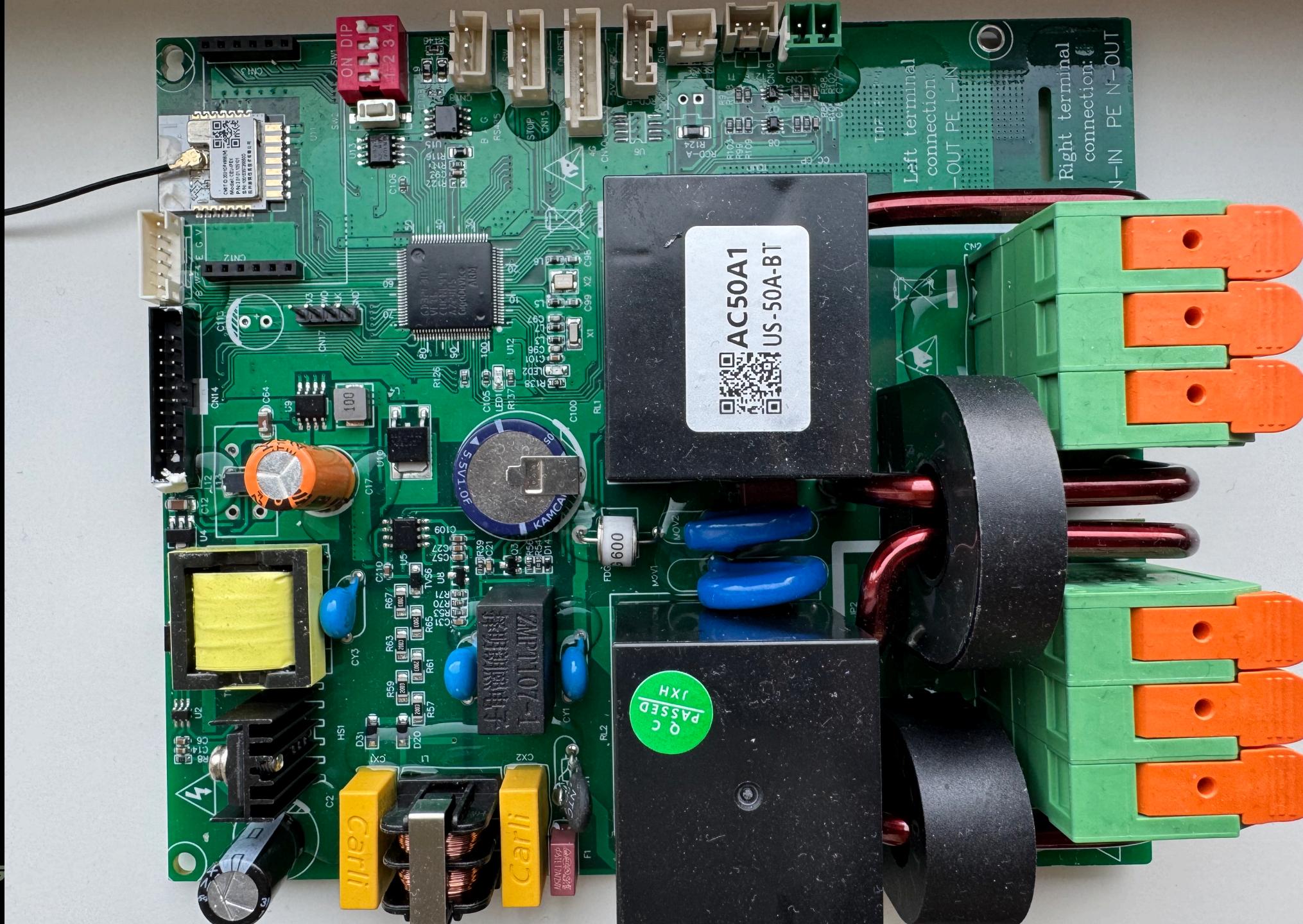
Hardware Debugging Interfaces

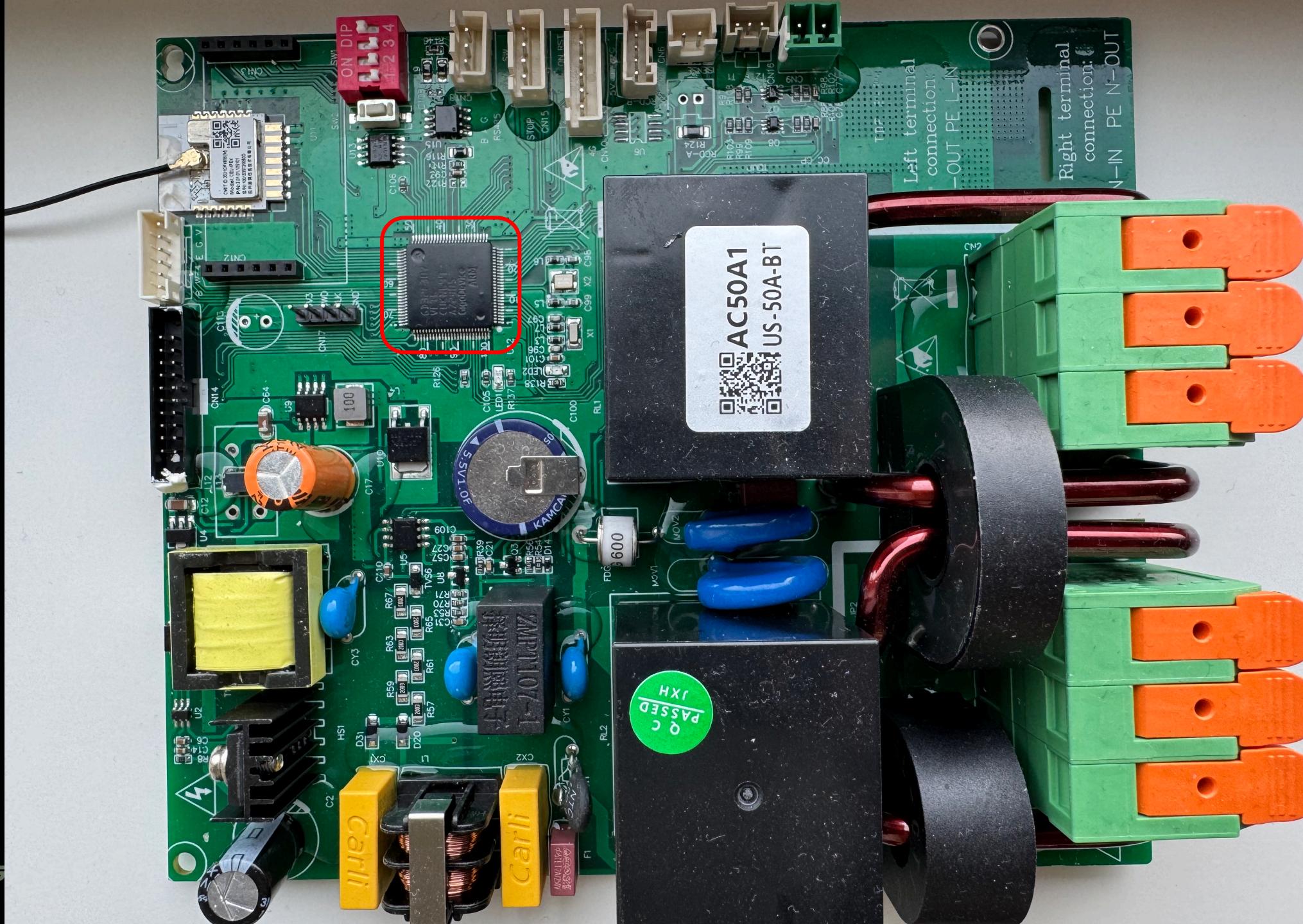
ARM 10-PIN Interface	ST 14-PIN Interface	OCDS 16-PIN Interface	ARM 20-PIN Interface
VCC 1	□ □	IJEN 1	□ □
GND 3	□ □	GND 3	□ □
GND 5	□ □	TDI 5	□ □
RTCK 7	□ □	VCC 7	□ □
GND 9	□ □	TMS 9	□ □
	2 TMS	TDI 11	□ □
	4 TCLK	TDO 13	□ □
	6 TDO	8 /RST	2 VCC (optional)
	8 TDI	10 GND	VCC 1
	10 RESET	12 GND	4 GND
		14 /TERR	TRST 3
		TRST 9	TDI 5
		TCLK 11	6 GND
		BRKIN 13	8 GND
		TRAP 15	RESET 7
		12 GND	TMS 9
		14 OCDSE	TCLK 9
		16 GND	10 GND
		N/C 17	12 GND
		N/C 19	14 GND
			16 GND
			18 GND
			20 GND

Different Types of JTAG Pinouts

Hardware Debugging Interfaces









GigaDevice Semiconductor Inc.

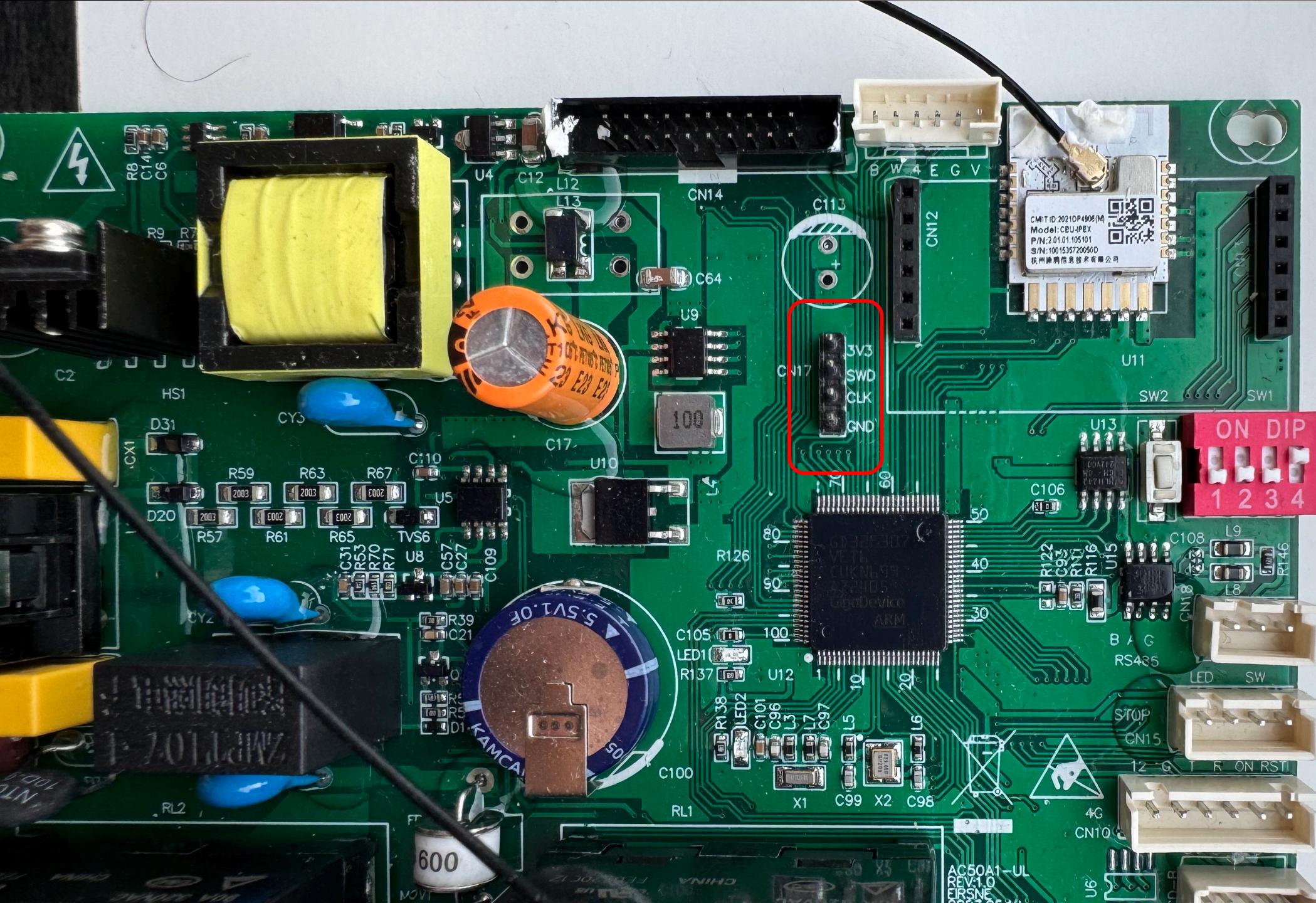
GD32F307xx
Arm® Cortex®-M4 32-bit MCU

Datasheet

Revision 1.6

(Sep. 2023)







ON DIP
1 2 3 4

TDF 2xN2x1
Left terminal
connection:
L-OUT PE L-IN
Right terminal
connection:
N-N PE N-OUT

AC50A1
US-50A-B

MAX10
JAFH

600

100

100

100

100

100

100

100

100

Target

www.segger.com

j-link^{Pro}

LAN

USB

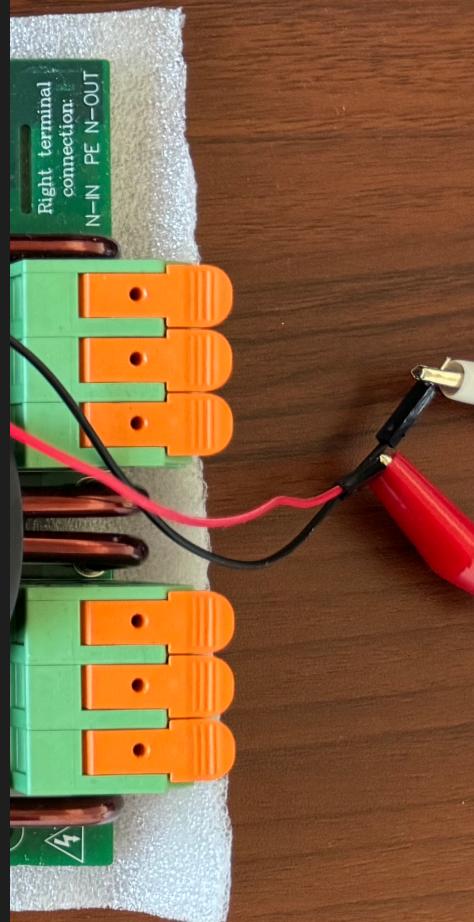
```
① JLinkExe
SEGGER J-Link Commander V8.10a (Compiled Oct 2 2024 14:14:28)
DLL version V8.10a, compiled Oct 2 2024 14:14:09

Connecting to J-Link via USB...O.K.
Firmware: J-Link Pro V5-1 compiled Dec 10 2024 10:54:56
Hardware version: V5.10
J-Link uptime (since boot): 0d 00h 00m 49s
S/N: 175104598
License(s): RDI, FlashBP, FlashDL, JFlash, GDB
USB speed mode: High speed (480 MBit/s)
IP-Addr: DHCP (no addr. received yet)
VTref=3.300V (fixed)

Type "connect" to establish a target connection, '?' for help
J-Link>|
```



```
| Device "CORTEX-M4" selected.  
  
Connecting to target via SWD  
Found SW-DP with ID 0x2BA01477  
| DPIDR: 0x2BA01477  
CoreSight SoC-400 or earlier  
Scanning AP map to find all available APs  
AP[1]: Stopped AP scan as end of AP map has been reached  
AP[0]: AHB-AP (IDR: 0x24770011, ADDR: 0x00000000)  
Iterating through AP map to find AHB-AP to use  
AP[0]: Core found  
AP[0]: AHB-AP ROM base: 0xE00FF000  
CPUID register: 0x410FC241. Implementer code: 0x41 (ARM)  
Found Cortex-M4 r0p1, Little endian.  
FPUnit: 6 code (BP) slots and 2 literal slots  
CoreSight components:  
ROMTbl[0] @ E00FF000  
[0][0]: E000E000 CID B105E00D PID 000BB00C SCS-M7  
[0][1]: E0001000 CID B105E00D PID 003BB002 DWT  
[0][2]: E0002000 CID B105E00D PID 002BB003 FPB  
[0][3]: E0000000 CID B105E00D PID 003BB001 ITM  
[0][4]: E0040000 CID B105900D PID 000BB9A1 TPIU  
[0][5]: E0041000 CID 00000000 PID 00000000 ???  
Memory zones:  
Zone: "Default" Description: Default access mode  
Cortex-M4 identified.  
J-Link>
```





Device
Connect
Found S
DPIDR:
CoreSig
Scannin
AP[1]:
AP[0]:
Iterati
AP[0]:
AP[0]:
CPUID r
Found C
FPUunit:
CoreSig
ROMTbl[
[0][0]:
[0][1]:
[0][2]:
[0][3]:
[0][4]:
[0][5]:
Memory
Zone:
Cortex-
J-Link>

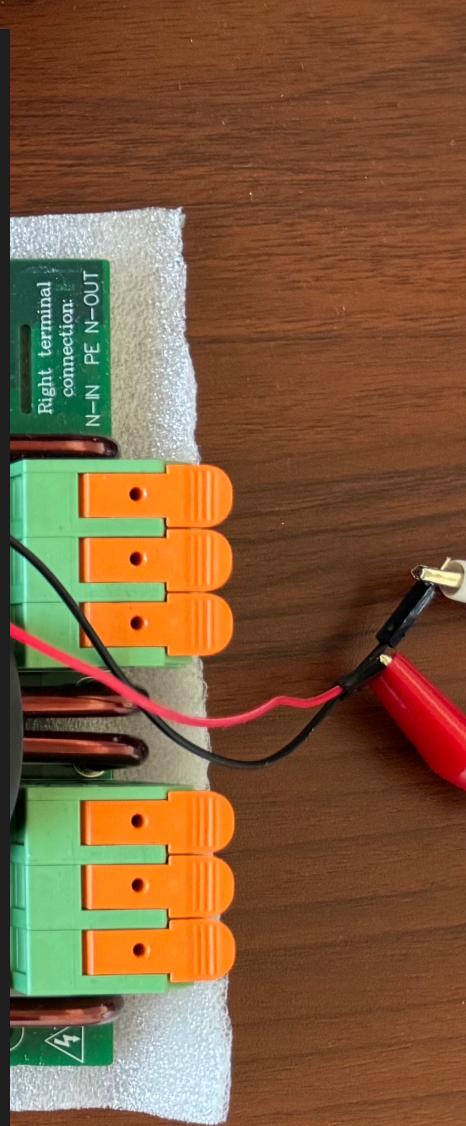
SRAM	AHB	0x2007 0000 - 0x3FFF FFFF	Reserved
		0x2006 0000 - 0x2006 FFFF	Reserved
		0x2003 0000 - 0x2005 FFFF	Reserved
		0x2001 8000 - 0x2002 FFFF	Reserved
		0x2000 0000 - 0x2001 7FFF	SRAM
Code	AHB	0x1FFF F810 - 0x1FFF FFFF	Reserved
		0x1FFF F800 - 0x1FFF F80F	Option Bytes
		0x1FFF F000 - 0x1FFF F7FF	Boot loader
		0x1FFF C010 - 0x1FFF EFFF	
		0x1FFF C000 - 0x1FFF C00F	
		0x1FFF B000 - 0x1FFF BFFF	
		0x1FFF 7A10 - 0x1FFF AFFF	Reserved
		0x1FFF 7800 - 0x1FFF 7A0F	Reserved
		0x1FFF 0000 - 0x1FFF 77FF	Reserved
		0x1FFE C010 - 0x1FFE FFFF	Reserved
		0x1FFE C000 - 0x1FFE C00F	Reserved

15



GD32F307xx Datasheet

Pre-defined Regions	Bus	Address	Peripherals
		0x1001 0000 - 0x1FFE BFFF	Reserved
		0x1000 0000 - 0x1000 FFFF	Reserved
		0x083C 0000 - 0x0FFF FFFF	Reserved
		0x0830 0000 - 0x083B FFFF	Reserved
		0x0810 0000 - 0x082F FFFF	Reserved
		0x0800 0000 - 0x080F FFFF	Main Flash
		0x0030 0000 - 0x07FF FFFF	Reserved
		0x0010 0000 - 0x002F FFFF	Aliased to Main Flash or Boot loader
		0x0002 0000 - 0x000F FFFF	
		0x0000 0000 - 0x0001 FFFF	



Device				
Connect				
Found S				
DPIDR:				
CoreSig				
Scannin				
AP[1]:				
AP[0]:				
Tterati				
	SRAM	AHB	0x2007 0000 - 0x3FFF FFFF	Reserved
			0x2006 0000 - 0x2006 FFFF	Reserved
			0x2003 0000 - 0x2005 FFFF	Reserved
			0x2001 8000 - 0x2002 FFFF	Reserved
			0x2000 0000 - 0x2001 7FFF	SRAM
	Code	AHB	0x1FFF F810 - 0x1FFF FFFF	Reserved
			0x1FFF F800 - 0x1FFF F80F	Option Bytes
			0x1FFF F000 - 0x1FFF F7FF	
			0x1FFF C010 - 0x1FFF EFFF	
			0x1FFF C000 - 0x1FFF C00F	Boot loader
			0x1FFF B000 - 0x1FFF BFFF	
			0x1FFF 7A10 - 0x1FFF AFFF	Reserved
			0x1FFF 7800 - 0x1FFF 7A0F	Reserved
			0x1FFF 0000 - 0x1FFF 77FF	Reserved
			0x1FFE C010 - 0x1FFE FFFF	Reserved



```
[J-Link>savebin dump.bin, 0x08000000, 0x100000
Opening binary file for writing... [dump.bin]
Reading 1048576 bytes from addr 0x08000000 into file...O.K.
J-Link>
```

```
[0][0]:
[0][1]:
[0][2]:
[0][3]:
[0][4]:
[0][5]:
Memory
Zone:
Cortex-
J-Link>
```

GigaDevice

GD32F307xx Datasheet

Pre-defined Regions	Bus	Address	Peripherals
		0x1001 0000 - 0x1FFE BFFF	Reserved
		0x1000 0000 - 0x1000 FFFF	Reserved
		0x083C 0000 - 0x0FFF FFFF	Reserved
		0x0830 0000 - 0x083B FFFF	Reserved
		0x0810 0000 - 0x082F FFFF	Reserved
		0x0800 0000 - 0x080F FFFF	Main Flash
		0x0030 0000 - 0x07FF FFFF	Reserved
		0x0010 0000 - 0x002F FFFF	Aliased to Main Flash or Boot loader
		0x0002 0000 - 0x000F FFFF	
		0x0000 0000 - 0x0001 FFFF	



Tools Window Help

0x00d3fd1	63	??	63h	c
0x00d3fd2	3a	??	3Ah	:
0x00d3fd3	25	??	25h	%
0x00d3fd4	73	??	73h	s
0x00d3fd5	00	??	00h	
0x00d3fd6	66 77 2d	ds	"fw->sw_ver:%s"	
	3e 73 77			
	5f 76 65 ...			
0x00d3fe4	66 77 2d	ds	"fw->file_size:%d"	
	3e 66 69			
	6c 65 5f ...			
0x00d3ff5	73 65 6e	ds	"send syn err:%d"	
	64 20 73			
	79 6e 20 ...			
0x00d4005	75 70 67	ds	"upgrade_start_cb fault:%d"	
	72 61 64			
	65 5f 73 ...			
0x00d401f	73 74 61	ds	"start tuya_iot_upgrade_dev"	
	72 74 20			
	74 75 79 ...			
0x00d403a	74 75 79	ds	"tuya_iot_upgrade_dev err:%d"	
	61 5f 69			
	6f 74 5f ...			
0x00d4056	70 72 65	ds	"pre_gw_ug_inform_cb"	
	5f 67 77			
	5f 75 67 ...			
0x00d406a	6d	??	6Dh	m
0x00d406b	63	??	63h	c
0x00d406c	75	??	75h	u
0x00d406d	2e	??	2Eh	.
0x00d406e	76	??	76h	v
0x00d406f	65	??	65h	e
0x00d4070	72	??	72h	r
0x00d4071	00	??	00h	
0x00d4072	67 65 74	ds	"get map seession id fault:%d"	
	20 6d 61			
	70 20 73 ...			
0x00d408e	67 65 74	ds	"get seession id :%d"	
	20 73 65			
	65 73 69 ...			

Defined Strings - 3929 items			
Location	String Value	String Representation	Data Type
0x00d401f	start tuya_iot_upgrade_dev	"start tuya_iot_upgrade_d..."	ds
0x00d403a	tuya_iot_upgrade_dev err:%d	"tuya_iot_upgrade_dev err..."	ds
0x00d4056	pre_gw_ug_inform_cb	"pre_gw_ug_inform_cb"	ds
0x00d4072	get map seession id fault:%d	"get map seession id fault:..."	ds
0x00d408e	get seession id :%d	"get seession id :%d"	ds
0x00d40a1	mul map ver err %d	"mul map ver err %d"	ds
0x00d40b4	mul map handle err %d	"mul map handle err %d"	ds
0x00d40ca	stream end err %d	"stream end err %d"	ds
0x00d40dc	data len err %d	"data len err %d"	ds
0x00d40ec	stream trans err %d	"stream trans err %d"	ds
0x00d4100	free mem %d is not enough	"free mem %d is not enou..."	ds
0x00d411a	stream start err %d	"stream start err %d"	ds
0x00d412e	stream server task init err %d	"stream server task init err..."	ds
0x00d4161	dp %d not found	"dp %d not found"	ds
0x00d4171	dp %d type invalid %d	"dp %d type invalid %d"	ds
0x00d4187	dp %d len invalid %d	"dp %d len invalid %d"	ds
0x00d419c	dp %d data invalid %d	"dp %d data invalid %d"	ds
0x00d41b2	dp report err %d	"dp report err %d"	ds
0x00d41ca	dev_ctrl is NULL	"dev_ctrl is NULL"	ds
0x00d41db	dp_ctrl->dp_desc.mode is M_RO	"dp_ctrl->dp_desc.mode ..."	ds
0x00d41f9	dp type is err (not obj_data)	"dp type is err (not obj_dat..."	ds
0x00d4217	dp_ctrl is NULL	"dp_ctrl is NULL"	ds
0x00d4227	state err:%d	"state err:%d"	ds
0x00d4234	close lowpower	"close lowpower"	ds
0x00d4243	open lowpower	"open lowpower"	ds
0x00d4251	sta is:%d---lp op err:%d	"sta is:%d---lp op err:%d"	ds
0x00d426b	nw stat err:%u	"nw stat err:%u"	ds
0x00d427a	ir stat:%d.wifi stat:%d.work hiah:%d	"ir stat:%d.wifi stat:%d.w..."	ds

Filter:

Decompiler

Defined Strings



Tools Window Help

000d3fd1	63	??	63h
000d3fd2	3a	??	3Ah
000d3fd3	25	??	25h

Defined Strings - 3929 items			
Location	String Value	String Representation	Data Type
000d401f	start tuya_iot_upgrade_dev	"start tuya_iot_upgrade_d..."	ds

TuyaOS

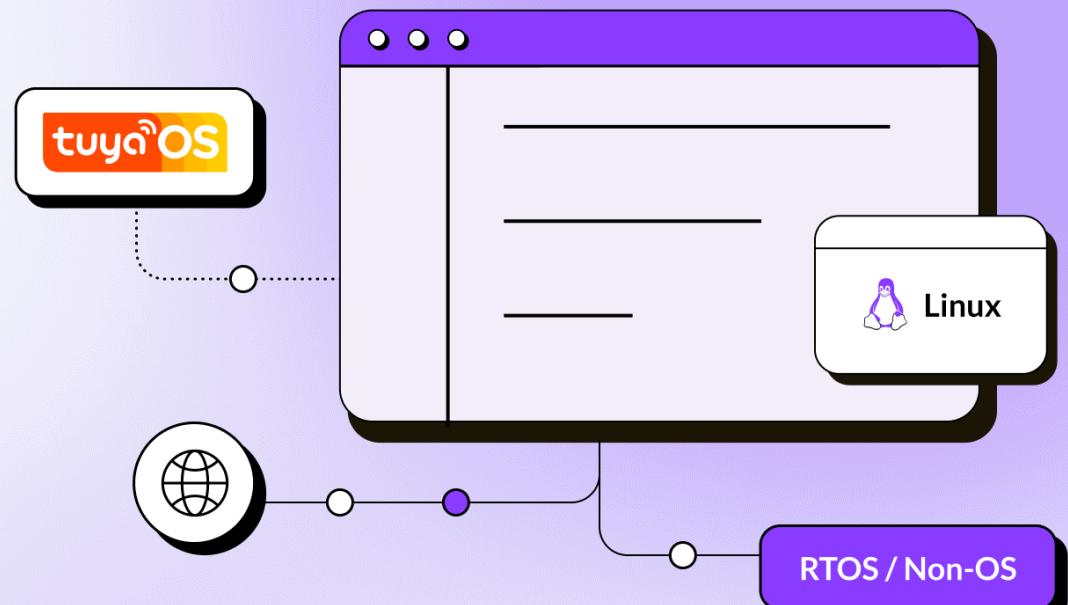
Built atop RTOS, Linux, and Non-OS, TuyaOS is a distributed and platform-agnostic IoT operating system.

[Get Started >](#)

000d4071	00	??	00h
000d4072	67	65 74	ds "get map seesion id fault:%d"
	20	6d 61	
	70	20 73 ...	

000d408e	67	65 74	ds "get seession id :%d"
	20	73 65	
	65	73 69 ...	

000d4251	sta is:%d---lp op err:%d	"sta is:%d---lp op err:%d" ds
000d426b	nw stat err:%u	"nw stat err:%u" ds
000d427a	ir stat:%d.wifi stat:%d.work hiah:%d	"ir stat:%d.wifi stat:%d.w... ds
Filter:		
C Decomplier	Defined Strings	x



Application		Product Capabilities															
		Electrical Products		Lighting Products		Home Appliances		Robots		Gateways		IP Cameras		Sensors		Wearables	
Service	Networking Subsystem				Gateway/Control Hub Subsystem				Multimedia Subsystem			Third-Party Service Subsystem		Sub-Device Subsystem			
	Device Manager	Network Config	Device Activation	Device Authentication	Local Linkage	Scene Group	Local Control	Alert Services	AI Algorithms	Alexa FFS	Alexa AFI	Bluetooth Low Energy					
	Health Manager	Control Manager	OTA	Scenes	Failover	Voice Services	Security Services	Streaming Services	Local Storage	Google LHC	Chromecast	Bluetooth Mesh					
	Low Power	FFC	Flash Provisioning	Security Protection	Engineering Mode	Sub-Device Manager	GUI Frameworks	Low Power Keep-Alive	Cloud Storage	HomeKit	Miracast	Beacon					
	Software Bus	Network Manager	Device Model	Others	Galaxy Link	Hot Update Engine	Others	Codec	URTC	Matter	DLNA	Sub-GHz Mesh	Zigbee Cluster Library				
	IoT Core Engine													Matter over Thread			
	Tuya IoT Core			AWS IoT Core			Azuer IoT Hub			Others							
	Generic Network Protocols				Multimedia Protocol Family				Miscellaneous								
Library	MQTT	HTTP	LwM2M	mDNS	P2P	WebRTC	RTSP/RTP	cJSON	SQLite	QR Code	FFmpeg						
Library	WebSocket	CoAP	mbed TLS	Others	SIP	ONVIF	Others	Lua Engine	Python Engine	LVGL GUI	Others						
Abstraction	System Services					Connectivity Protocol Family			Multimedia		Security Framework						
	Event Queue	Message Queue	Schedule Queue	Logging System	Events	Ethernet	Wi-Fi	Cellular	Video	Audio	Security Algorithms						
Abstraction	Time/Time Zone	Thread Manager	Secure Storage	RPC	Memory Manager	Bluetooth	Thread	Zigbee	Display	Others	Security Engine						
	TuyaOS API												Tools				
Kernel	System Interfaces			OS Drivers				Generic Drivers				Management Tools					
	Linux/RTOS/Non-OS			Ethernet/Wi-Fi/Bluetooth/Zigbee/Thread/Cellular/...				Video/Audio /Display		PWM	ADC	DAC	GPIO				
	Arch		Glibc/uClibc/Newlib						Interrupt	Timer	I2C	Others	Testing Tools				

WOLFBOX
Drive on. Always on.

Smart Home EV Charger

Power Up Your Drive



7X Fast
Charging



38miles
1 Hour



APP
Control



24/7
Online Support



Wi-Fi
Bluetooth



CSA
Certification



ENERGY
STAR



LCD
Screen



BK7231T COB Hardware Design Guideline

Last Updated on : 2024-06-24 08:01:48

 download

This topic describes hardware design guidelines on development based on the BK7231T Wi-Fi chip.

Application scope

BK7231T is a highly integrated wireless and radio frequency (RF) chip, with a built-in Wi-Fi protocol stack and various library functions. It features a low-power 32-bit CPU, 1T1R WLAN, 256 kbit static RAM, 2 MB flash, and various peripherals. As a real-time operating system (RTOS), BT7231T integrates all Wi-Fi MAC and TCP/IP libraries. You can develop your own embedded Wi-Fi products.

Features

- Built-in low-power 32-bit CPU, 256 kbit static RAM, and 2 MB flash.
- The maximum clock speed is 120 MHz.
- Full functionality of IEEE 802.11b/g/n Wi-Fi connectivity.
- Integrated Bluetooth Low Energy (LE) system.
- The maximum output power is +17 dBm for 802.11b transmission.
- Various peripheral interfaces include PWM, I2C, UART, SPI, SDIO, USB, and IrDA.
- Operating voltage: 3.0V–3.6V
- Operating temperature: -40°C–105°C

Minimal system diagram

README

MIT license



Tuya Cloudcutter

This repository contains the toolchain to exploit a wireless vulnerability that can jailbreak some of the latest smart devices built with the bk7231 chipset under various brand names by Tuya. The vulnerability as well as the exploitation tooling were identified and created by [Khaled Nassar](#) and [Tom Clement](#) with support from [Jilles Groenendijk](#).

Our tool disconnects Tuya devices from the cloud, allowing them to run completely locally. Additionally, it can be used to flash custom firmware to devices over-the-air.

 Do you like this tool? Please consider giving it a star on Github so it reaches more people. 

Minimal system diagram 

README

MIT license



Tuya Cloudcutter

```
- not found!
[+] Searching for known exploit patterns
=====
[!] The binary supplied appears to be patched and no longer vulnerable to the tuya-cloudcutter exploit.
=====
[!] Storage file not found
[+] Device class: bk7231n_common_user_config_ty
[+] Version: 3.1.5
```

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Minimal system diagram

README

Tuya

- nc
- [+] Searchin
- [!] The bina
- [!] Storage
- [+] Device c
- [+] Version:

Our tool di
used to fla

i Do you

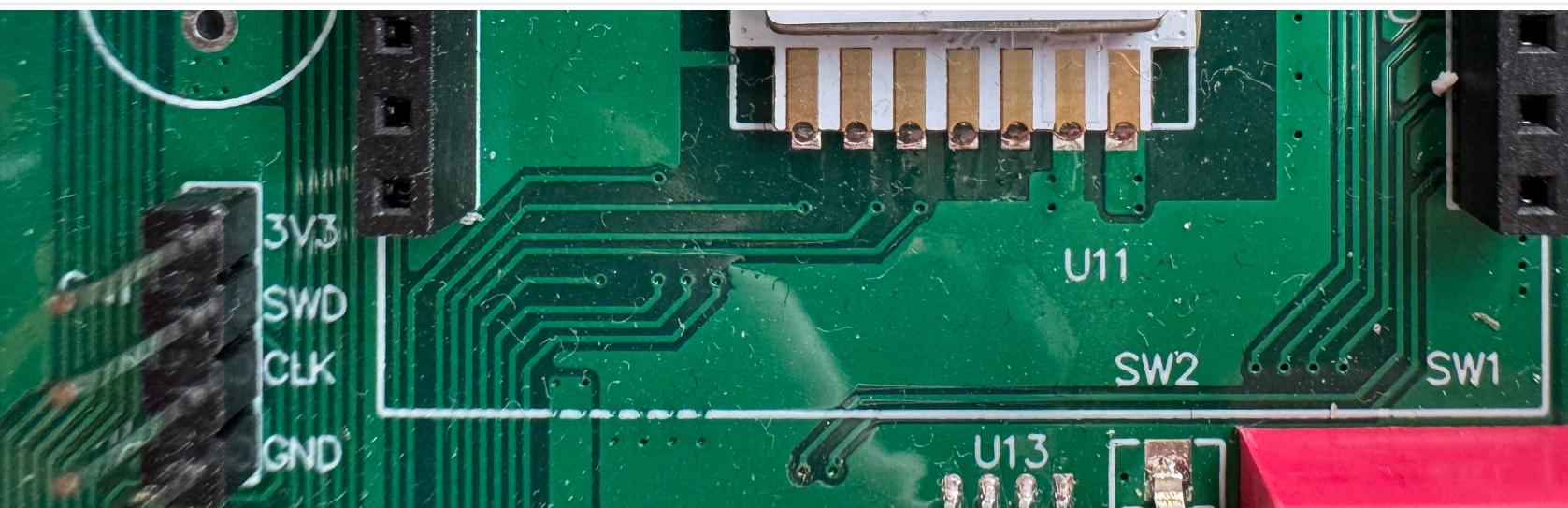


README



6 RX2 I/O UART_RX2, which corresponds to P1 on the internal IC. (Correspond to Pin 28 of the IC)

7 TX2 I/O UART_TX2, which is used for outputting logs and corresponds to Pin 29 (named as P0 by the original manufacturer) of the IC



The Disclosure

- Disclosed vulnerabilities to manufacturer in November
 - Would not affect our research endeavor
- Vulnerability was “known” by the manufacturer
- Nothing was fixed, go on, buy that thing and juice it up.

DETAILING THE ATTACK SURFACES OF THE WOLFBOX E40 EV CHARGER

December 03, 2024 | Dmitry Janushkevich

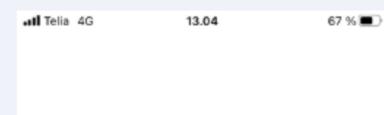
The WolfBox E40 is a Level 2 electric vehicle charge station designed for residential home use. Its hardware has a minimal user interface, providing a Bluetooth Low Energy (BLE) interface for configuration and an NFC reader for user authentication. Typical for this class of devices, the appliance employs a mobile application for the owner's installation and regular operation of the equipment.

At the moment of writing, the software versions were reported as follows:

- Main module: 3.1.17
- MCU module: 1.2.6
- Mobile application: 1.0.3

WOLFBOX EV MOBILE APPLICATION

The manufacturer distributes one application to configure and maintain the device. The application, named WolfBox EV, is available for both Android and iOS users.



From pwn2own@trendmicro.com <pwn2own@trendmicro.com> 

 Reply

 Forward

 Archive

 Junk

 Delete

More 



To Thomas Serpinis <thomas.serpinis@auxilium-labs.com> 

15.01.2025, 11:37

Subject Re: Pwn2Own Auto 2025 Application: WOLFBOX Level 2 EV Charger Target

Hello,

Thanks for reaching out. Please excuse our lateness, as this email was in the junk folder.

The Aftermath

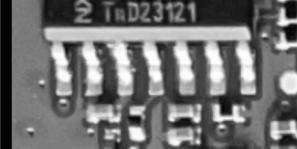
- While UAC is implemented, no protection to tampering is implemented
- Malicious actors can potentially tamper the device in minutes
 - Gaining persistence and remote access to the target
 - Most times chargers are exposed in public places, that's why they implement UAC
- Additionally, exposure of connected devices, like vehicles over V2G

~~Excuses~~ The Conflict

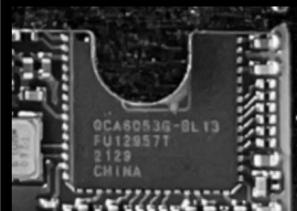
Excuses VS Reality



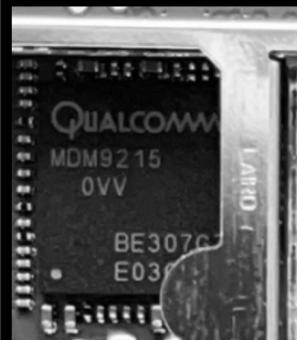




High-Speed CAN Transceiver with Standby and Sleep Mode
<https://www.nxp.com/docs/en/data-sheet/TJA1043.pdf>



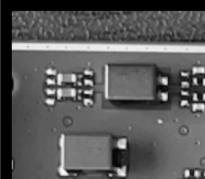
Qualcomm® APQ8053 SoCs for IoT
https://www.qualcomm.com/content/dam/qcomm-martech/dm-assets/documents/apq8053-socs-product-brief_87-pg760-1-b.pdf



LTE/4G Modem Chip



CMOS QUAD 2-INPUT NAND SCHMITT TRIGGER
<https://www.ti.com/lit/ds/symlink/cd4093b-q1.pdf?ts=1694496622695>



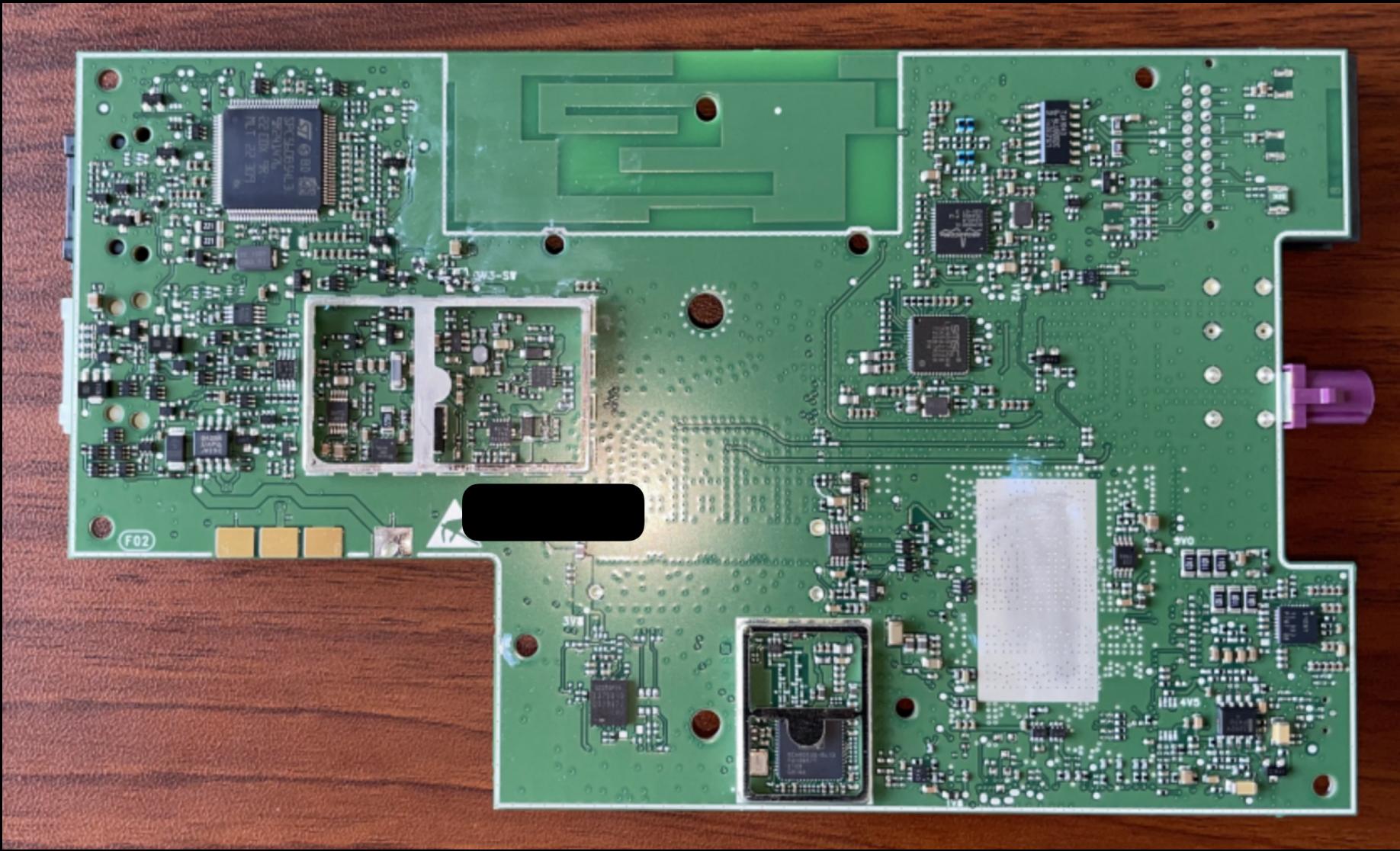
Transformers

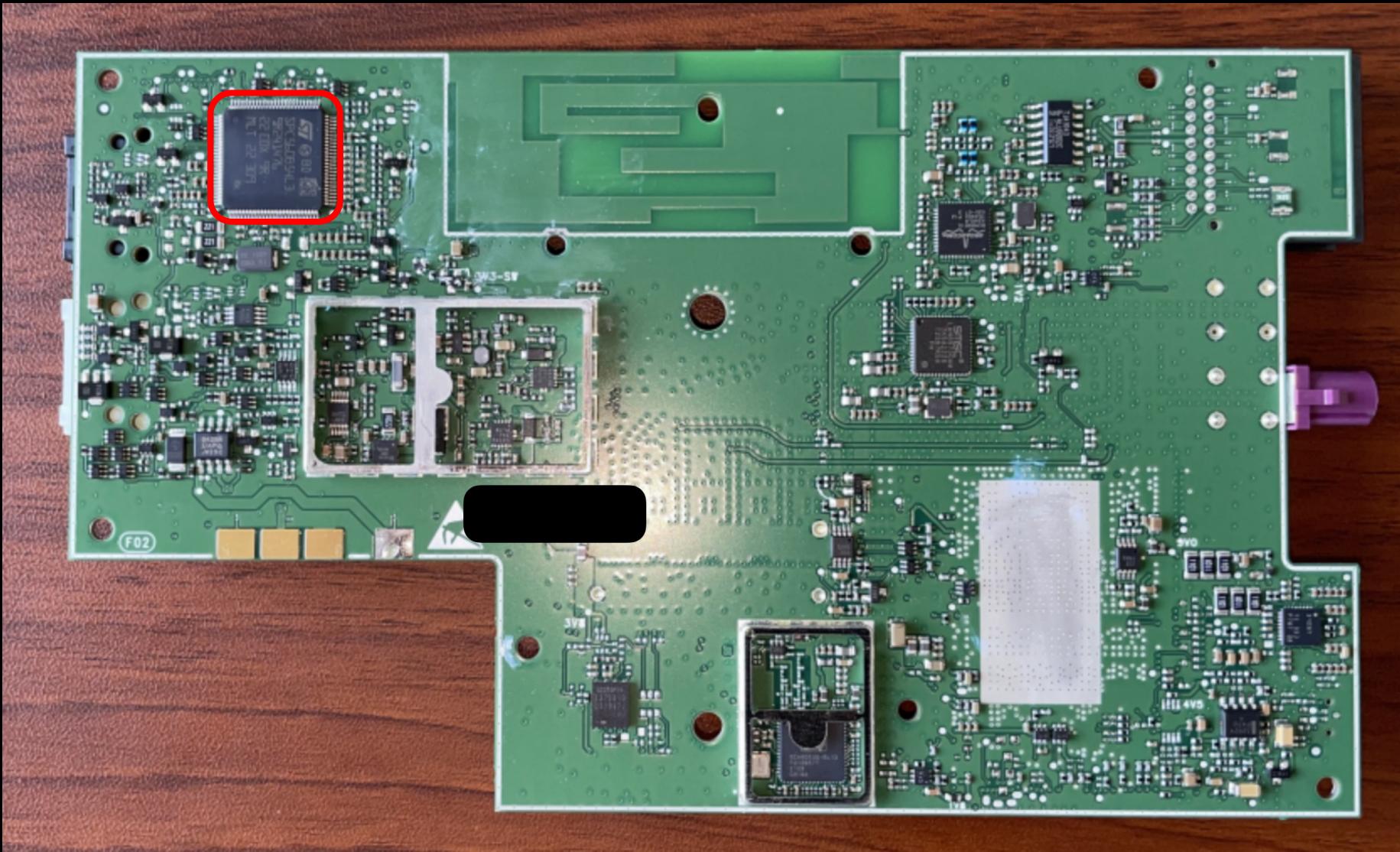
<https://diyiotkit.readthedocs.io/en/latest/hardware/knowledge.html#transformers>

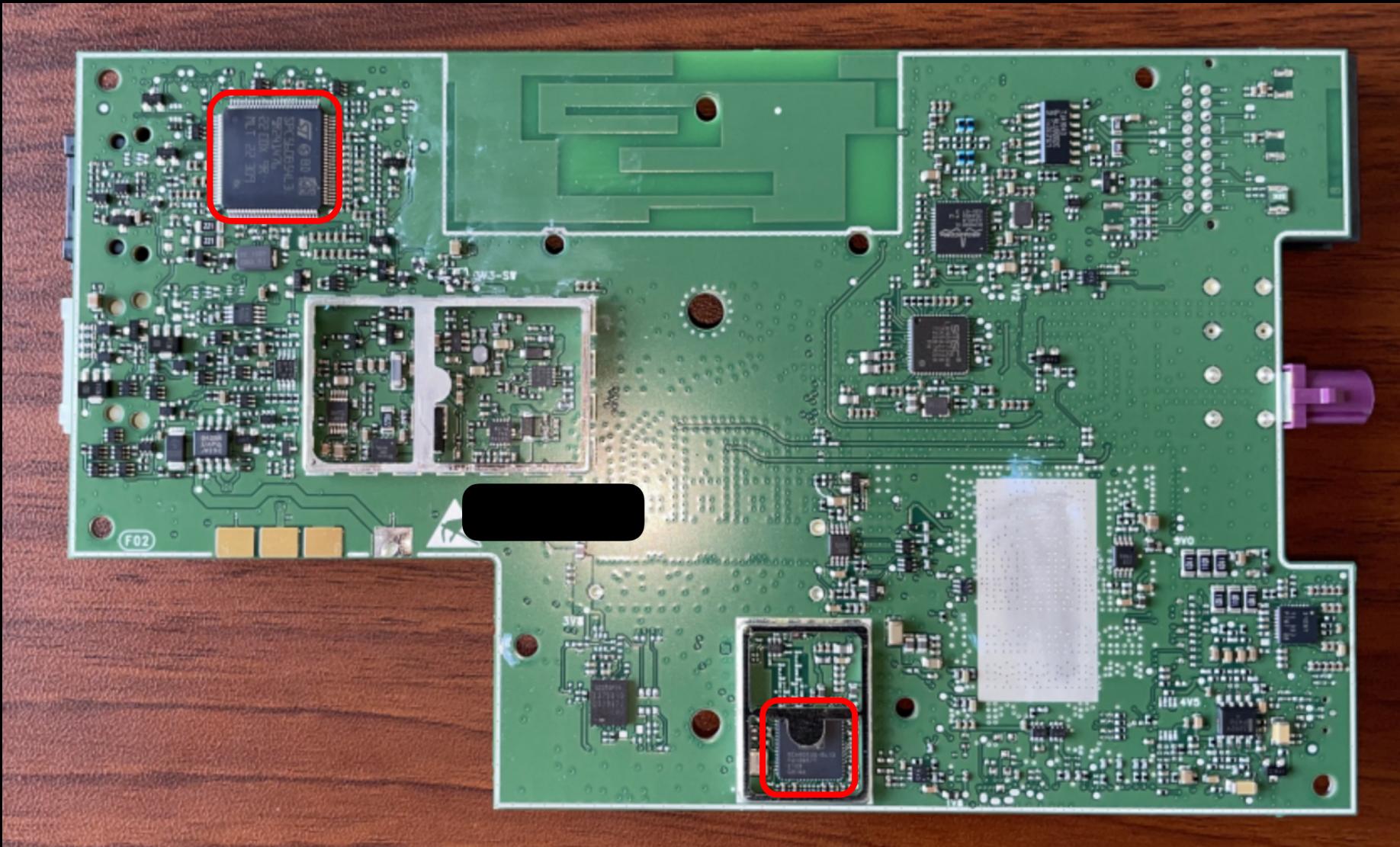


Automotive Specific Architecture

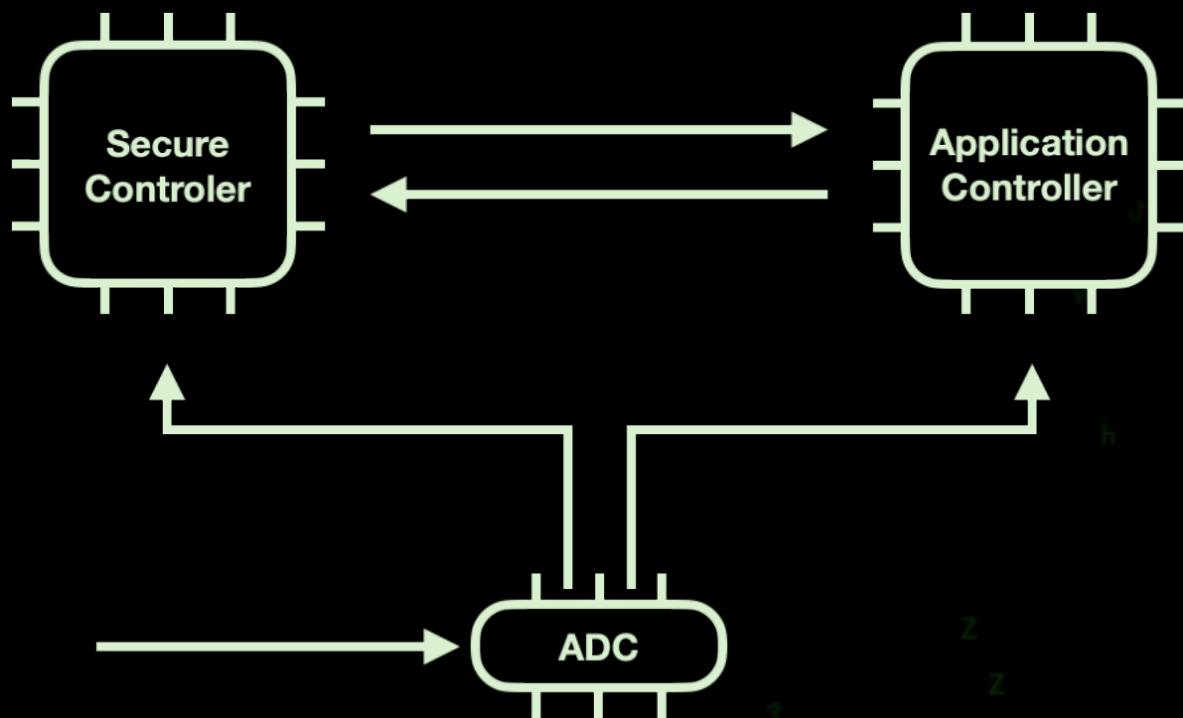
- Most Electronic Control Units (ECUs) use custom automotive specific architectures
 - e.g. Tricore, Renesas, etc.
- Safety related functionality, with "real-time" responsiveness is required in automotive systems







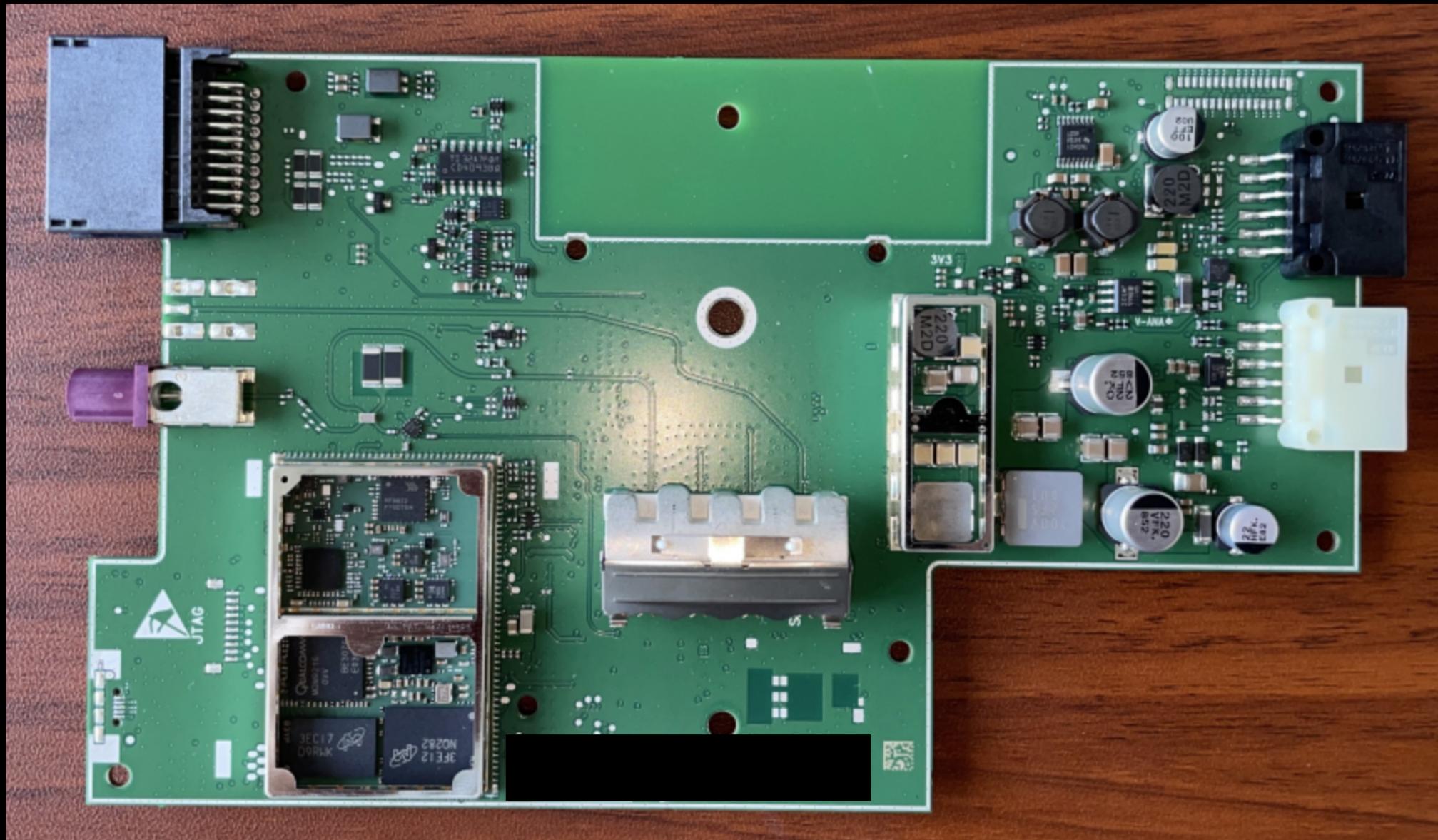
The Two SoCs

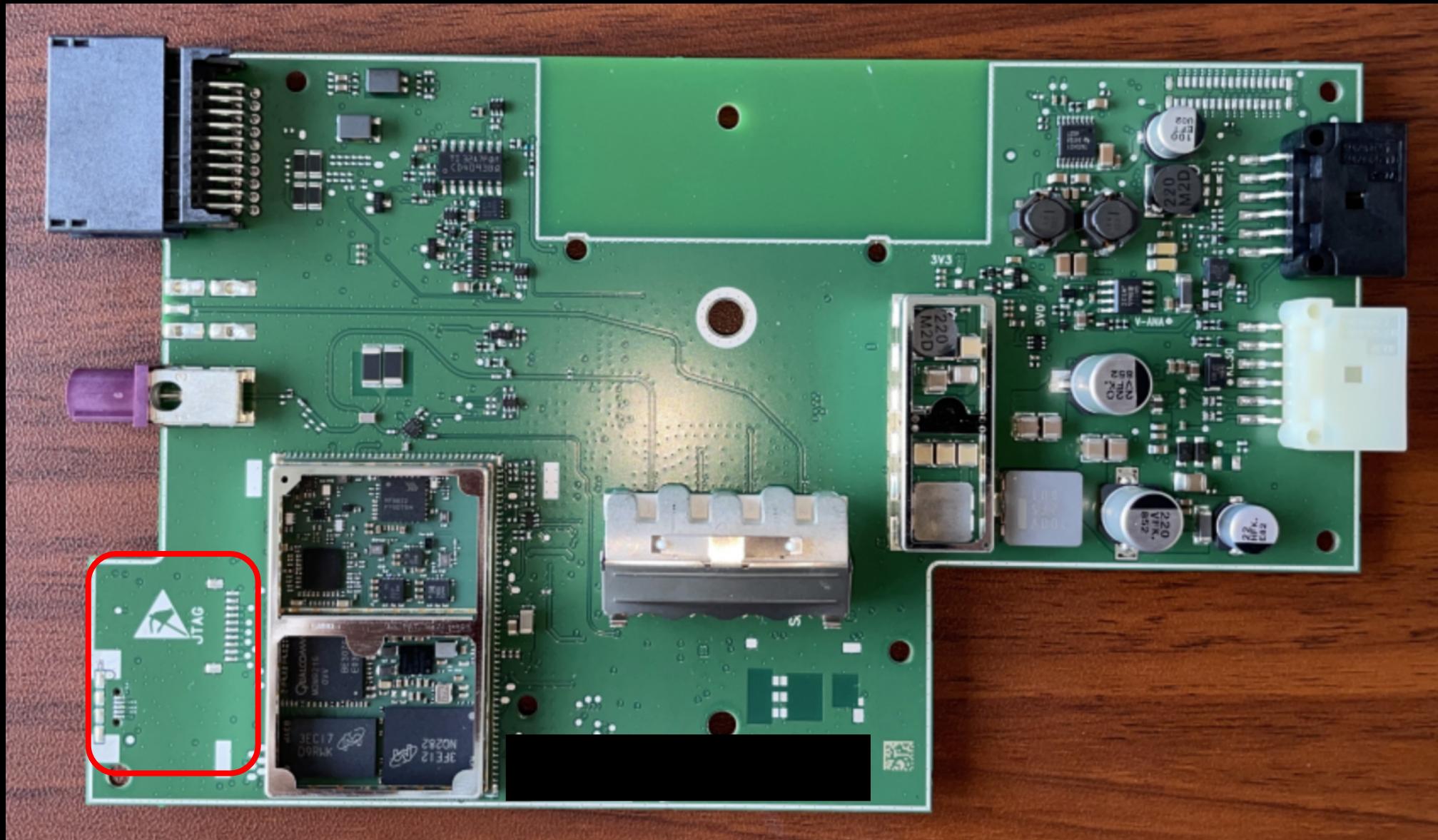


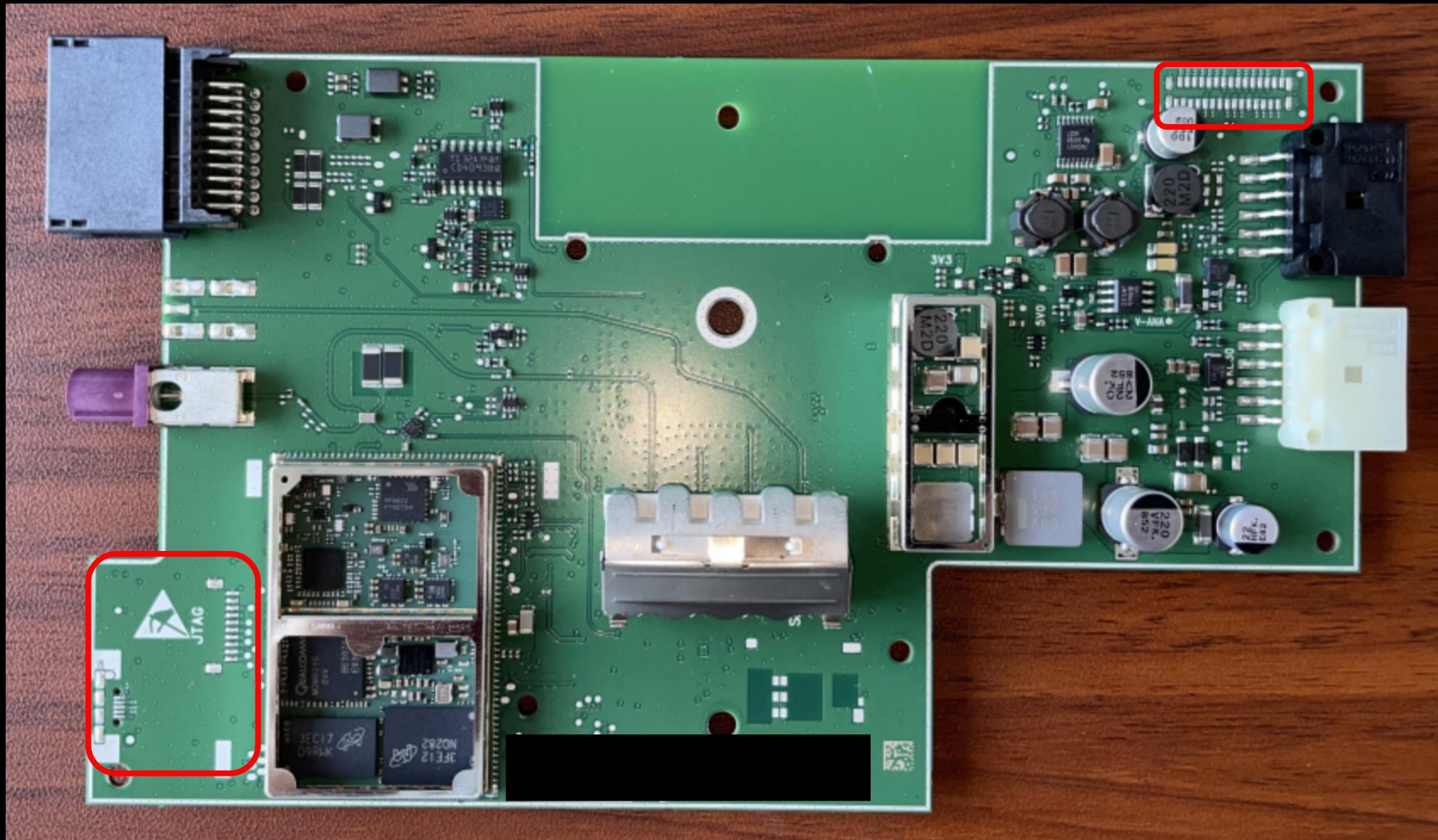
"Safety"-Critical Systems

- Many ECUs are considered "Safety-critical systems"
 - Their failure may result in loss or damage to equipment, death or injury, and environmental harm
 - NOT only applicable to the Automotive Industry
 - In automotive, it mostly applies to ECUs which supply critical functionality or can directly affect safety
 - e.g. Airbag, Braking, Power Steering, and others

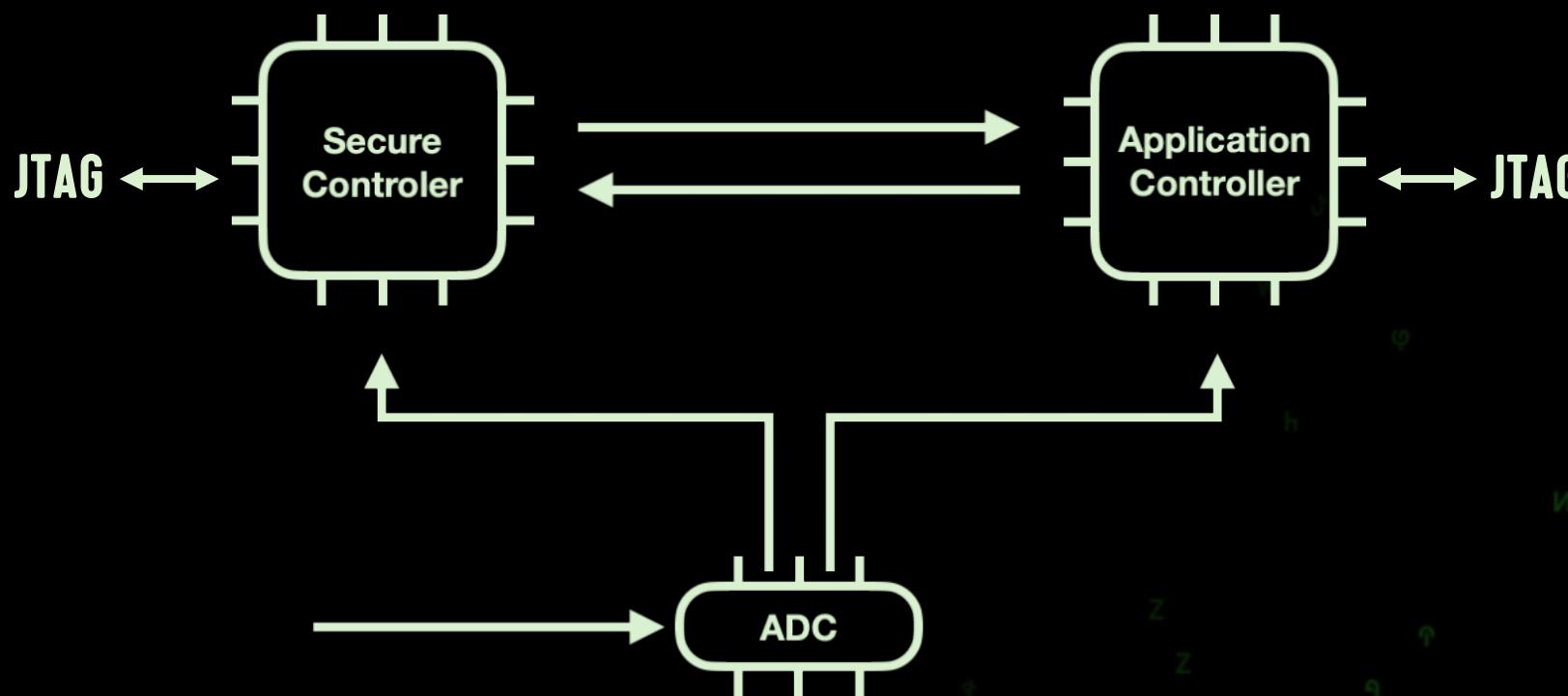




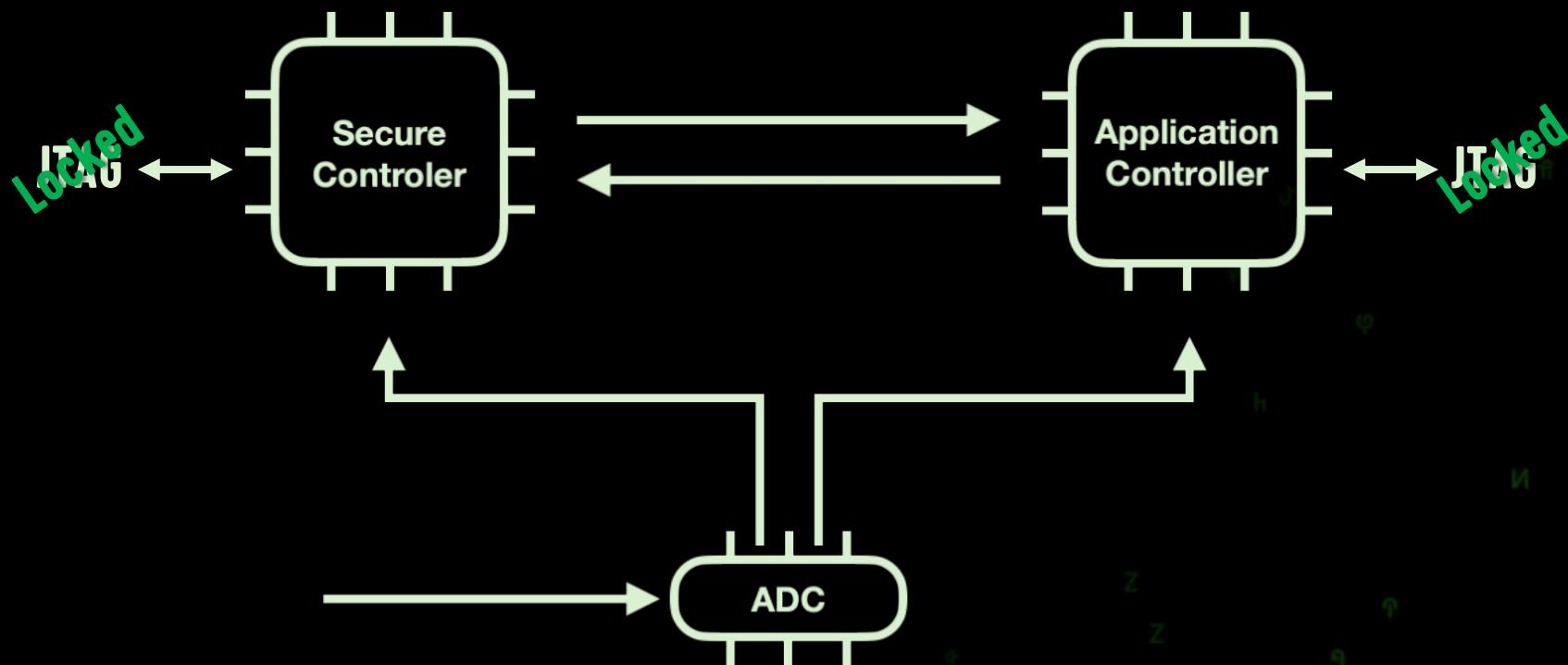


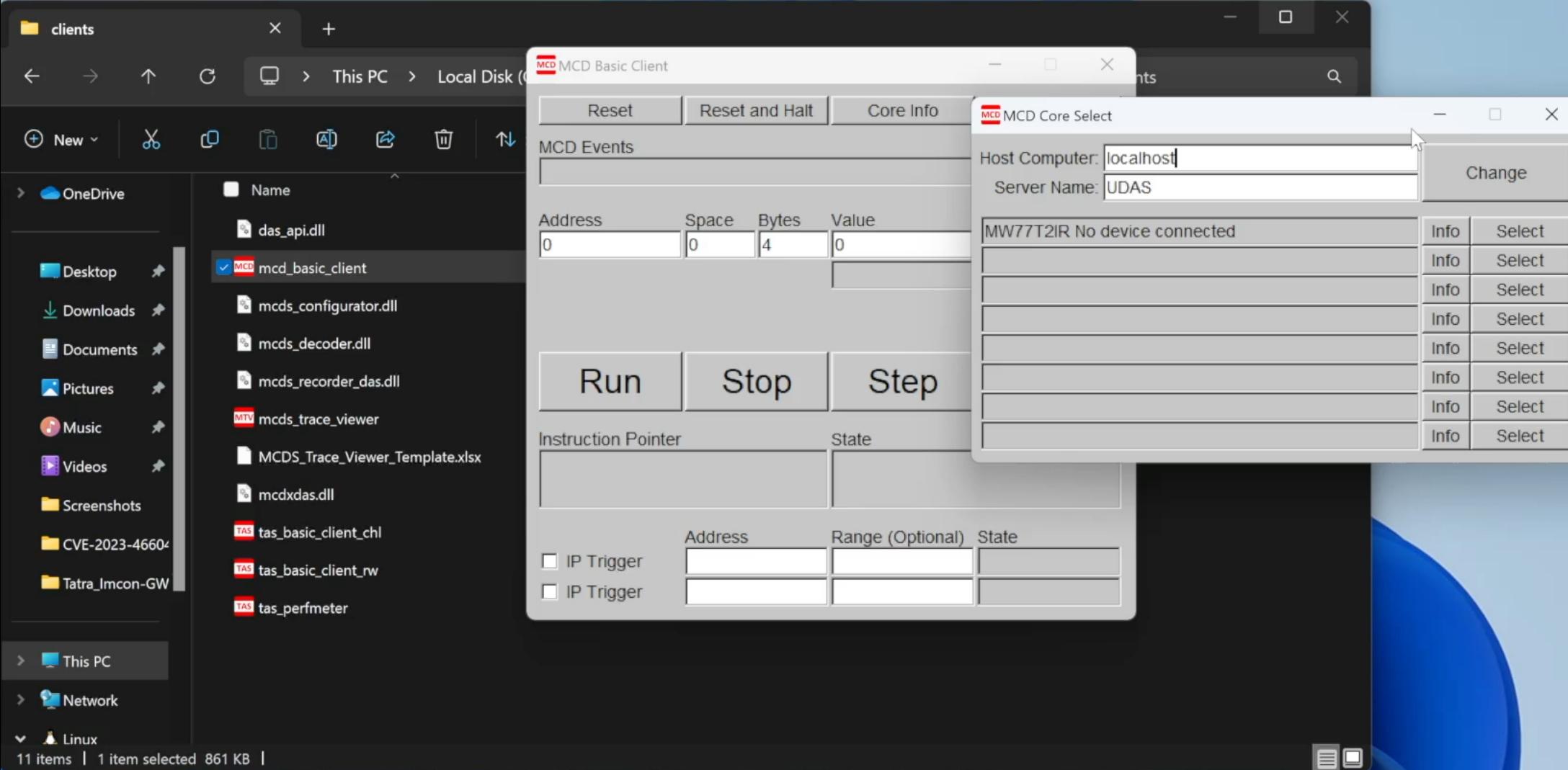


The Two SoCs

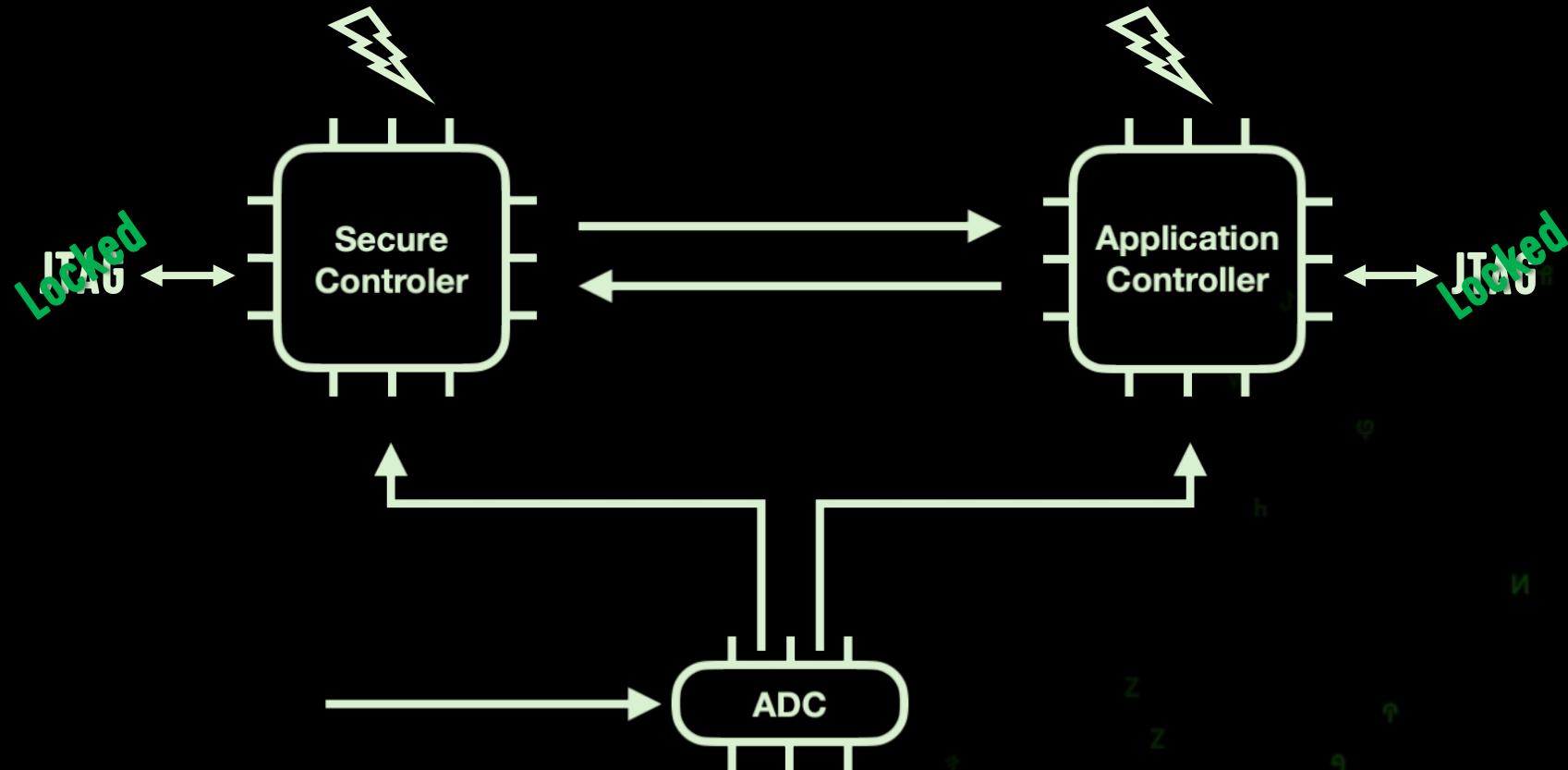


The Two SoCs

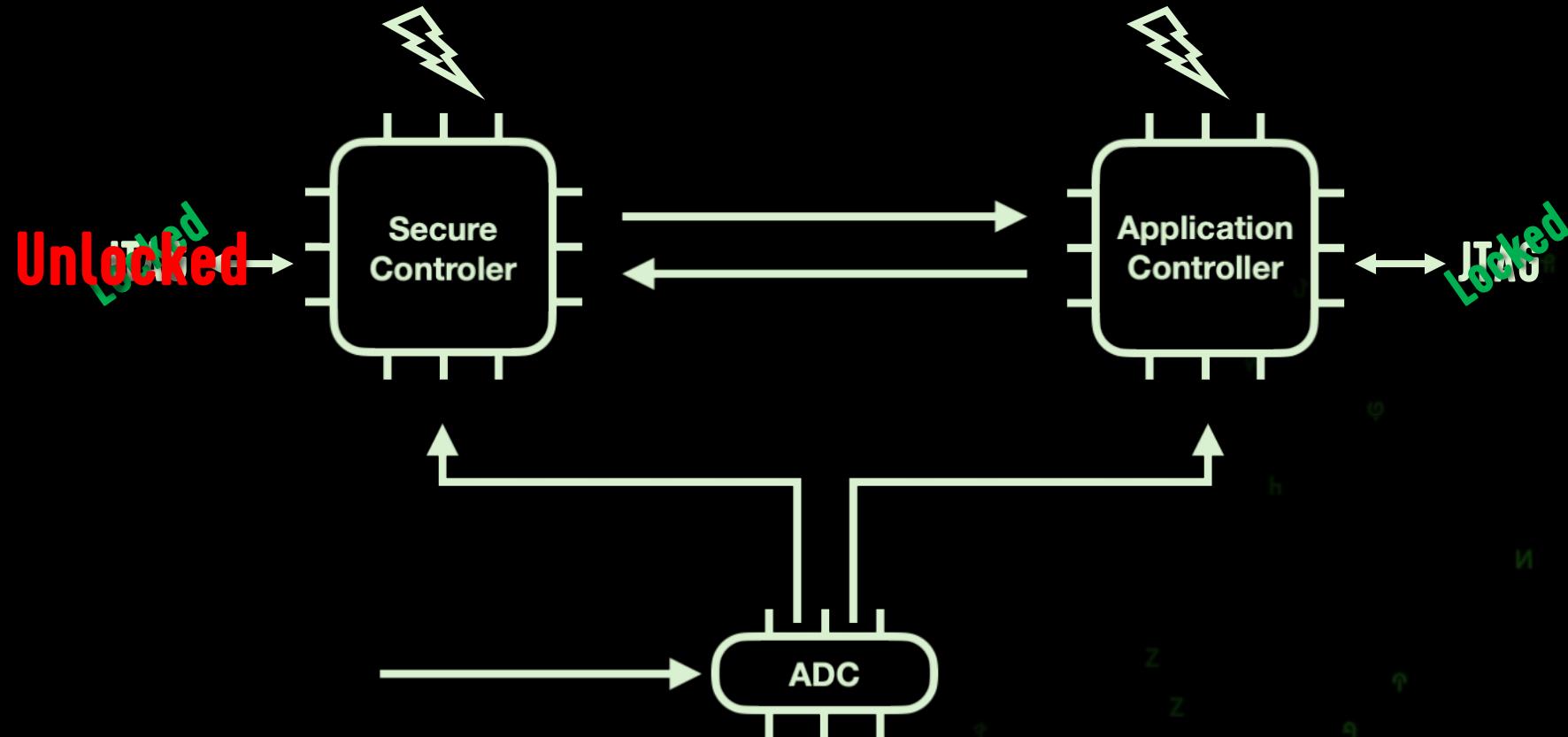




The Two SoCs



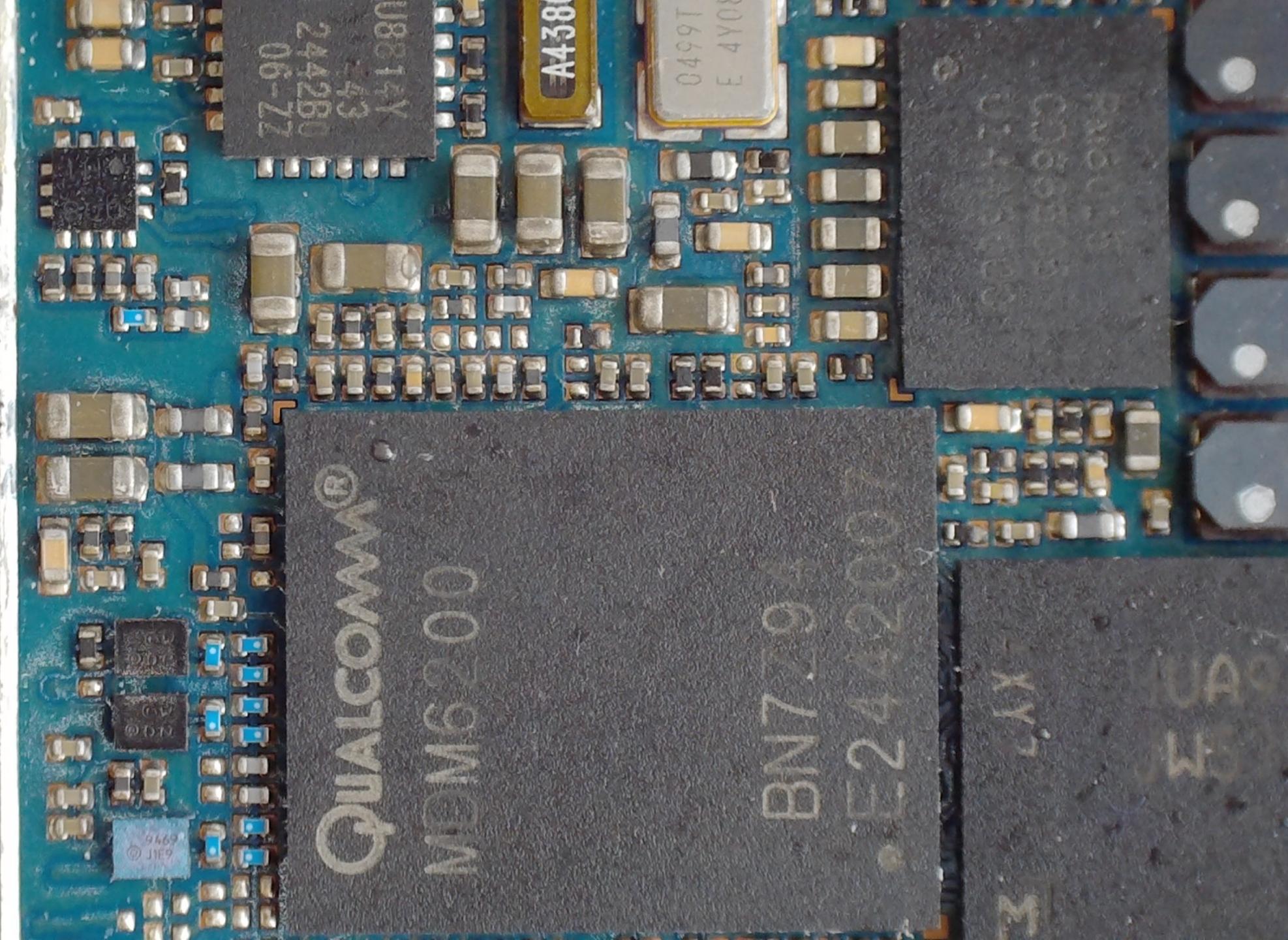
The Two SoCs

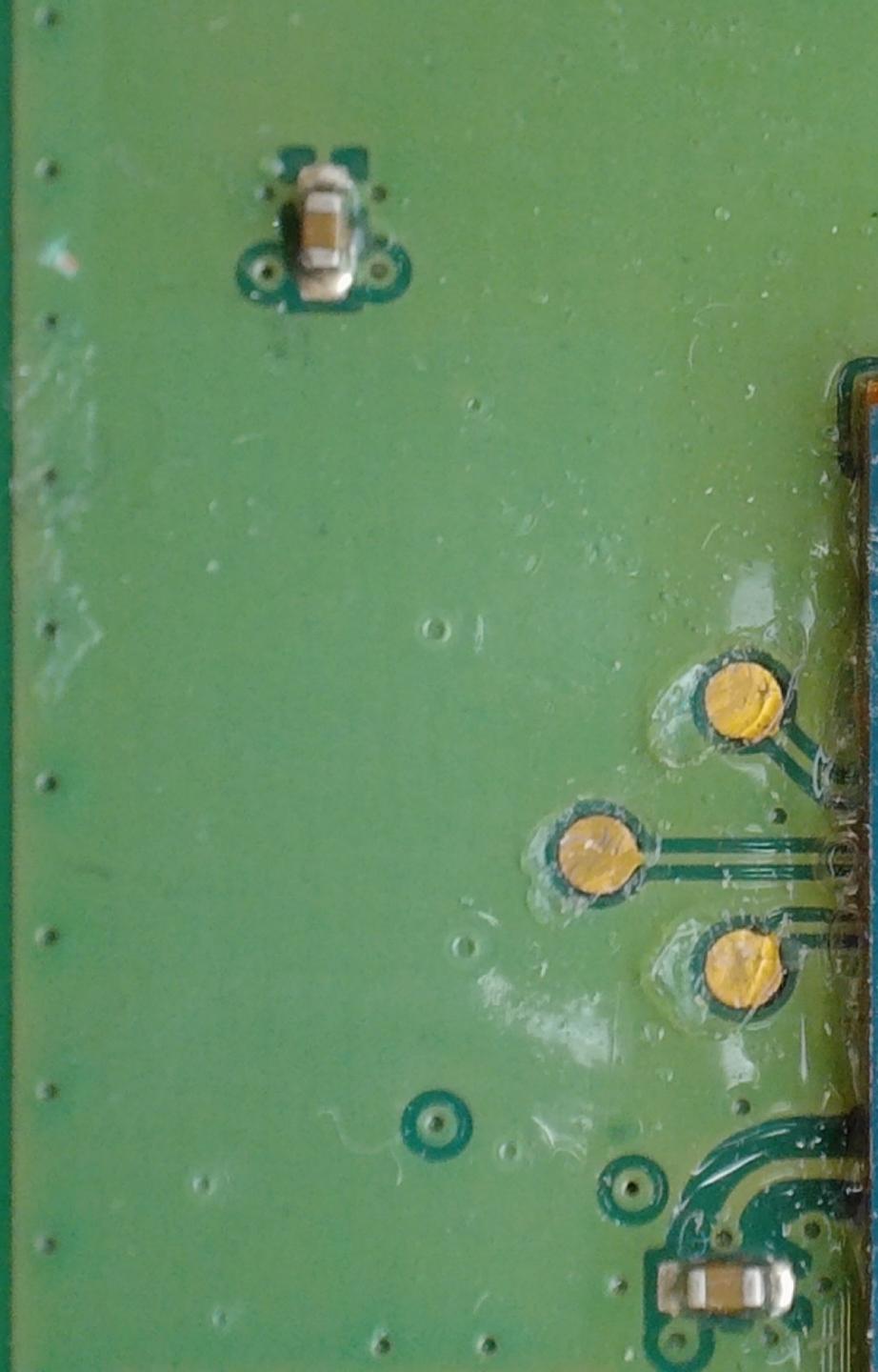
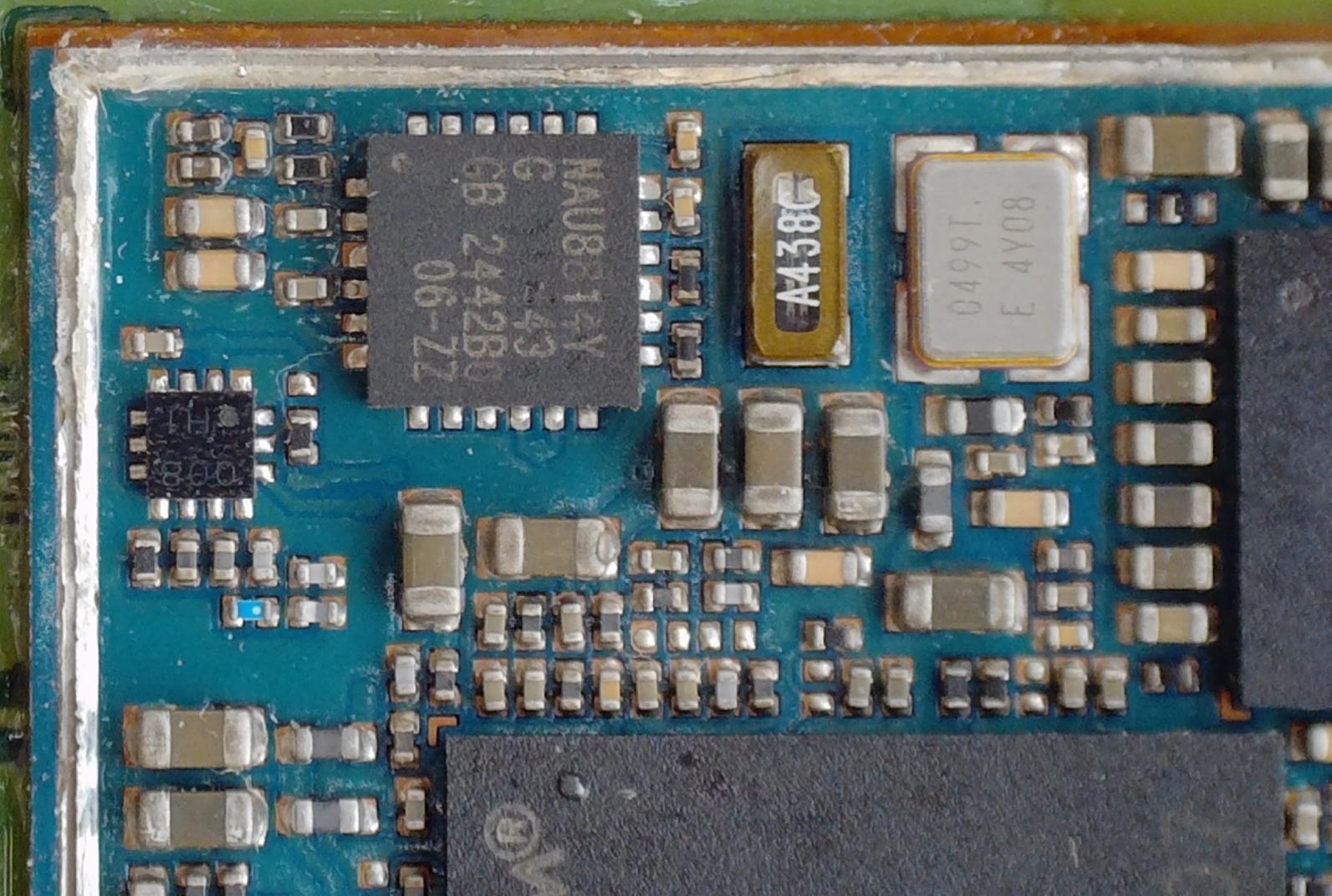


SECURITY LEVEL?



MAXIMUM!







- > Monitors
- > Network adapters
- > Other devices
- > Ports (COM & LPT)
 - Qualcomm HS-USB QDLoader 9008 (COM5)
 - Standard Serial over Bluetooth link (COM3)
 - Standard Serial over Bluetooth link (COM4)
- > Print queues
- > Processors

Cf Decompile: FUN_000192f6 - (.bin)

```
1
2 void FUN_000192f6(void)
3 {
4     undefined4 uVar1;
5     ushort uVar2;
6     undefined2 uVar3;
7     char cVar4;
8     char cVar5;
9     byte bVar6;
10    int iVar7;
11    int iVar8;
12    uint uVar9;
13
14    cVar4 = DAT_d000df5f;
15    bVar6 = DAT_d000dfd8;
16    cVar5 = DAT_d000df60;
17    if (((byte)(cVar4 - 1U) < 3) {
18        iVar7 = STM_TIM0;
19        iVar8 = STM_CAP;
20        uVar9 = STM_CLC;
21        uVar9 = uVar9 >> 8 & 7;
22        uVar2 = DAT_d000df58;
23        DAT_d000df50 = 0;
24        uVar1 = Ramd0000fcc;
25        DAT_d000df5a = ~uVar2;
26        DAT_d000df54 = uVar1;
27        DAT_d000df40 = CONCAT44(iVar8 * uVar9, iVar7 * uVar9);
28    }
29    uVar3 = DAT_d000df58;
30    DAT_d000df60 = cVar5 + '\x01';
31    DAT_d000fdf8 = bVar6 + 1 & 7;
32    (&DAT_d000dfc8)[bVar6] = uVar3;
33    return;
34 }
```



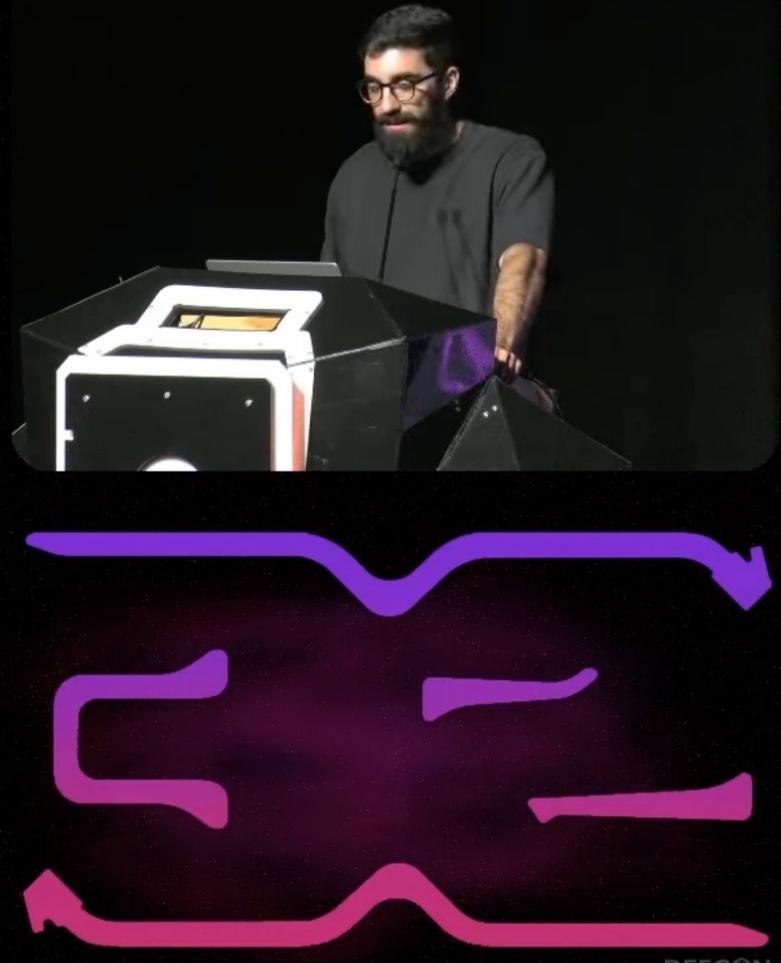
thomas.serpinis — pi@raspberrypizero2w: ~/Tools/caringcaribounext — ssh pi@10.42.0.1 — 223x62

— pi@raspberrypizero2w: ~/Tools/caringcaribounext — ssh pi@10.42.0.1

```
pi@raspberrypizero2w:~/Tools/caringcaribounext $ ccn.py -i socketcan -b 500000 -c can0 uds_fuzz defcon_poc -r 0 -g 7 -il 1 -d 1.9 10032703 d3adb3 0x754 0x75c
pi@raspberrypizero2w:~/Tools/caringcaribounext $ candump can0
```

Now what?

- Despite the help from the contact point, OEM decided that cost of fixing the issue exceeds the expectations for these models
- The argument: *The new E/E architecture will fix those issues in future models – MY25/MY26*
 - Meaning that the actual architecture which is currently sold, is still vulnerable, and will be
 - OEM is not willing to fix or disclose the findings
- Due to the safety aspect of the vulnerabilities, I decided to NOT disclose them publicly and search other avenues of escalation



DEFCON >>>

DEFCON

So what?

- 24 vulnerabilities were responsibly disclosed
- 4 of them approved and CVE assigned
- 20 of them were neglected, manufacturer did not answer
- ALL of them are a result of hardware analysis and firmware extraction

SECURITY THROUGH OBSCURITY



What's The Solution?

Towards a secure future, with reasonable solutions

The Challenge

- Debug interfaces can be locked with adequate security protections
 - Software protections from passwords to securely generated and stored certificates
 - Hardware protections like removing headers and traces, implement fuses, chip tamperproof protections, etc.
- Though, it's not sunshine and flowers
- Side channel attacks and reverse engineering can lead to potential compromise
 - Main target is how expensive a company makes it for an attacker to achieve their goal

WE NEED TO HAVE SECURE ALTERNATIVES

The Reality

- Automotive components implement several modern and non-modern interfaces:
 - CAN(-FD)
 - Automotive Ethernet
 - FlexRay
- Not only secure implementation in low levels can be achieved, but higher level solutions are available

The Easy Solution

- CAN and UDS
 - CAN FD allows for enhanced bandwidth, encryption and authentication
 - Security features like AES, MAC, SecOC can be implemented
 - Application layer protocols like UDS and XCP can be used in a secure way to allow access to specific regions of memory after secure authentication mechanisms
 - E.g. certificate authentication service 0x29 on UDS, with encrypted data under data identifiers or memory locations

```
[└$ python3 cc.py -i can0 uds dump_dids 0x7df 0x7e8
```

CARING CARIBOU v0.3

Loaded module 'uds'

Dumping DIDs in range 0x0000-0xffff

Identified DIDs:

DID Value (hex)

The Obvious Solution

- Automotive Ethernet
 - All the advantages of conventional ethernet on a single pair cable
 - High bandwidth which supports robust security protocols, like MACsec and TLS/DTLS
 - Application level authentication can be easily achieved, bot with automotive specific protocols (UDS over DoIP) or conventional ones
- Perfect solution, with no excuses for ECUs that implement Automotive Ethernet

The Next Challenge

- Trust and integrity have to be maintained
- HSMs can provide enhanced security
 - Dedicated hardware for secure key management, cryptographic operations, and secure storage
- Secure Boot can provide an established and trusted foundation
 - Ensures only authorized software runs on the ECU

The Next Challenge

- Authentication and Authorization
 - Robust authentication protocols (mutual authentication, cryptographic keys, challenge-response, PUFs)
 - Lightweight protocols for resource-constrained ECUs
 - Integration with HSMs for secure key storage
 - Effective authorization mechanisms (RBAC, ABAC) to prevent misuse
 - Secure session management and auditing of access attempts

The Elephant in the Room

- Remote Forensic Data Retention and retrieval
 - Anonymized data are already shared with the secure backend connection of modern vehicles
 - With real time data sharing, and utilization of the Ecall ECUs for data sharing after the crash, data can be obtained even if ECUs are severely damaged
- Huge privacy concern, especially if not implemented properly

The Poop Mans Idea

- Dedicated Physical Interface
 - Specialized protected port
 - Obscured and difficult to access
 - Completely isolated from other debug functionality
 - Solely for access to the specific memory locations hosting the forensics data
- Increased hardware complexity

||||||| The Final Countdown

Moving Towards Secure Automotive Forensics

THE END