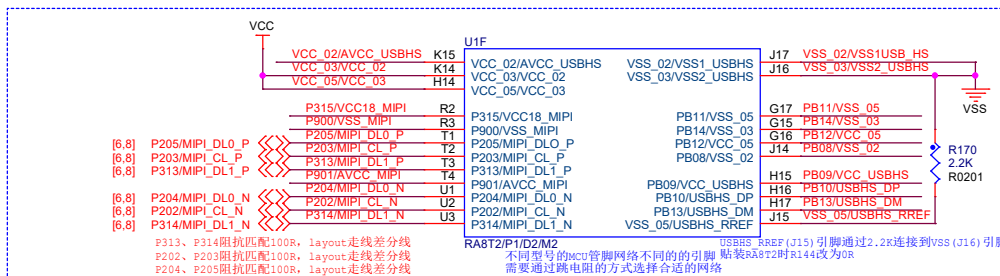
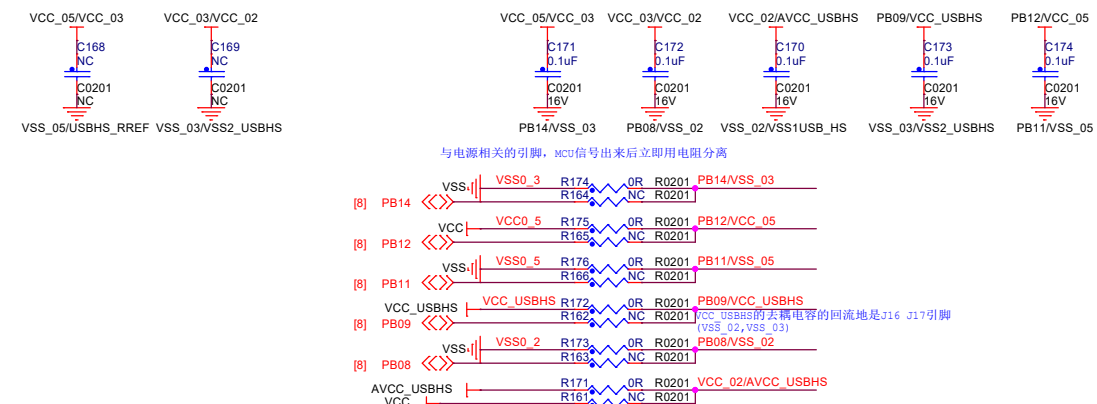


核心板缺省功能阻容设置

核心板芯片	R7KA8T2L	R7KA8P1K
C168-169	0.1uF	NC
C170	0.1uF	0.1uF
C171-C174	NC	0.1uF
C191-C192	NC	0.1uF
R145-R150	NC	NC
R151-R168	0R	NC
R170	0R	2.20K 1%
R171-R180	NC	0R
R191-R193	NC	0R

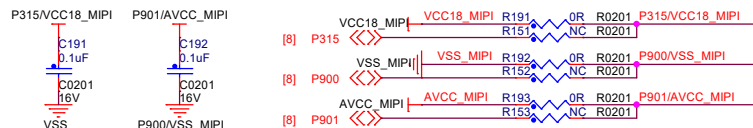


## 电阻选装

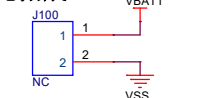


## MIPI部分

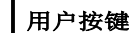
要贴的器件靠近MCU摆放, NC器件在MCU-BTB间任意摆放



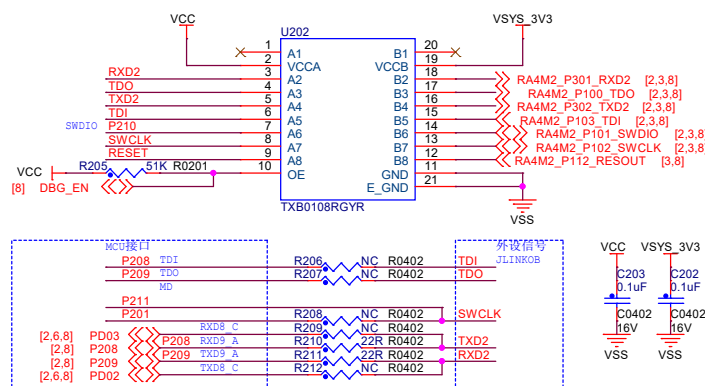
## 通孔拓展



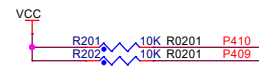
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## 接口信号



## 通孔拓展



VIN有大电流需求，请用粗线或铜皮连到VIN网络。  
J201, J202之间的间距请保证2.54mm的倍数

## 晶振

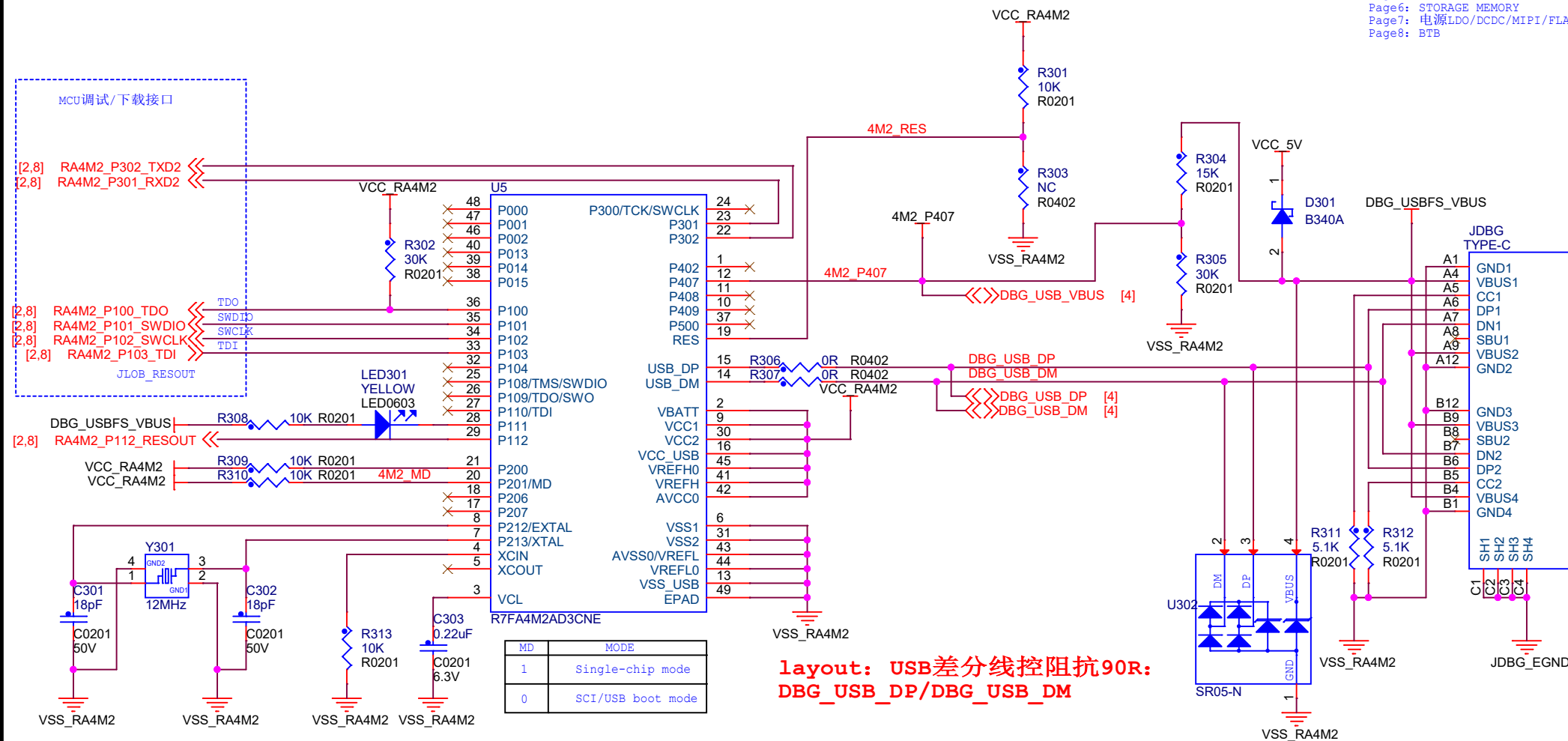


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Title 野火瑞萨RA8P1/D2/M2-BTB核心板(PMIC)_原理图-供WS贴装用			
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# Jlink OB

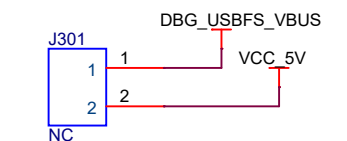
注意: R7FA4M2只能12MHz晶振

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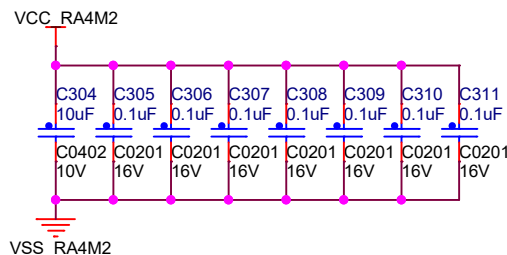
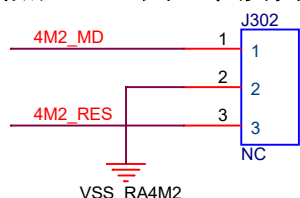


layout: USB差分线控阻抗90R:  
DBG\_USB\_DP/DBG\_USB\_DM

## 测试点 1.25间距/预留



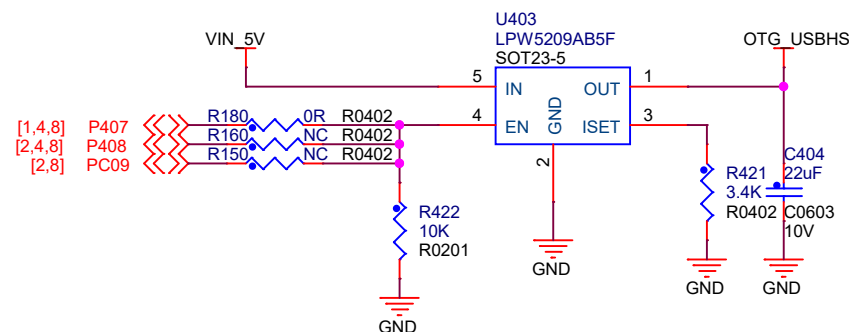
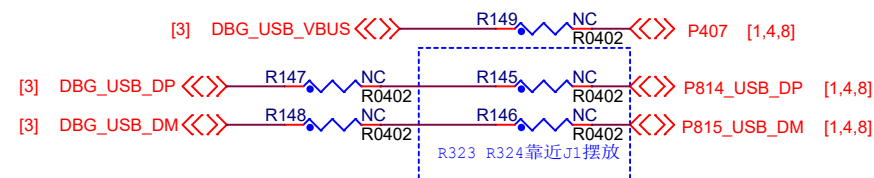
debug隔离工作时, 用作短路二极管运行



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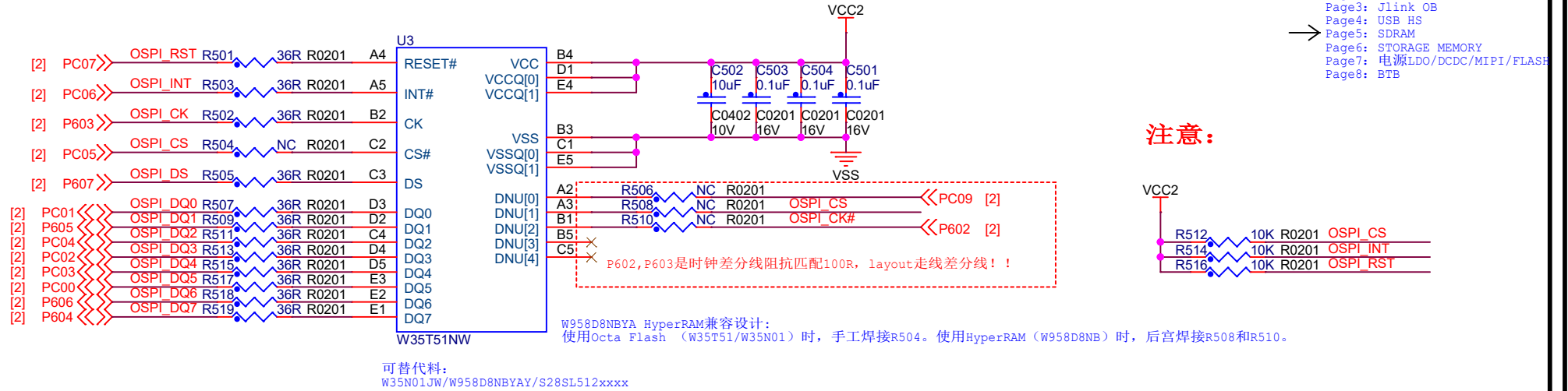
Title 野火瑞萨RA8P1/D2/M2-BTB核心板(PMIC)_原理图-供WS贴装用			
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如果使用USB HS，主晶振只能选12/20/24/48MHz

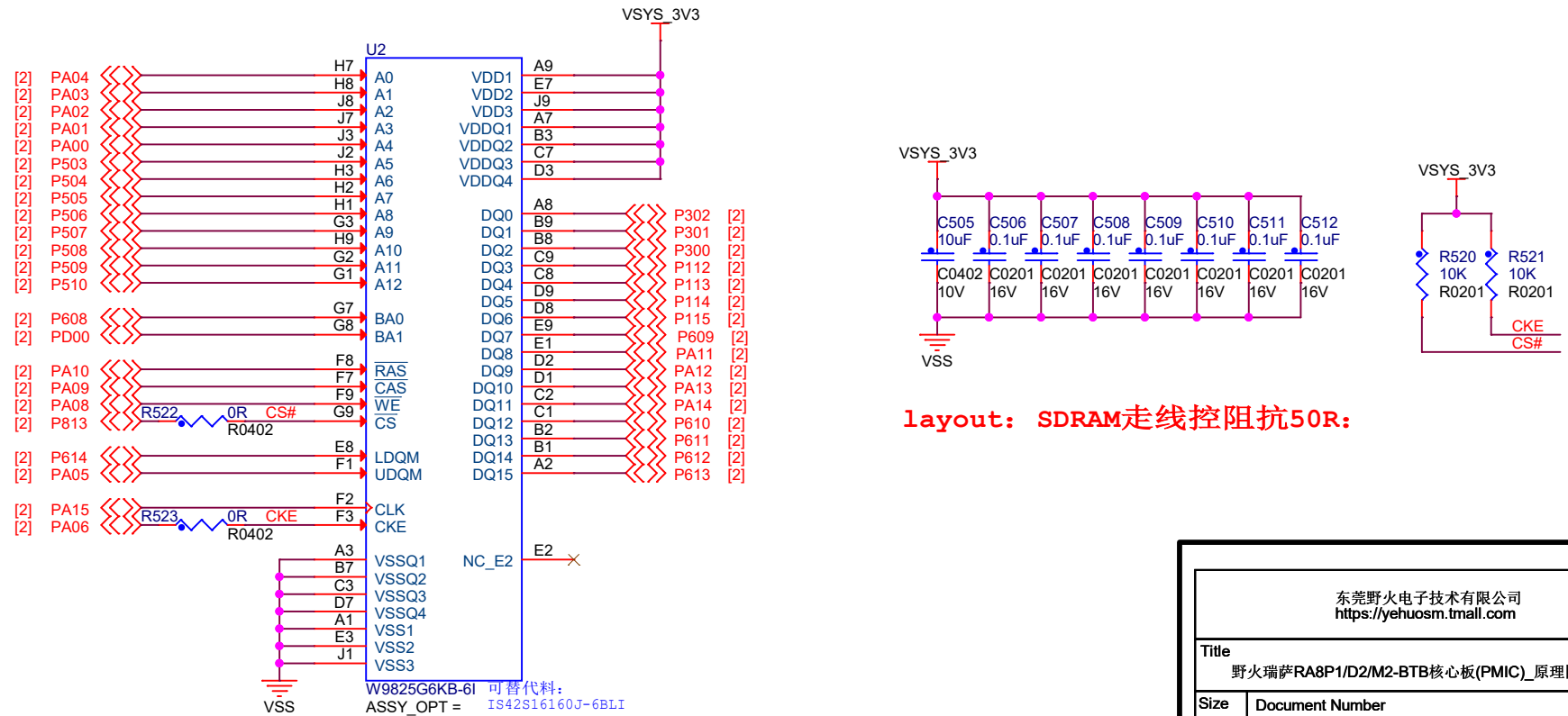


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## FLASH



## SDRAM

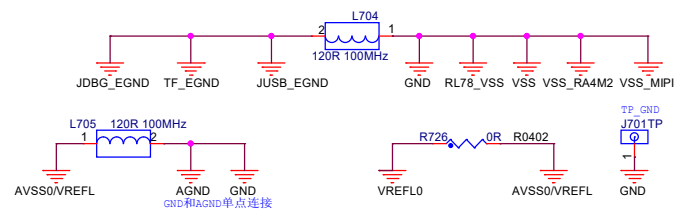
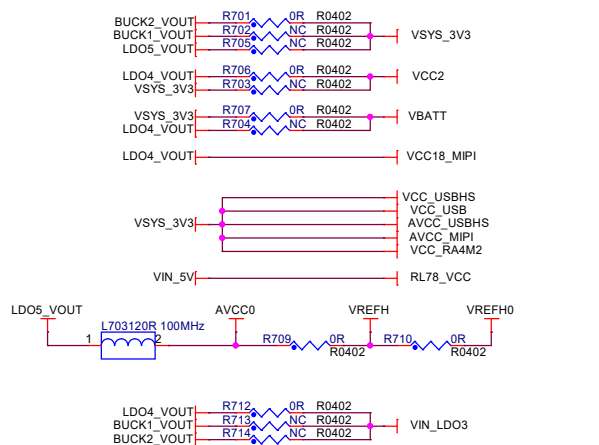


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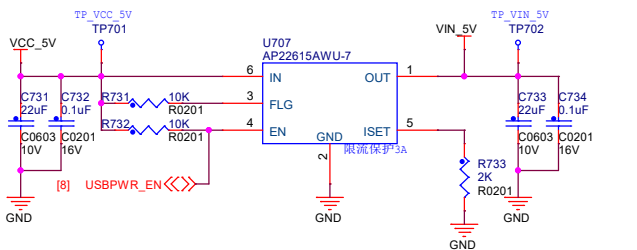
Title 野火瑞萨RA8P1/D2/M2-BTB核心板(PMIC)_原理图-供WS贴装用			
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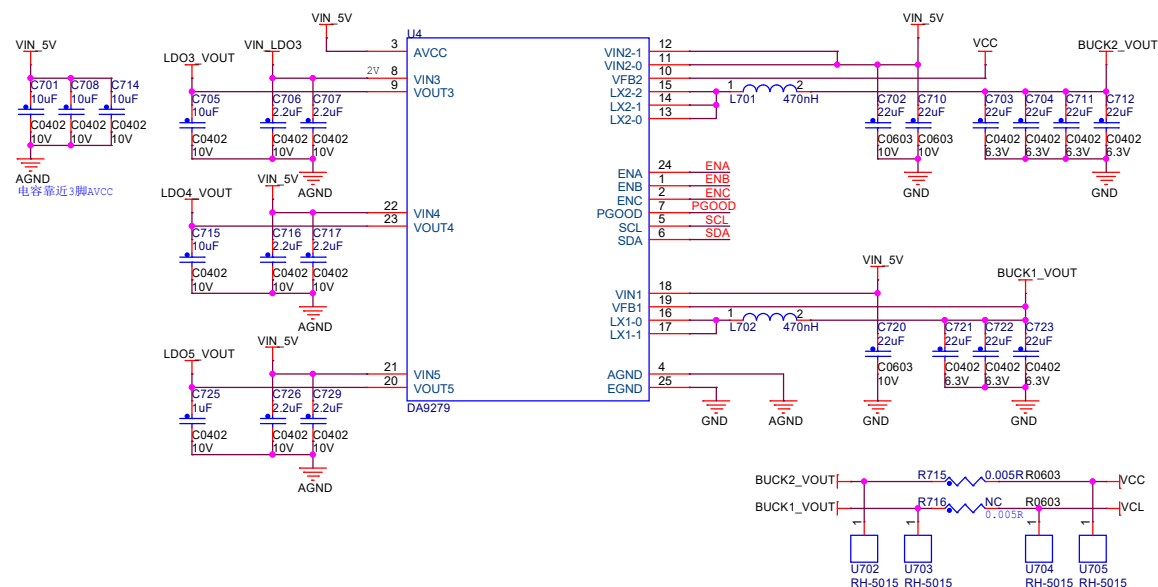
## 电源分割



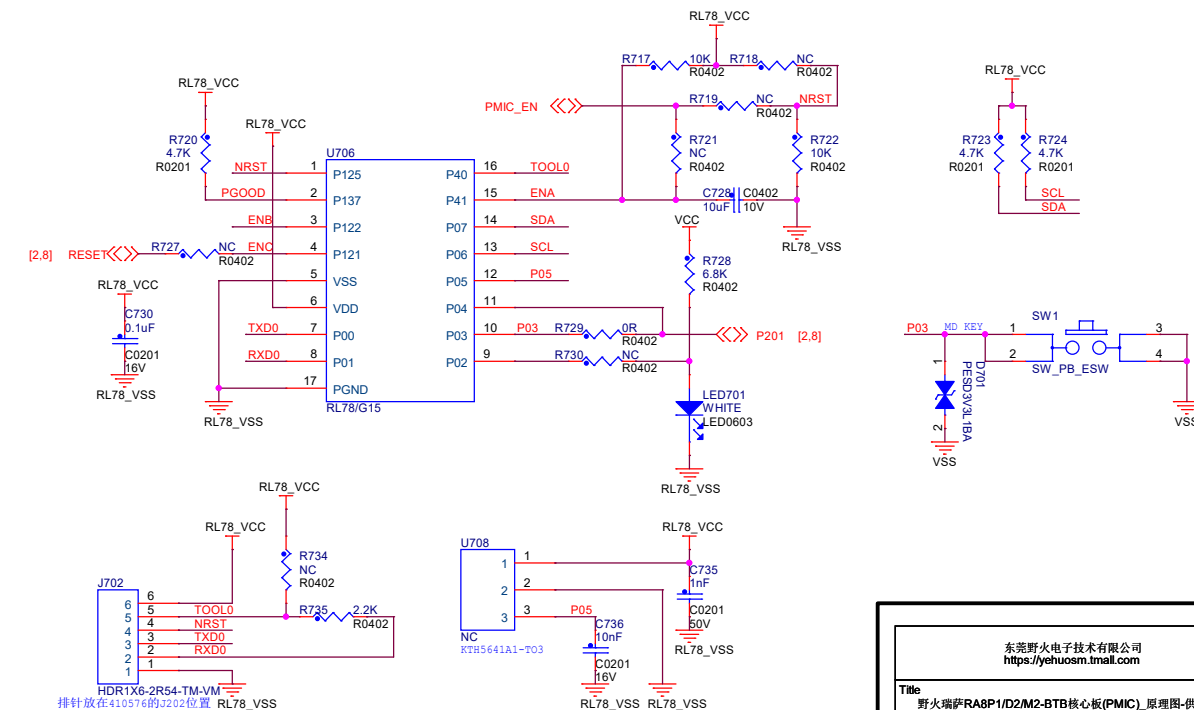
## 电源保护



## PMIC



## MCU



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Title  
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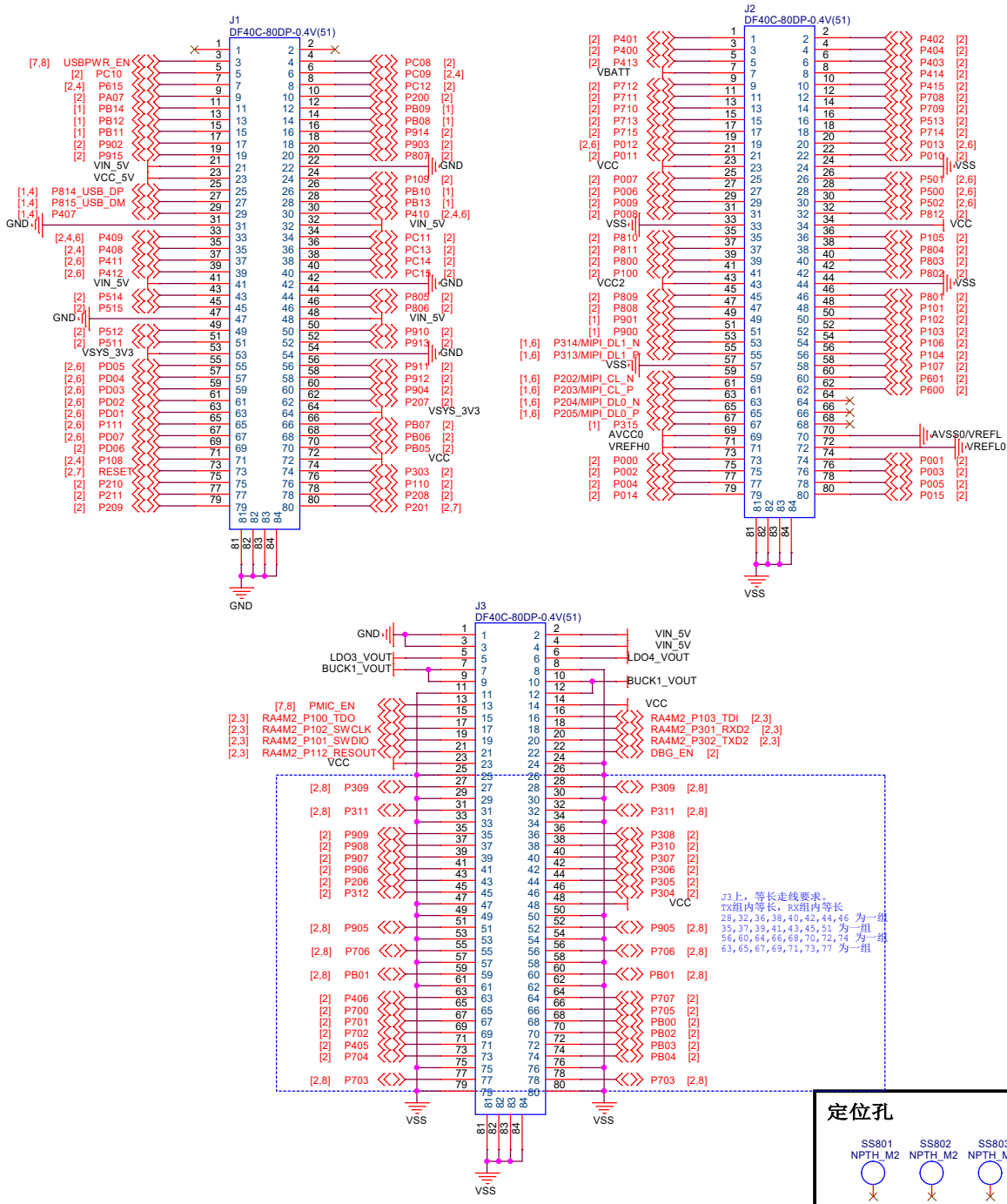
Size	Document Number	Re
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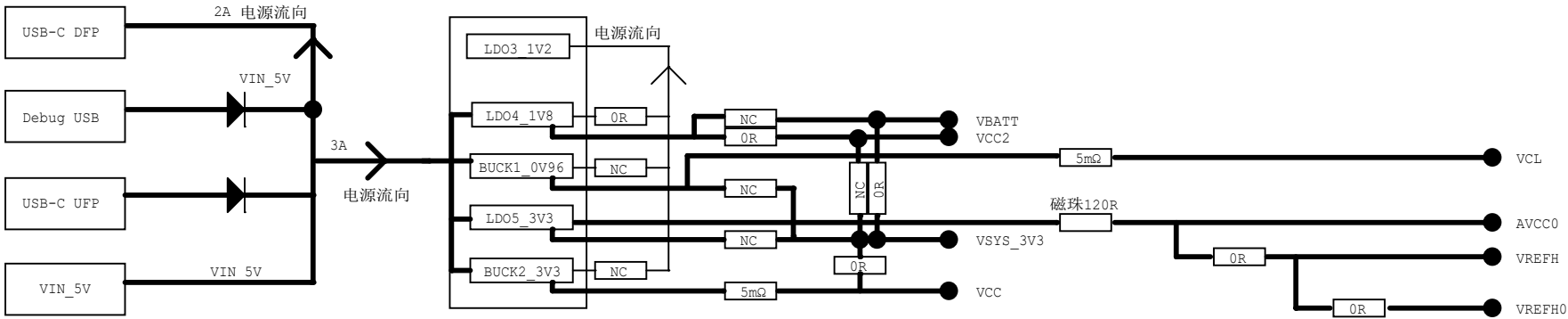
BTB座子

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电源拓扑图



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历史版本

版本号	日期	设计	描述
V0.1	2025-05-19	LPC	初始版本

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# 野火瑞萨RA8P1/D2/M2-BTB核心板 (PMIC)\_原理图

## -供WS贴装用\_V0.1

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