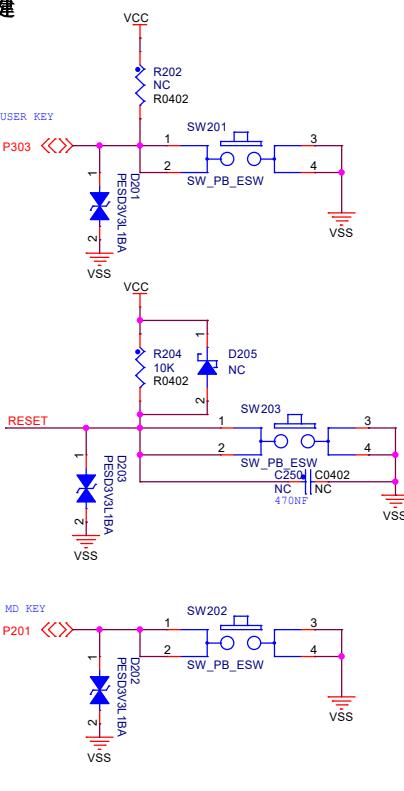
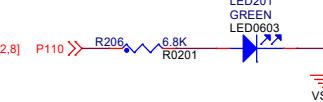


### 用户按键

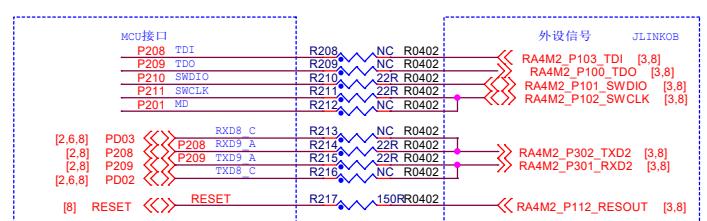


### LED

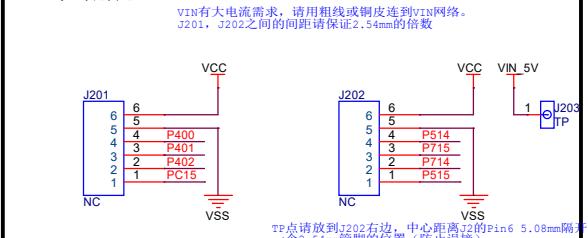


如果使用USB HS, 主晶振只能选12/20/24/48MHz

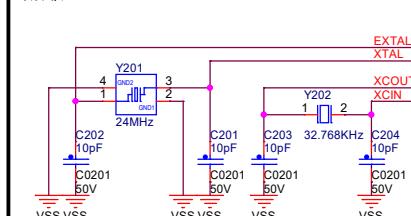
### 接口信号



### 通孔拓展



### 晶振



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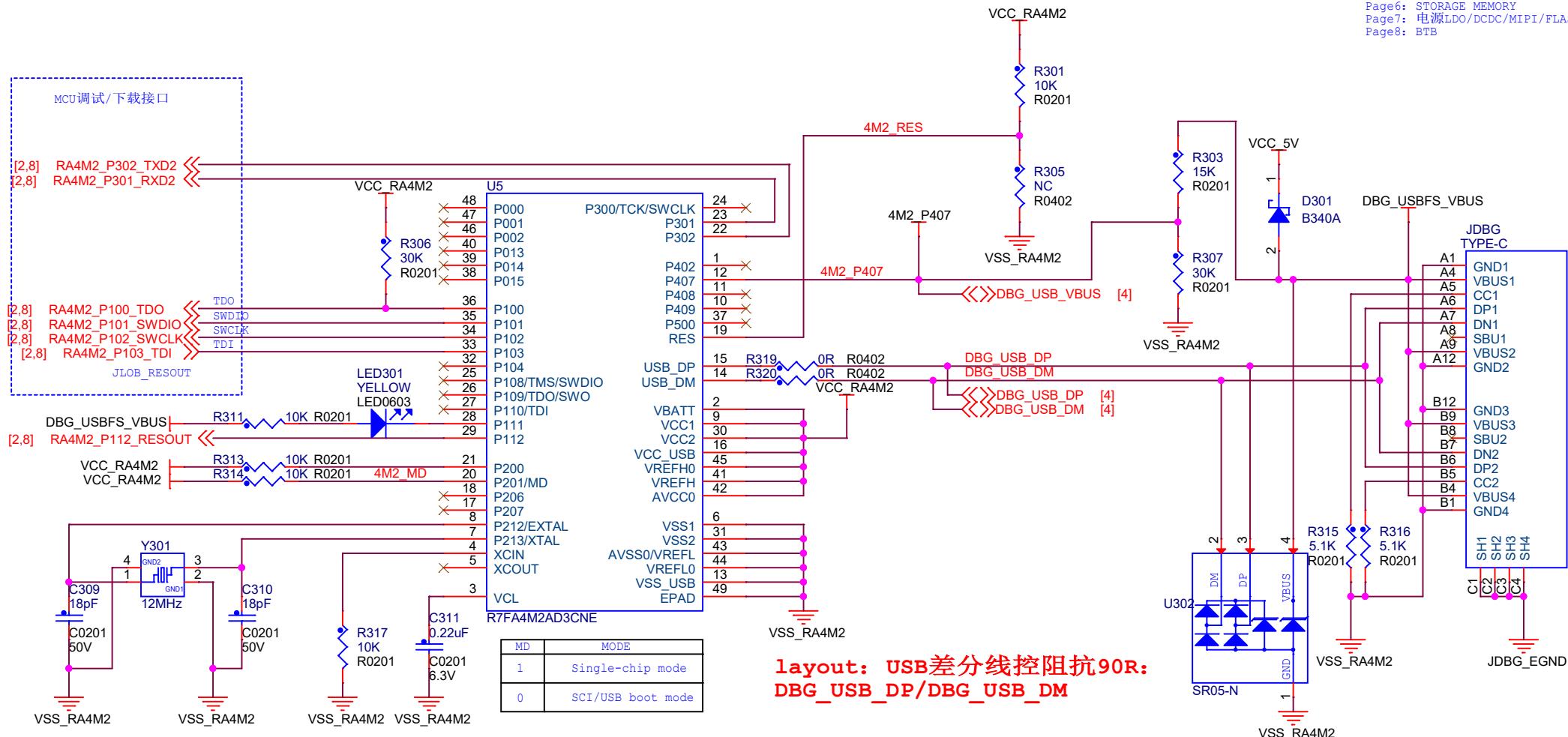
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Size A3 Document Number MCU IO Rev V1.1

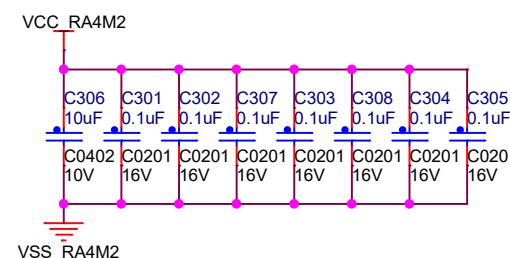
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Jlink OB

注意：R7FA4M2只能12MHz晶振



### 测试点 1.25间距/预留



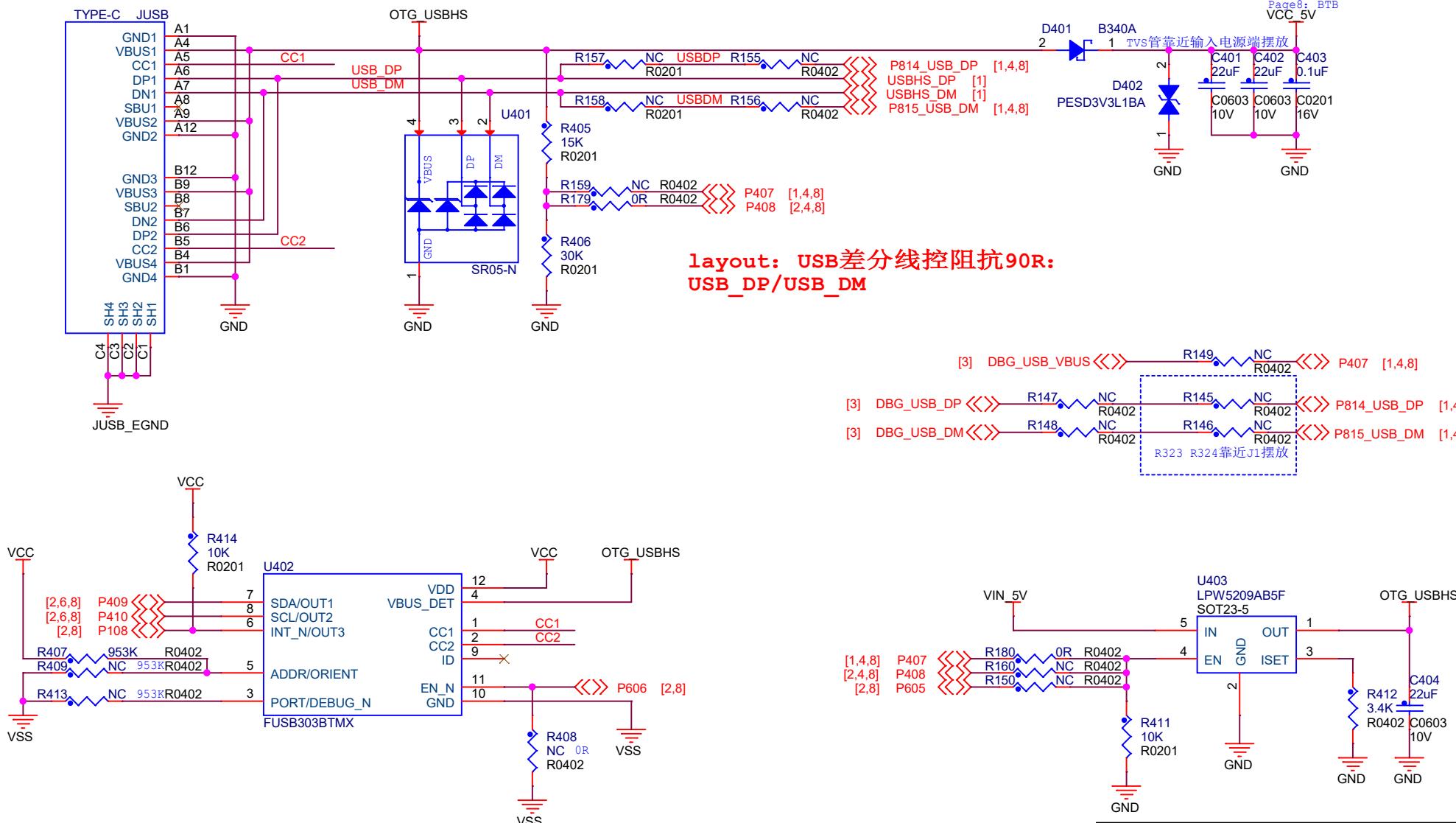
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## USB HS

如果使用USB HS, 主晶振只能选12/20/24/48MHz

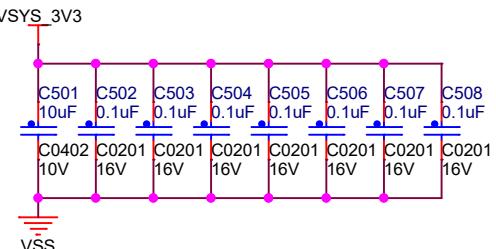
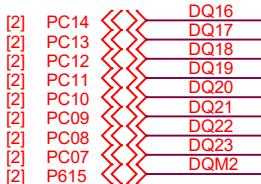
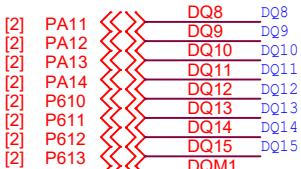
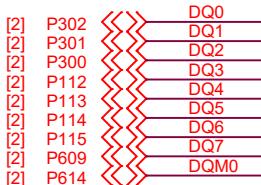
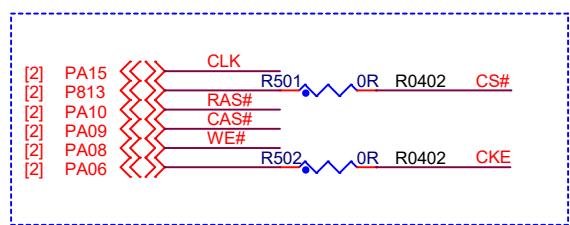
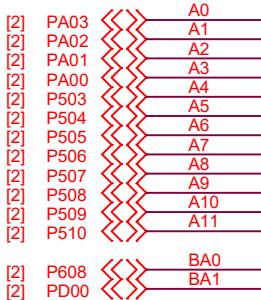
Page1: MCU 电源  
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 VCC 5V



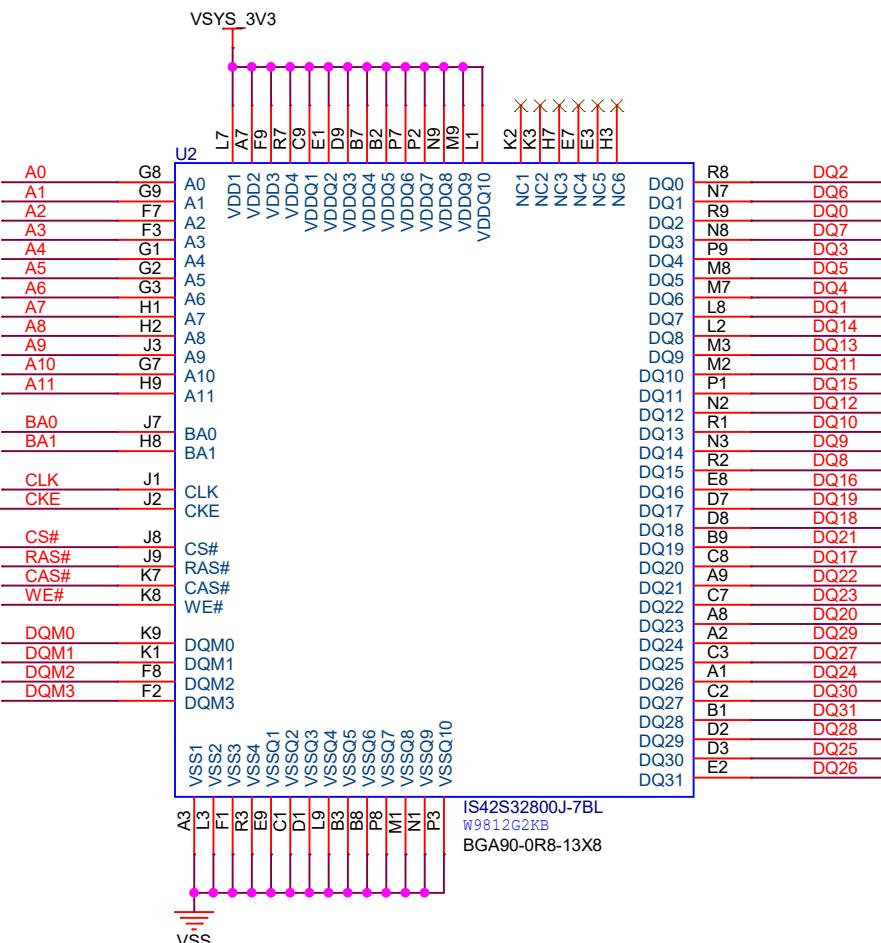
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| A4                                  | USB HS          | V1.1 |
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# SDRAM



layout: SDRAM走线控阻抗50R:

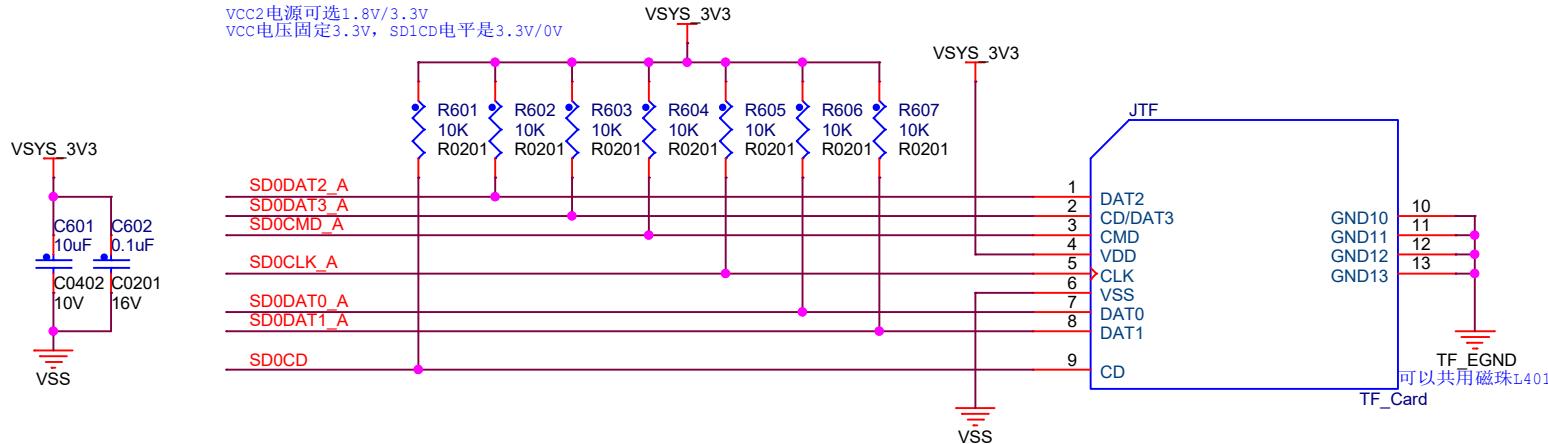


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Title  
野火瑞萨CPKHM1-RA8P1K核心板

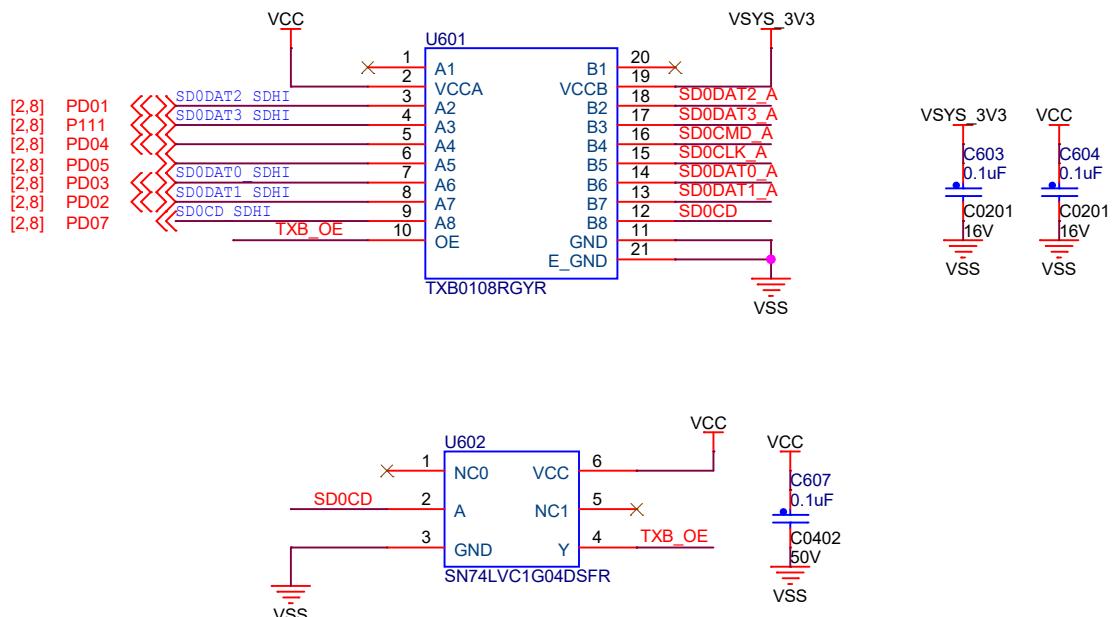
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|-------------------------------------|-----------------|------|
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## SD卡

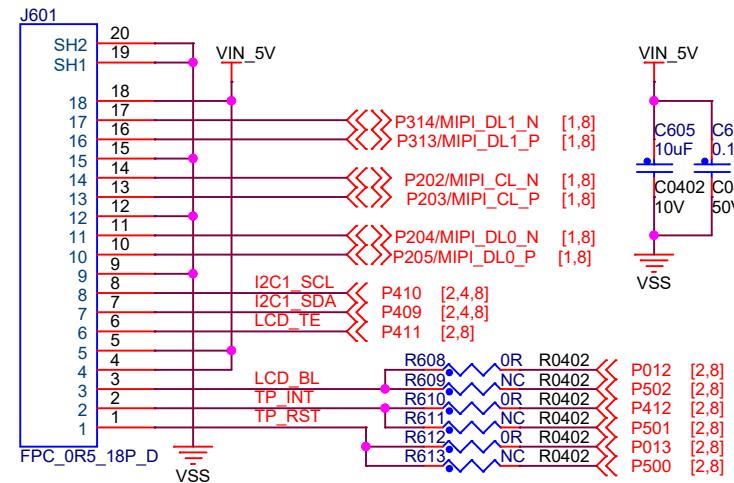


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## SD卡电平转换

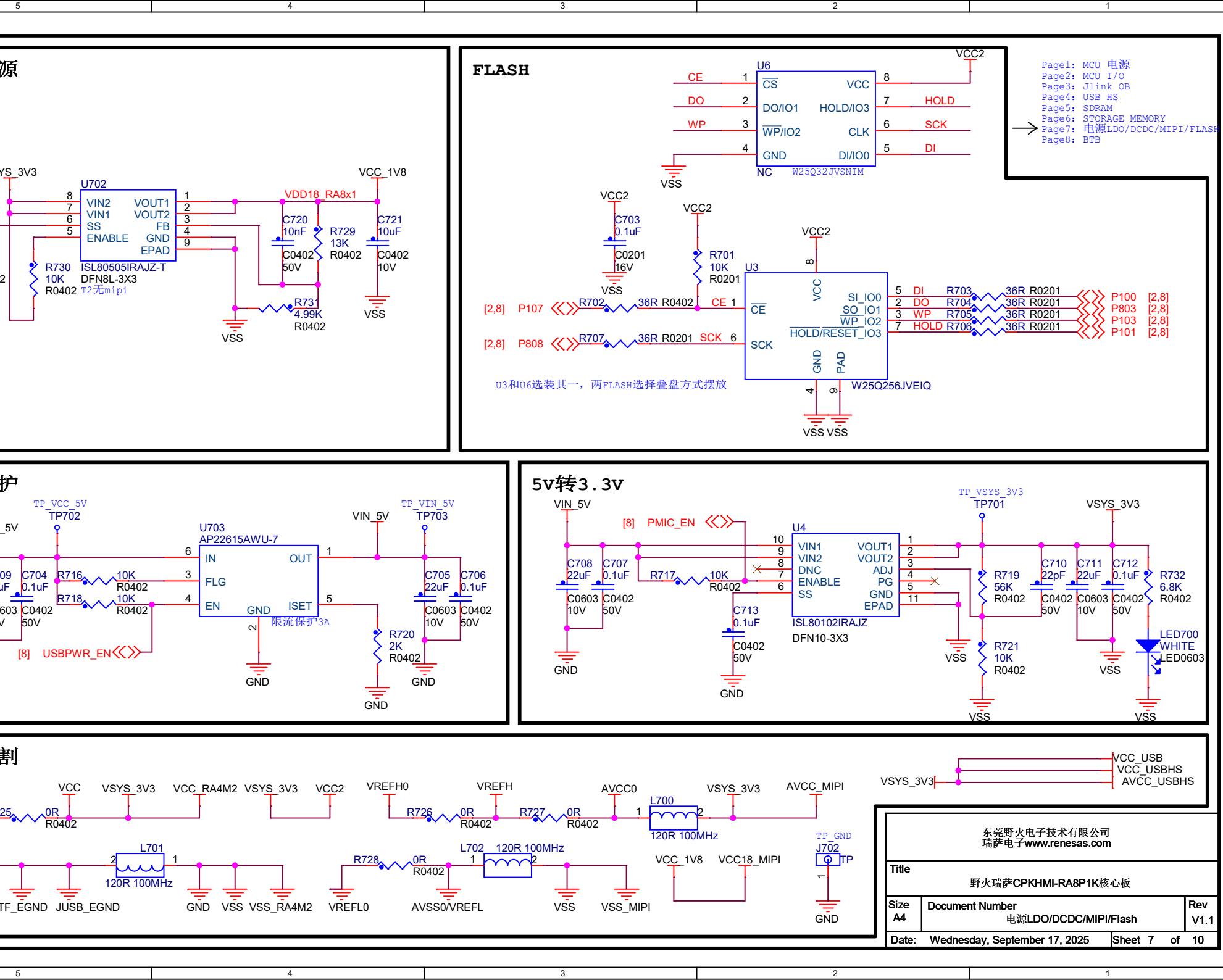


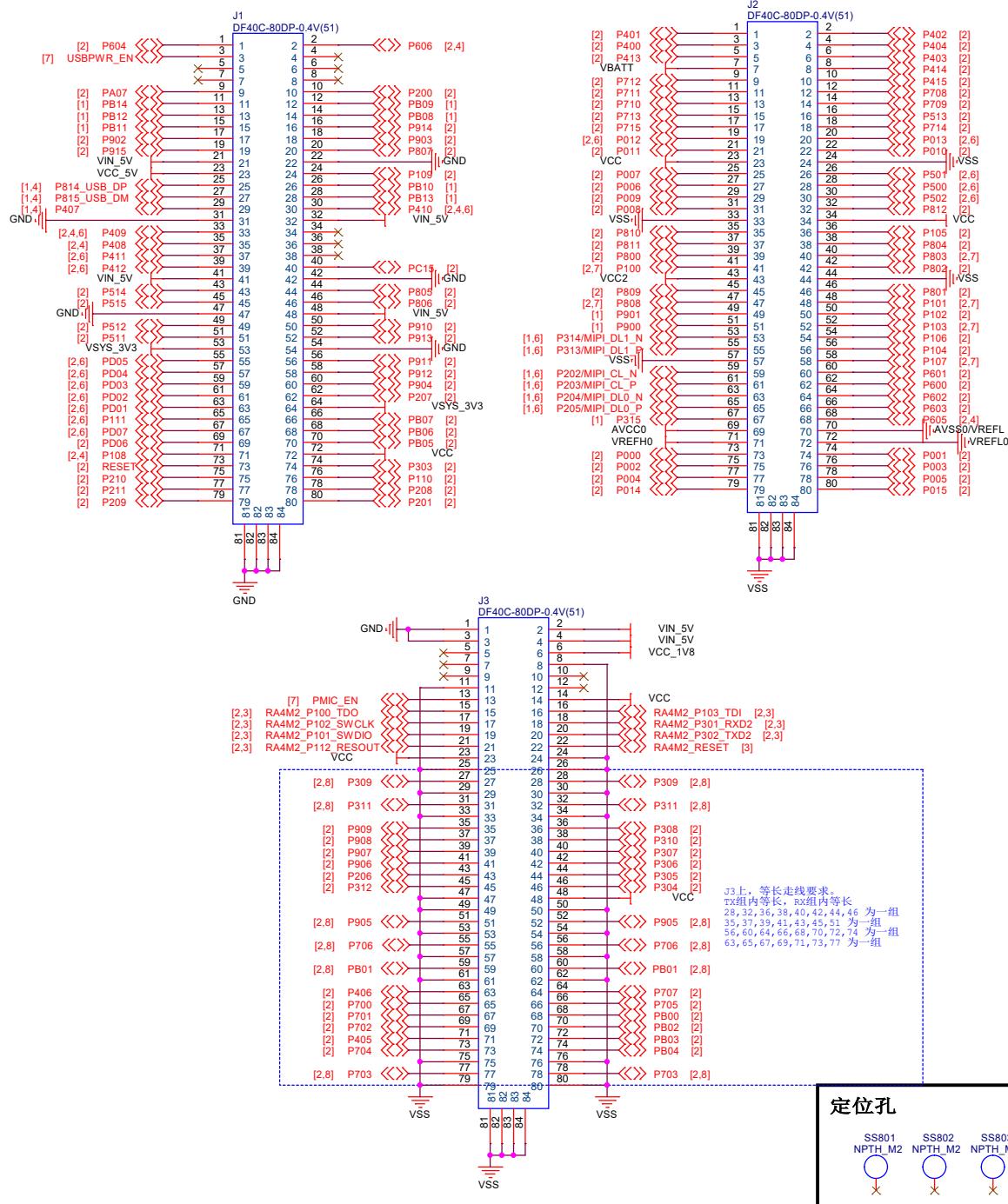
## MIPI屏



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|-------------------------------------|-----------------|----------------------|-------|
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## 历史版本

| 版本号  | 日期         | 设计  | 描述          |
|------|------------|-----|-------------|
| V1.0 | 2025-08-28 | LPC | 量产版本        |
| V1.1 | 2025-09-17 | LPC | 修正VCC_USB连接 |
|      |            |     |             |
|      |            |     |             |
|      |            |     |             |

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# 野火瑞萨CPKHMI-RA8P1K核心板\_原理图\_v1.1

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|-------|------------------------------|----------------|
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