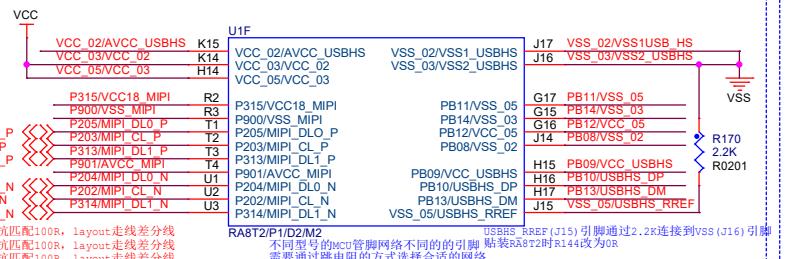
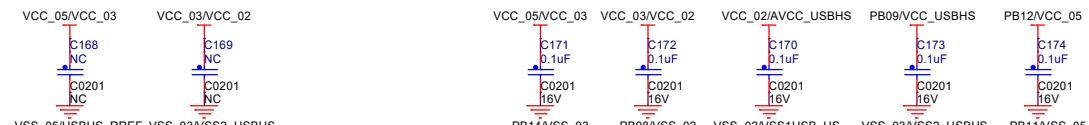


由于RA8P1/D2/M2和RA8T2的部分管脚网络不同，通过选装阻容的方式，应对不同型号的IC贴装。默认贴装RA8P1/D2/M2

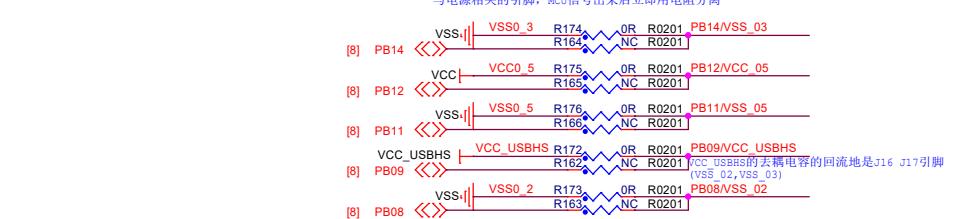


电阻选装

要贴的器件靠近MCU摆放，NC器件在MCU-BTB间任意摆放



与电源相关的引脚，MCU信号出来后立即用电阻分离

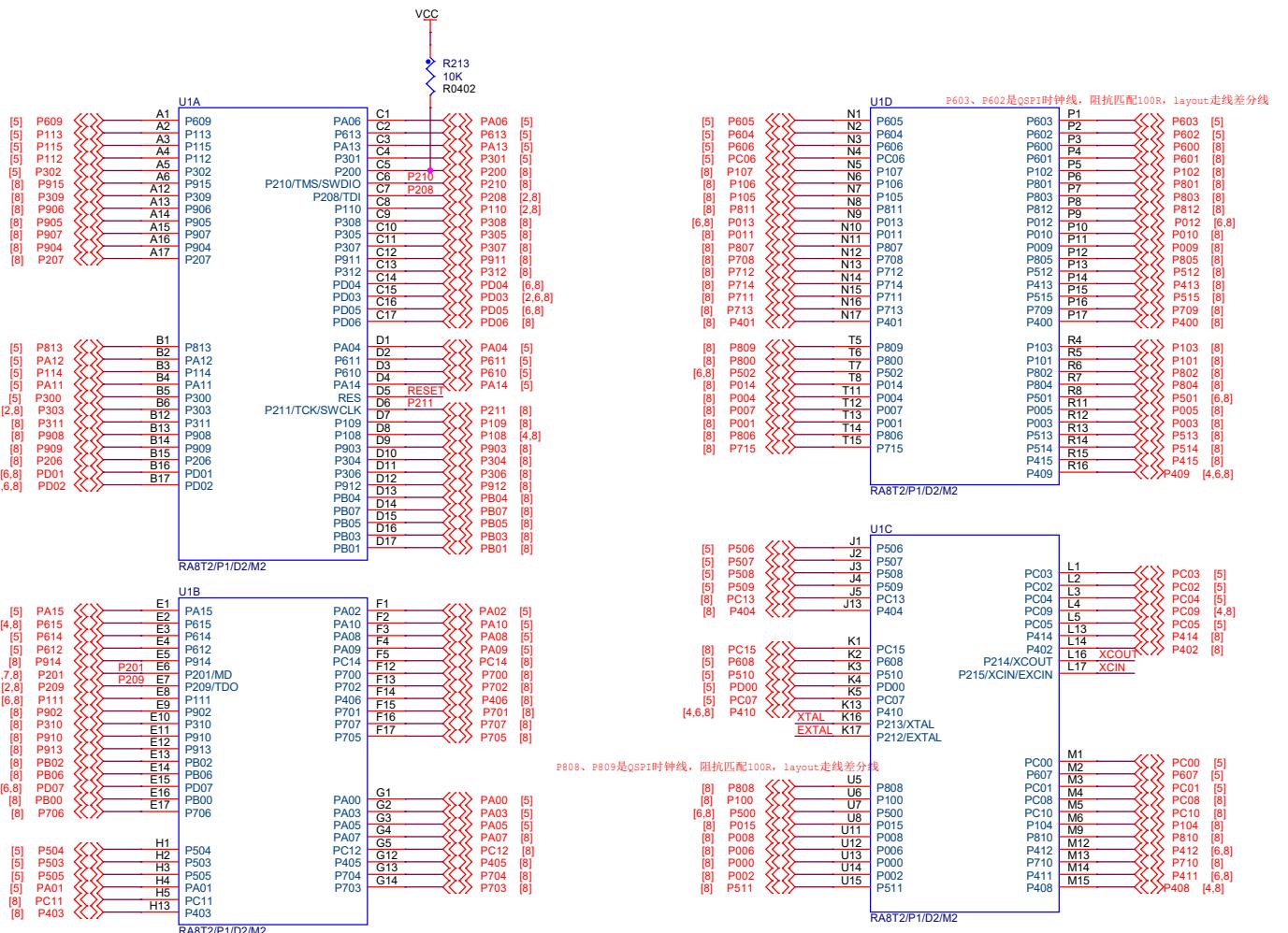


差分线走到J1(BTB)附近再用电阻分离。

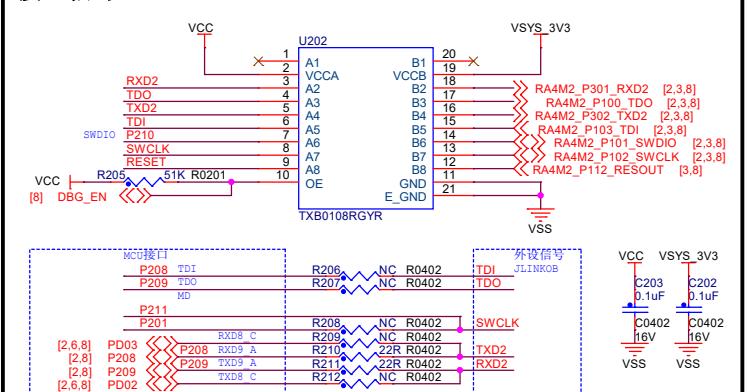


通孔拓展

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Title 野火瑞萨RA8P1/D2/M2-BTB核心板(PMIC)_原理图-供WS贴装用		
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Date: Wednesday, July 09, 2025		Sheet 1 of 11



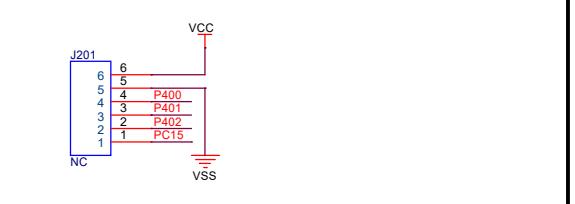
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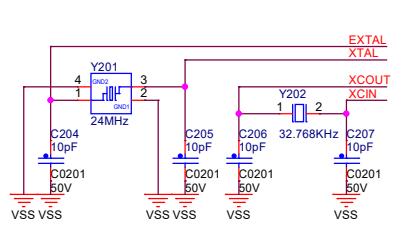
如果使用USB HS，主晶振只能选12/20/24/48MHz

通孔拓展

VIN有大电流需求,请用粗线或铜皮连到VIN网络。
T201, T202之间的间距请保证2.54mm的倍数



晶振

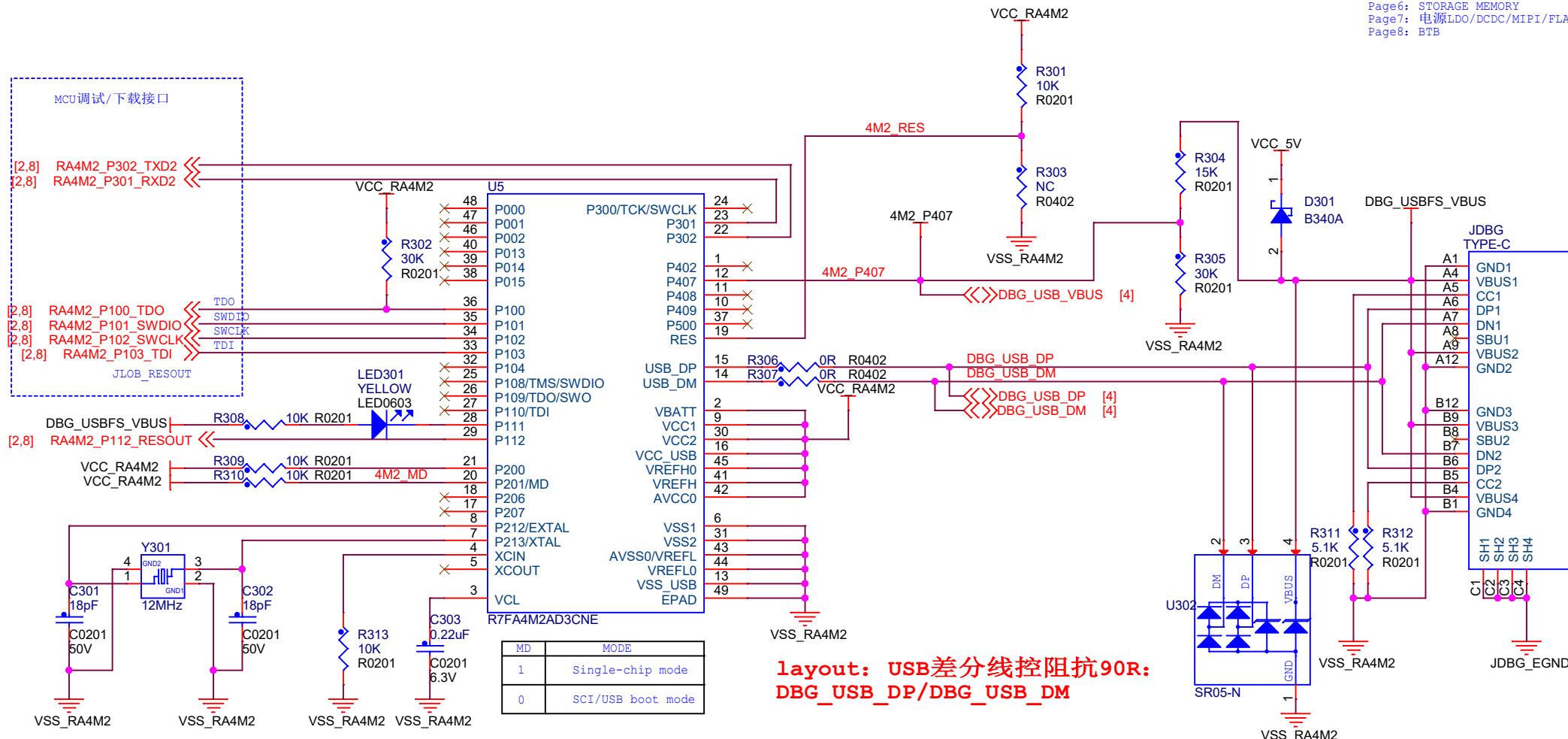


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https://yehuosm.tmall.com	
Title 野火瑞萨RA8P1/D2/M2-BTB核心板(PMIC)_原理图-供WS贴装用	
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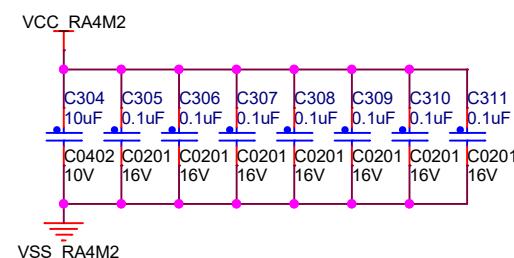
Jlink OB

注意：R7FA4M2只能12MHz晶振

Page1: MCU 电源
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Page5: SDRAM
Page6: STORAGE MEMORY
Page7: 电源/LDO/DCDC/MIPI/FLASH
Page8: BTB



测试点 1.25间距/预留



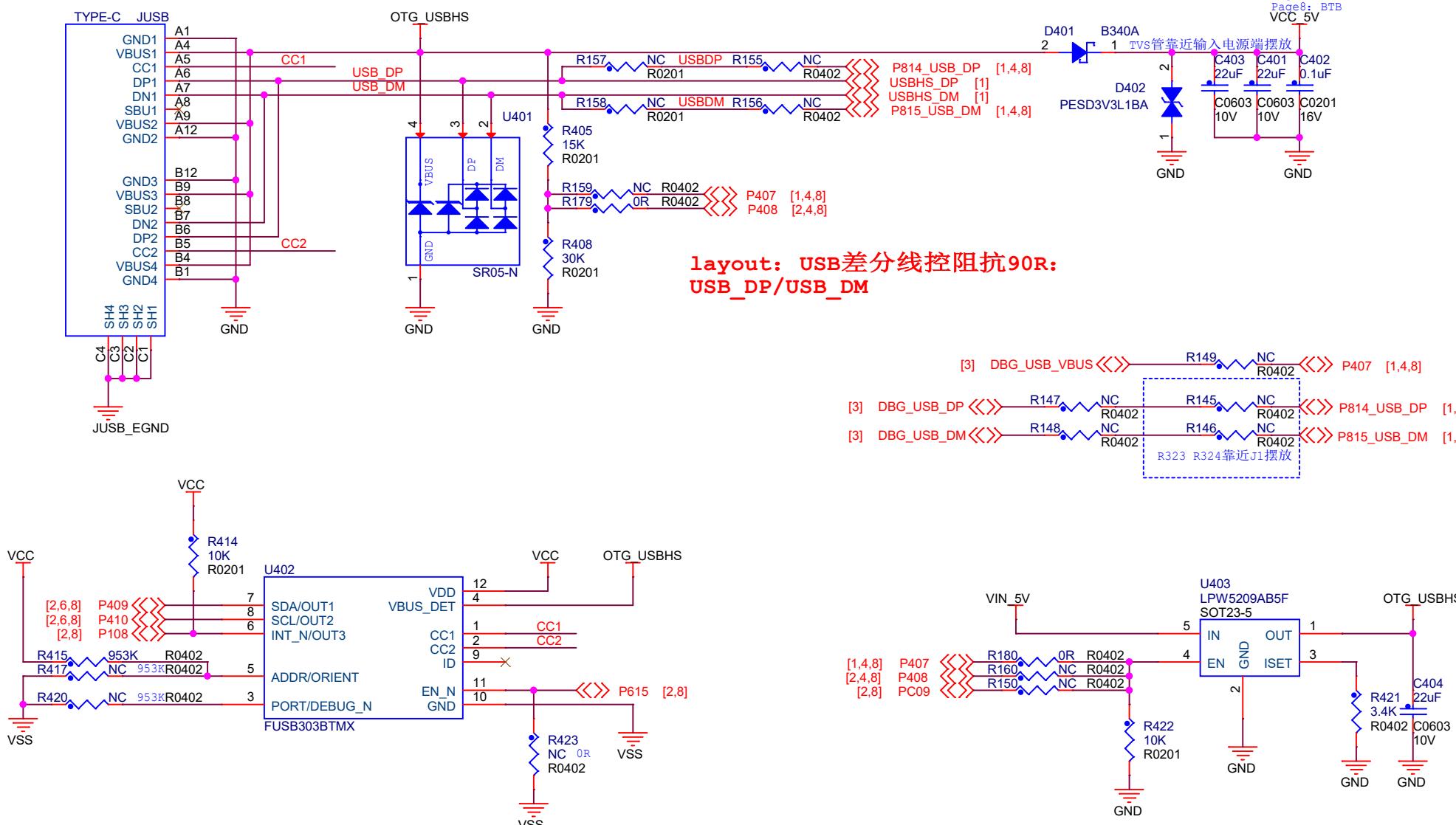
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USB HS

如果使用USB HS, 主晶振只能选12/20/24/48MHz

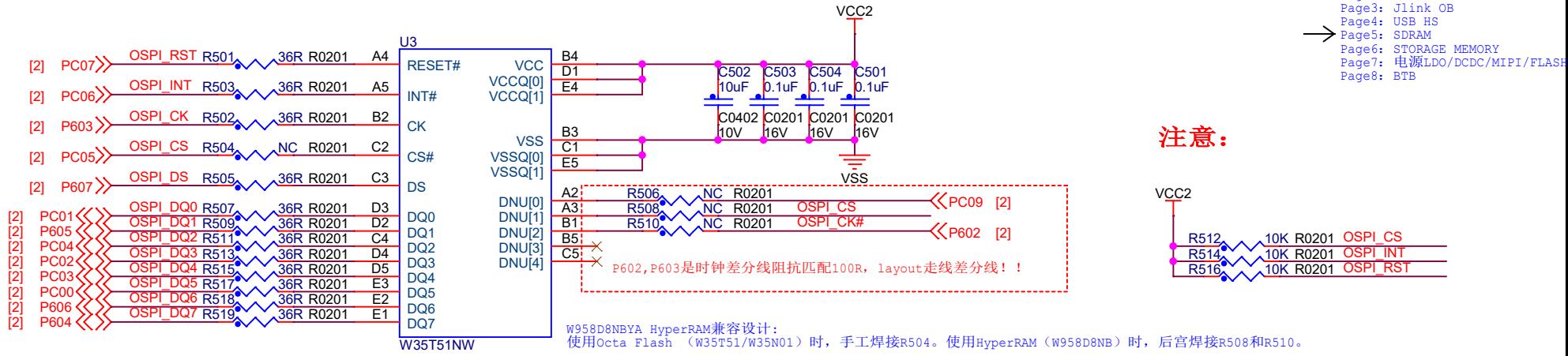
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 Page8: BTB
 VCC 5V



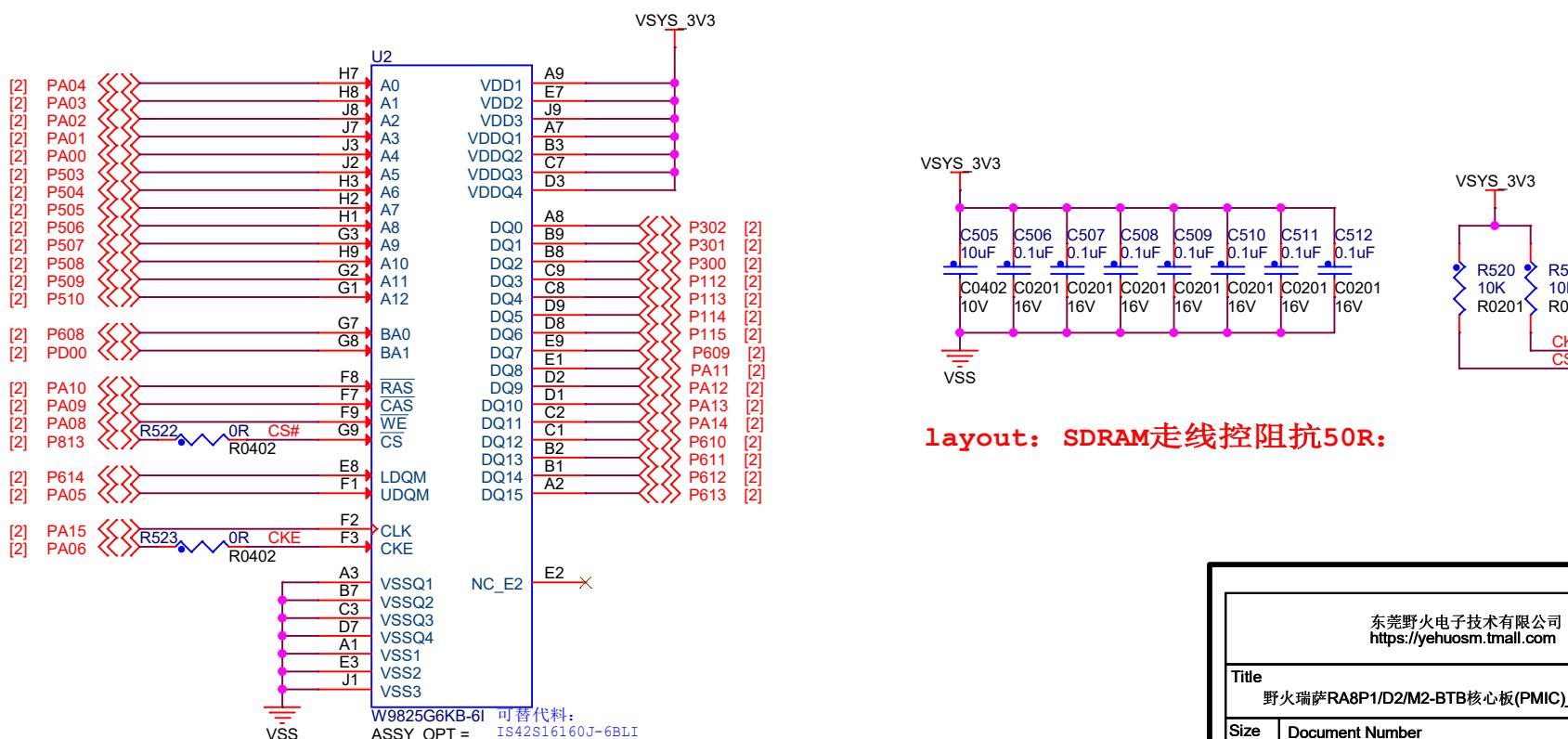
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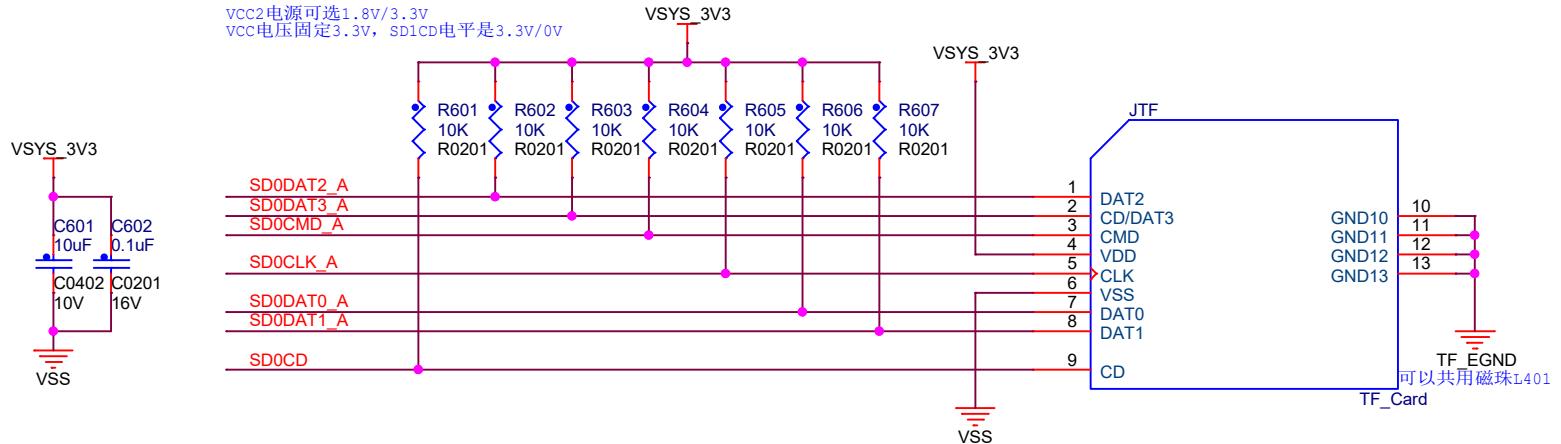
FLASH



SDRAM

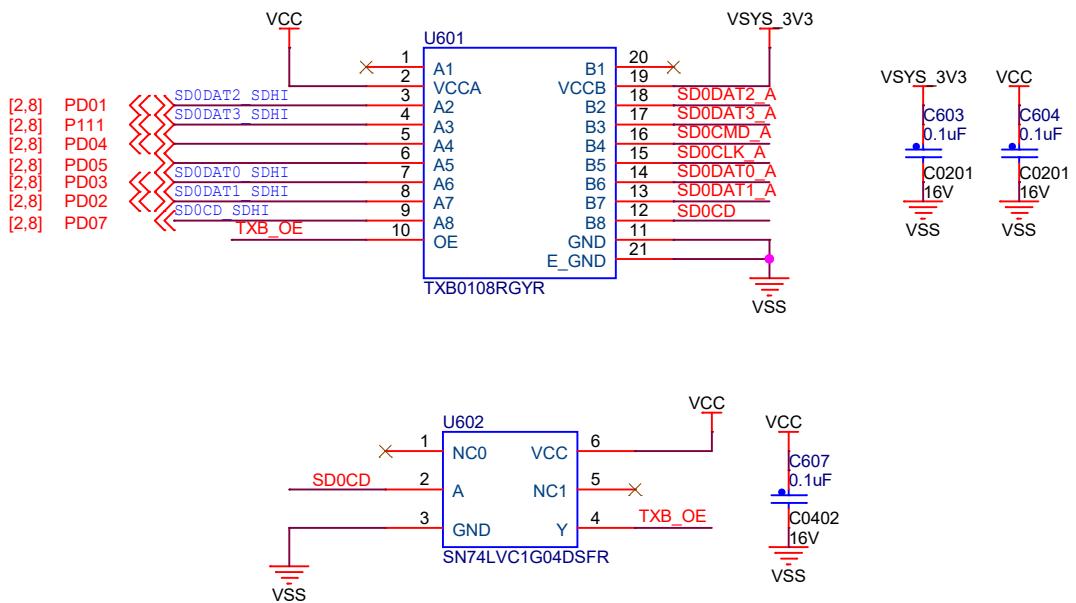


SD卡

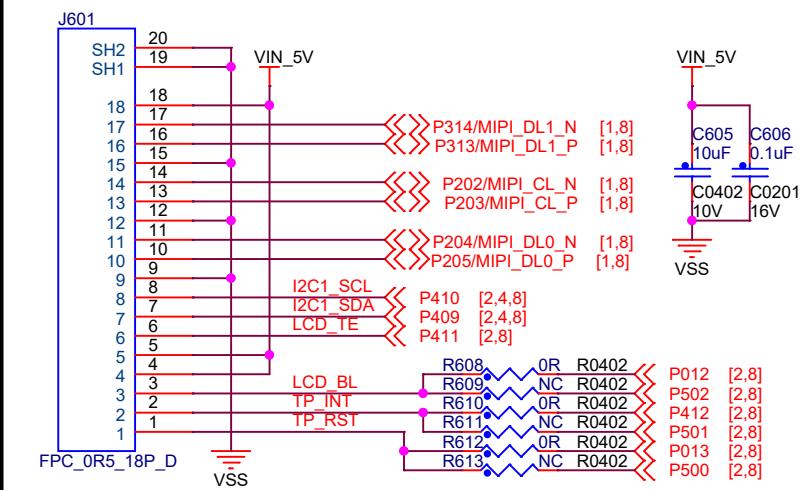


Page1: MCU 电源
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SD卡电平转换



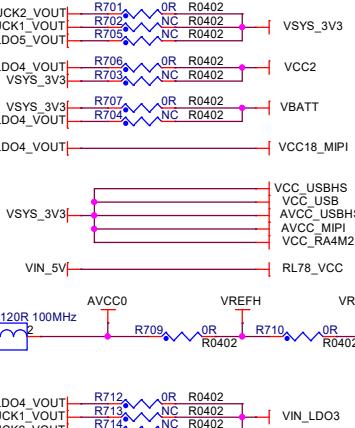
MIPI屏



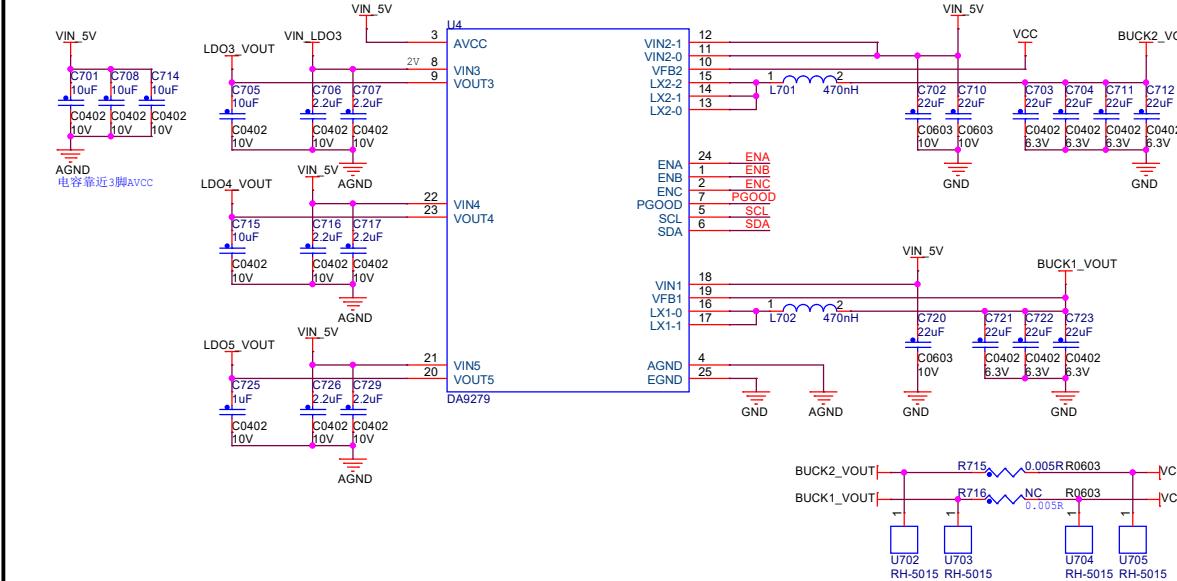
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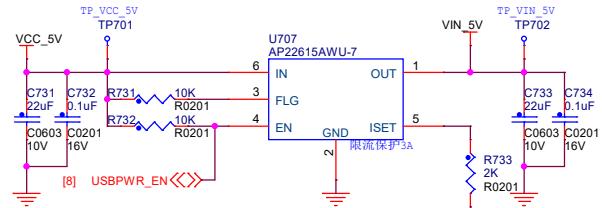
电源分割



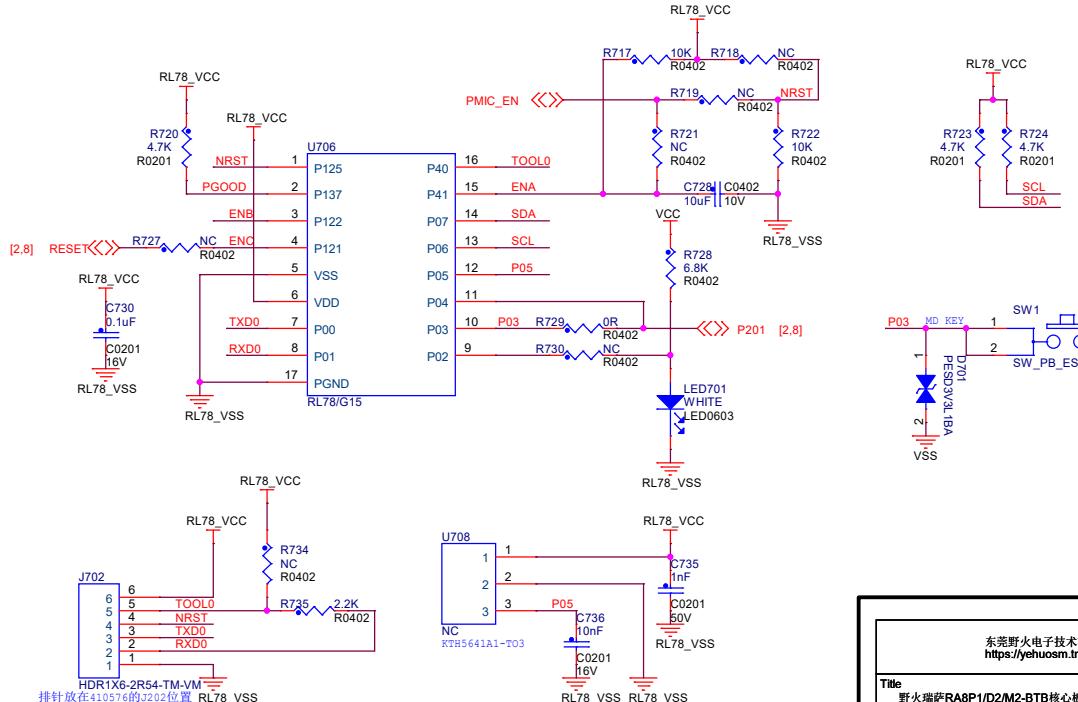
PMIC



电源保护



MCU



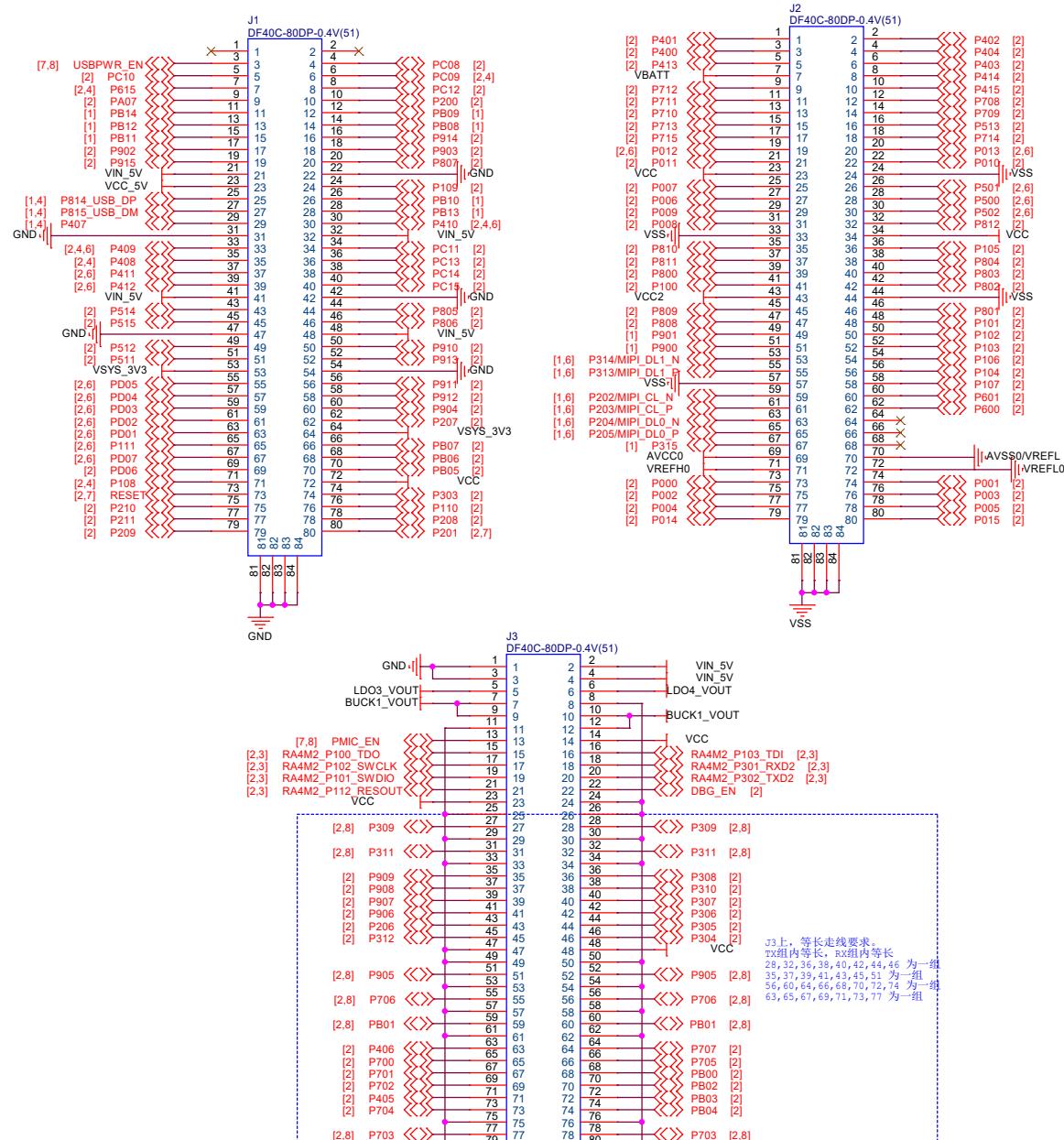
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FRA8B1/D2/M2 RTR核心板(RMIC) 原理图 供WS贴装用

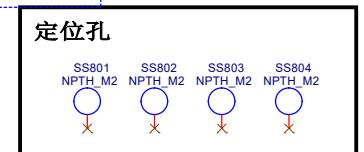
Document Number _____

STORAGE MEMORY

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J3上，等长走线要求。
TX组内等长，RX组内等长
28, 32, 36, 38, 40, 42, 44, 46 为一组
35, 37, 39, 41, 43, 45, 51 为一组
56, 60, 64, 66, 68, 70, 72, 74 为一组
63, 65, 67, 69, 71, 73, 77 为一组

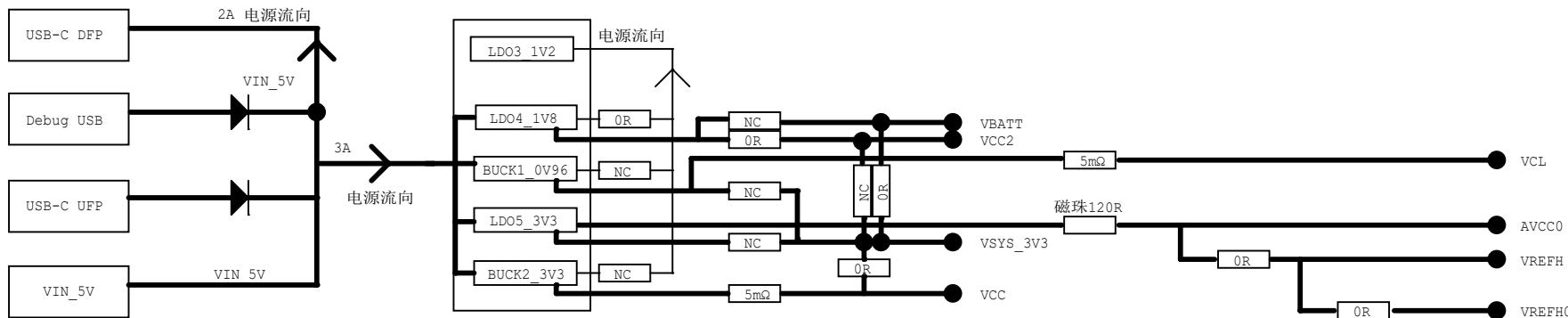


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RA8P1/D2/M2-BTB核心板(PMIC)_原理图-供WS贴装用

ment Number
BTB

电源拓扑图



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历史版本

版本号	日期	设计	描述
V0.1	2025-05-19	LPC	初始版本

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