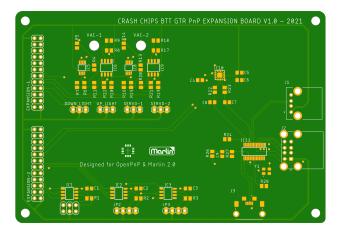
	EXPAN:	SION-1	
±5V	1_0	O^2	+12V
±3V3	<u>3</u> ○	O^4	GND
VAC_SIG_1	5	$\widetilde{\bigcirc}_{6}^{6}$	VAC_SIG_2
UP_LIGHT_SIG	70	O_8	DOWN_LIGHT_SIG
SERVO_1_SIG	9	<u></u>	<u>SERV</u> O_2_SIG
	11_	<u></u>	
	13.	<u>~</u> 14	
	15_	<u>~</u> 16	
	17	∠18	
GND	19	20	GND
GND	21	22	+3V3
+12V	23	24	+5V
		Ů.	

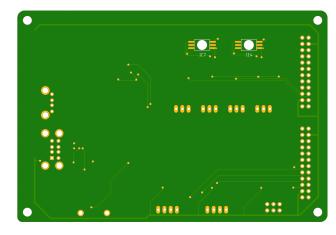
EXPANSION-2					
±5V	1	Ω^2	+12V		
±3V3	3	\sim 4	GND		
485_1_RXI	5.	$\stackrel{\frown}{\sim}_{6}$	<u>485_</u> 3_RXI		
485 1 RTS	7	\sim 8	485_3 RTS		
485_1_TXO	9	10	485_3_TXO		
485_2_RXI	11	12	485_2_RTS		
485_2_TXO	13	<u>C</u> 14			
MCU_UART_TX	15	<u>~</u> 16			
MCU_UART_RX	17	<u>~</u> 18			
GND	19	20	GND		
GND	21	22	+3V3		
+12V	23	<u>24</u>	+5V		
		\circ			

CRASH CHIPS BTT GTR PnP EXPANSION BOARD V1.0 - 2021

Expansion Header, See PnP-Interface for Interfacting this board to the BTT GTR 1.0

Board Layout





EXPANSION HEADER

TITLE: PnP-Expansion-V1

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