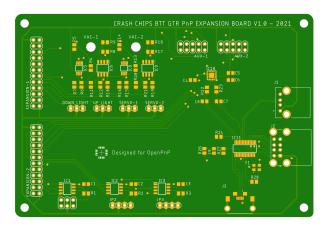
EXPANSION-1 ±5V ±3V3 VAC_SIG_1 UP_LIGHT_SIG +12V GND VAC_SIG_2 DOWN_LIGHT_SIG SERVO_2_SIG AUX_1_D1 AUX_1_A1 SERVO_1_SIG SDA SCL MOSI AUX_1_A2 MISO G N D SCK GND GND +3V3 +12V +5V

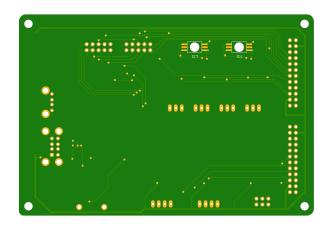
EXPANSION-2			
±5V	1_	\bigcirc^2	+12V
±3V3	3	$\overset{4}{\sim}$	GND
485_1_RXI	5	\sim 6	485_3_RXI
485_1_RTS	7	\sim 8	485_3_RTS
485_1_TXO	9	210	485_3_TXO
485_2_RXI	11	12	<u>485_</u> 2_RTS
485_2_TXO	13	<u></u>	AUX_ 2_D1
MCU_UART_TX	15	16	AUX_2_A1
MCU_UART_RX	17	18	AUX_2_A2
GND	19	20	GND
GND	21	22	+3V3
+12V	23	<u>~24</u>	+5V

CRASH CHIPS BTT GTR PnP EXPANSION BOARD V1.0 - 2021

Expansion Header, See PnP-Interface for Interfacting this board to the BTT GTR 1.0

Board Layout





EXPANSION HEADER

TITLE: PnP-Expansion-V1

Document Number: REV:

Date: not saved! Sheet: 1/4

