An Arduno(tri) Due compatible SMIX core board. Design goals - Board 12 - 2 layer SDCByrm more direction to take advantage of cheap PCB service Service contented board, as can be programmed and run with just USB cable - Compatible with "Due" board target in Advance USB cable - Compatible with "Due" board target in Advance USB cable - Compatible with "Due" board target in Advance USB cable - Compatible with "Due" board target in Advance USB cable - Compatible with "Due" board target in Advance USB cable - Compatible with "Due" board target in Advance USB cable - Compatible with "Due" board target in Advance USB cable - Compatible with "Due" board target in Advance or extend Symptomic USB cable or the state of the state o	CoreSam3x			
Basel 1s 2 layer 50-50mm max dimension to bate advantage of cheep PCB service. - Compensate yet Differently 603 or larger - Self contained board, so can be programmed and run with just USB cable - Composition with "Disk board bagels Andrean (D.C. no extra support required - O.J.* pitch jola spacing - O.J.* pitch jola spacing - all Due pins - some extra GPIO extra pins - some extra GPIO extra pins - safety backup pins - 1186 pins - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board USB (OIG, is, device or host) - on board S.V. regulator - IED for S.V. power, 0.33 - NIC crystal - IED for S.V. power, 0.35 - Vice (SI, V.) (SI, V.	An Arduino(tm) Due compatible SAI	M3X core board.		
Flex Power. Sheet: P	Design goals			
Features: - all Due pins - some extra GPL0 extra pins - some extra GPL0 extra pins - Blattery beckup pins - Blatt	 Board is 2 layer 50x50mm max Components are DIY friendly 060 Self contained board, so can be Compatible with "Due" board tale Programmed via USB, JTAG pins 0.1" pitch pin spacing 	dimension to take advantage of cheap PCB servi 03 or larger : programmed and run with just USB cable rget in Arduino IDE, no extra support required or 6 pin serial programming header	ce.	
- dimensions: 2° by 16° (51x41mm) excluding mounting lugs "Arduino" is a trademark of Arduino LLC http://arduino.cc/ Sheet: Power Sheet: Processor File: Power.sch File: CoreSam3x.sch Sheet: /	Features: - all Due pins - some extra GPIO extra pins - Ethernet pins - Battery backup pins - JTAG pins - EBI pins - on board USB (OTG, ie. device of the company of	or host) e or external 5V		
Sheet: Processor File: Processor.sch	two mounting lugs for M2.5dimensions : 2" by 1.6" (51x41	mm) excluding mounting lugs		
Some schematics and library symbols derived from Duet project https://github.com/T3P3/Duet licensed under the CERN OHW Licence volume by the company of the	"Arduino" is a trademark of Arduin	o LLC http://arduino.cc/		
M2 OSHW_logo_2 LOGO_OSHW PCB_GREEN_RMC W1 PCB_GREEN_RMC W2 License: GPL v2 File: CoreSam3x.sch Sheet: /				
Some schematics and library symbols derived from Duet project https://github.com/T3P3/Duet licensed under the CERN OHW Licence v Begreen_RMC License: GPL v2 File: CoreSam3x.sch Sheet: /	Sheet: Power	Sheet: Processor		
OSHW_logo_2 LOGO_OSHW PCB_GREEN_RMC License: GPL v2 File: CoreSam3x.sch Sheet: /	Sheet: Power	Sheet: Processor		
License: GPL v2 File: CoreSam3x.sch Sheet: /	Sheet: Power	Sheet: Processor	→ H1	
Sheet: /	Sheet: Power	Sheet: Processor File: Processor.sch M2 0\$HW_logo_2	M1	Some schematics and library symbols derived from Duet project https://github.com/T3P3/Duet licensed under the CERN OHW Licence v1.
	Sheet: Power	Sheet: Processor File: Processor.sch M2 0\$HW_logo_2	M1	Some schematics and library symbols derived from Duet project https://github.com/T3P3/Duet licensed under the CERN OHW Licence v1.
	Sheet: Power	Sheet: Processor File: Processor.sch M2 0\$HW_logo_2	M1 CB_GREEN_RMC	License: GPL v2 File: CoreSam3x.sch



