





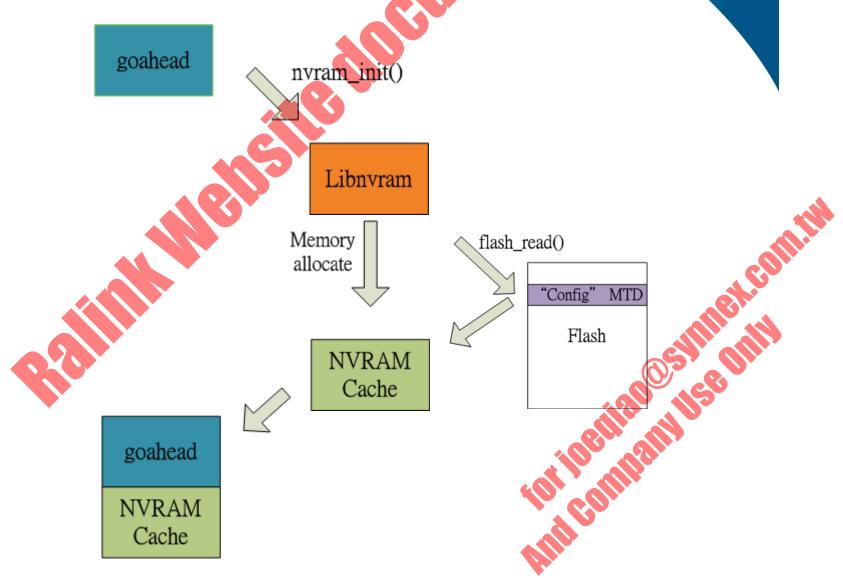
Original Design

- User-space library
- Provide APIs for NVRAM operations
- Allocate a user-space memory region to store NVRAM data from Flash, and each user-space program has individual one
- Data is stored in ASCII format
- API:

nvram_init, nvram_get, nvram_bufget, nvram_set, nvram_bufset,nvram_commit, nvram_close, nvram_clear



Original Design: nvram_init()





Original Design: nvram_bufget()

goahead

NVRAM Cache

wan_ipaddr=192.168.1.1 wan_netmask=255.255.255.0

wan_gateway=

wan_primary_dns=9.9.9.9

wan_secondary_dns=5.5.5.5

wan_pppoe_user=pppoe_user

wan_pppoe_pass=pppoe_passwd wan_12tp_server=12tp_server

Wan_12tp_user=12tp_user

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1..

nvram_bufget(wan_ipaddr)

Libnvram

nvram_bufget(wan_ipaddr) = 192.168.1.1



Original Design: nvram bufset()

goahead

NVRAM Cache

wan ipaddr=192.168.1.1

wan_netmask=255.255.255.0

wan gateway=

wan primary dns=9.9.9.9

wan_secondary_dns=5.5.5.5

wan_pppoe_user=pppoe_user

wan_pppoe_pass=pppoe_passwd wan_12tp_server=12tp_server

Wan_12tp_user=12tp_user

nvram_bufset(wan_ipaddr, 10.10.10.254)

Libnyram

goahead

NVRAM Cache

wan_ipaddr=10.10.10.254

wan_netmask=255.255.255.0

wan_gateway=

wan_primary_dns=9.9.9.9

wan_secondary_dns=5.5.5.5

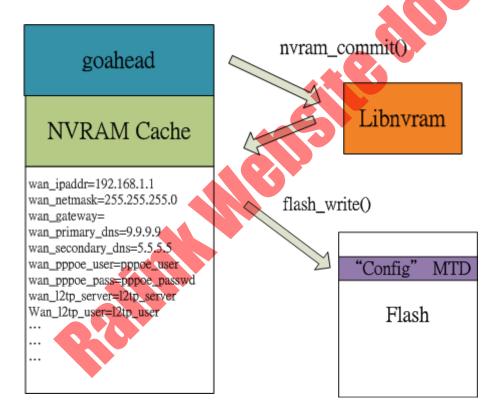
wan_pppoe_user=pppoe_user

wan pppoe pass=pppoe passwd wan 12tp_server=12tp_server

Wan 12tp_user=12tp_user



Original Design: nvram_commit()





Original Design: nvram close()

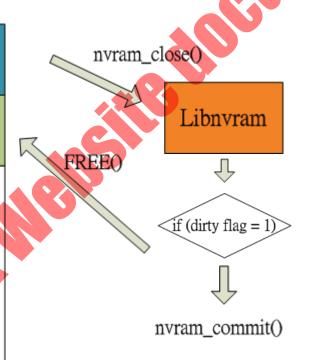


NVRAM Cache

wan_ipaddr=192.168.1.1
wan_netmask=255.255.255.0
wan_gateway=
wan_primary_dns=9.9.9.9
wan_secondary_dns=5.5.5.5
wan_pppoe_user=pppoe_user
wan_pppoe_pass=pppoe_passwd
wan_12tp_server=12tp_server
Wan_12tp_user=12tp_user
...



goahead





Original Design: nvram get()

```
char *nvram_get(int index, char *name)
{
          //LIBNV_PRINT("--> nvram_get\n");
          nvram_close(index);
          nvram_init(index);
          return nvram_bufget(index, name);
}
```

Sync data with Flash and return the freshest value

value

Antingilian



Original Design: nvram_set()

Set the value and commit current data to Flash



NVRAM Cache

wan_ipaddr=192.168.1.1 wan_netmask=255.255.255.0 wan_gateway= wan_primary_dns=9.9.9.9 wan_secondary_dns=5.5.5.5 wan_pppoe_user=pppoe_user wan pppoe pass=pppoe passwd wan_12tp_server=12tp_server Wan_l2tp_user=l2tp_user

1. nvram_bufset(wan_ipaddr, 10.10.10.254)

3. nvram_bufget(wan_netmask) = ?

TR069

NVRAM Cache

wan_ipaddr=192.168.1.1 wan_netmask=255.255.255.0 wan_gateway= wan_primary_dns=9.9.9.9 wan_secondary_dns=5.5.5.5 nvram_bufget(wan_ipaddr) = ?

nvram_bufget(wan_netmask) = ? wan_pppoe_user=pppoe_user

2. nvram bufset(wan netmask, 255.0.0.0)

Ralink

System



Solutions?

- Use nvram_get() and nvram_set() instead
 - nvram_commit() takes long time (flash erase and write)
 - Not efficient

Notification

- Need to know which program needs to be notified(is using NVRAM)
- Other program may not be interested in it

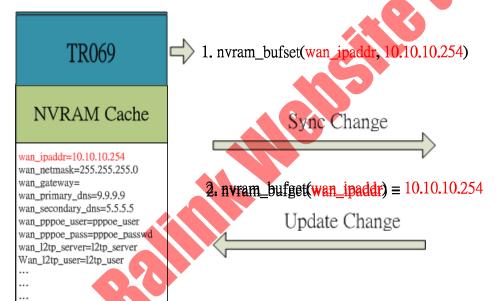
Centralized Database

- Every NVRAM user synchronize changes with it
- Keep the freshest data

using California Calif



New Design



Kernel

NVRAM Cache

wan_ipaddr=10.10.10.254
wan_netmask=255.255.255.0
wan_gateway=
wan_primary_dns=9.9.9.9
wan_secondary_dns=5.5.5.5
wan_pppoe_user=pppoe_user
wan_pppoe_pass=pppoe_passwd
wan_l2tp_server=l2tp_server
Wan_l2tp_user=l2tp_user
...

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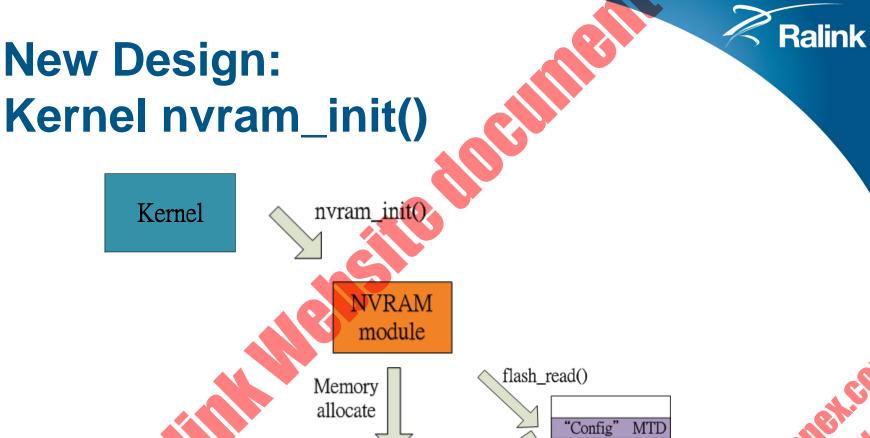
New Design: nvram_init()

 Kernel NVRAM module will invoke nvram_init() at the boot time which does the same thing with previous user-mode nvram_init()

 User-space program also needs to call libnvram nvram_init(), but it gets the NVRAM data from Kernel NVRAM cache instead, not directly from Flash.

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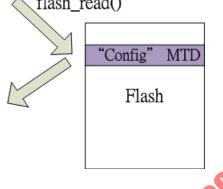


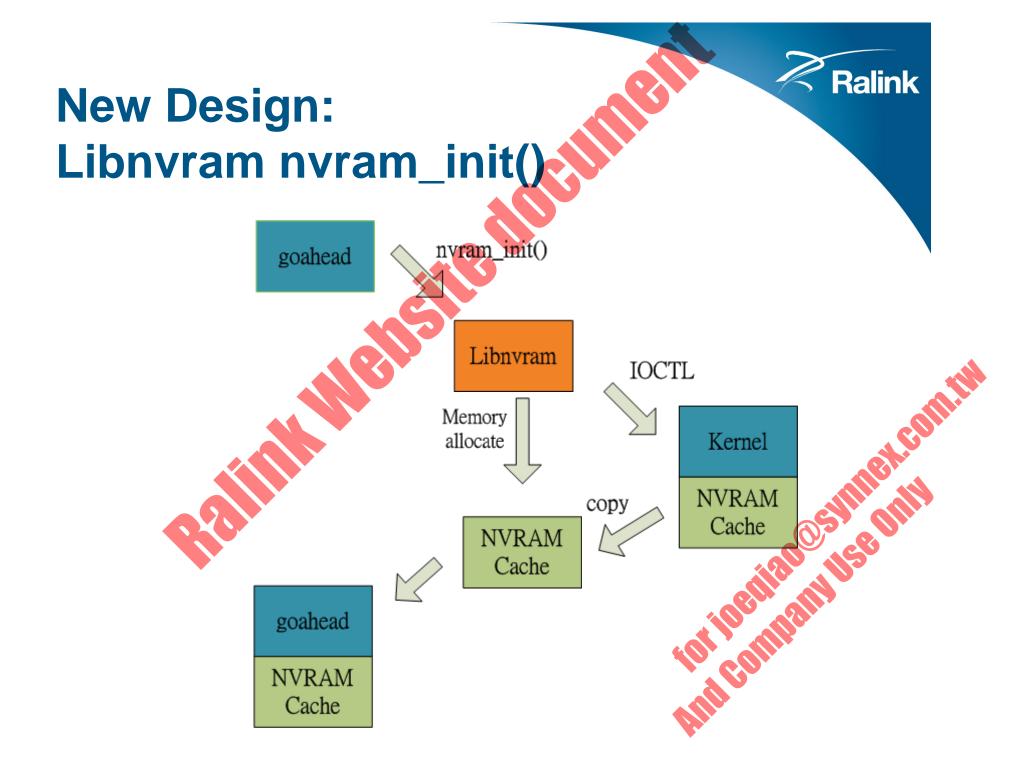
NVRAM

Cache

Kernel

NVRAM Cache









NVRAM Cache

wan_ipaddr=10.10.10.254

wan_netmask=255.255.255.0
wan_gateway=
wan_primary_dns=9.9.9.9
wan_secondary_dns=5.5.5.5
wan_pppoe_user=pppoe_user
wan_pppoe_pass=pppoe_passwd
wan_l2tp_server=l2tp_server
Wan_l2tp_user=l2tp_user

...

nvram_bufget(wan_ipaddr)



IOCTL

NVRAM Cache

nvram_bufget(wan_ipaddr) = 10.10.10.254 wan_ipaddr=10.10.10.254
wan_netmask=255.255.255.0
wan_gateway=
wan_primary_dns=9.9.9.9
wan_secondary_dns=5.5.5.5
wan_pppoe_user=pppoe_user
wan_pppoe_pass=pppoe_passwd
wan_12tp_server=12tp_server
Wan_12tp_user=f2tp_user
...

'an_t2tp_user=t2tp_user .

Kernel

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NVRAM Cache

wan_ipaddr=192.168.1.1 wan_netmask=255.255.255.0 wan_gateway= wan_primary_dns=9.9.9.9 wan_secondary_dns=5.5.5.5 wan_pppoe_user=pppoe_user wan_pppoe_pass=pppoe_passwd wan_12tp_server=12tp_server Wan_12tp_user=12tp_user nvram_bufset(wan_ipaddr, 10.10.10.254)



IOCTL

Kernel

NVRAM Cache

wan_ipaddr=10.10.10.254

wan_netmask=255.255.255.0

wan_gateway=
wan_primary_dns=9.9.9.9
wan_secondary_dns=5.5.5.5
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wan_pppoe_pass=pppoe_passwd
wan_12tp_server=12tp_server
Wan_12tp_user=12tp_user

...

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NVRAM Cache

wan_ipaddr=192.168.1.1

wan_netmask=255.255.255.0 wan_gateway=

wan_primary_dns=9.9.9.9 wan_secondary_dns=5.5.5.5

wan_pppoe_user=pppoe_user

wan_pppoe_pass=pppoe_passwd wan_l2tp_server=l2tp_server

Wan_12tp_user=12tp_user

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nvram_commit()

Libnvram

IOCTL

flash_write()

"Config" MTD

Flash

Kernel

NVRAM Cache

wan_ipaddr=10.10.10.254

wan_netmask=255.255.255.0

wan_gateway=

wan_primary_dns=9.9.9.9

wan_secondary_dns=5.5.5.5

wan_pppoe_user=pppoe_user

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Wan_12tp_user=12tp_user

...

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New Design: Libnvram nvram_commit()

- Which copy of NVRAM cache will be committed to Flash?
 - → Kernel NVRAM cache
- Why?
 - → Kernel NVRAM cache keeps the freshest data



Code Review

New added files:

- source/linux-2.6.21.x/arch/mips/rt2880/nvram.c
- source/linux-2.6.21.x/arch/mips/rt2880/nyram.h
- source/linux-2.6.21.x/arch/mips/rt2880/crc32.c

Modified files:

- source/lib/libnvram/nyram_env.c
- source/lib/libnvram/nvram.h
- source/vendors/Ralink/RT3052/makedevlinks



Test Scenario

Memory leak

- 1. Program A keeps calling nvram_bufget()
- Program B keeps calling nvram_bufset()
- 3. Use "free" command to check if system free memory is decreasing

Kernel NVRAM semaphore

- 1. Program A calls X₁ times nvram_bufget(), Y₁ times nvram_bufset(), and Z₁ times nvram_commit()
- 2. Program A calls X₂ times nvram_bufget(), Y₂ times nvram_bufset(), and Z₂ times nvram_commit()
- 3. Increase global counter in Kernel NVRAM critical setction
- 4. Check if the value of global counter = $X_1 + X_2 + Y_1 + Y_2 + Z_1 + Z_2$



Test Scenario

Synchronization

- Program A and Program B increase a nyram value in turn
- Program A increase X times, Program B increase Y times
- Check if that nvram value equals to (X + Y)

