

Technical Report: Secure End-to-End Publicly Auditable 2PC

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1 Introduction

Secure Multiparty Computation (MPC) allows n parties P_1, \dots, P_n to securely evaluate a joint function $f(x_1, \dots, x_n)$ while keeping the secret input x_i private to its owner P_i . MPC comes with two main flavors of security [18], namely *Semi-honest* and *Active Security*:

Semi-Honest (SEM) only guarantees security if the adversary acts according to the protocol, i.e. one will not deviate from the protocol but only tries to break privacy from the MPC transcript that is available to it.

Active Security (ACT) guarantees the security of the MPC protocol even if the adversary acts arbitrarily. This is a stronger security setting.

Generic MPC is enabled through two approaches: Secret-Sharing (SS) and Garbled-Circuit [18] (GC). Our focus is on the latter due to its attractive property of constant round complexity.³

Efficiency of Actively Secure Garbled Circuit. ACT for GC is trivially available through the usage of Zero-Knowledge-Proof [22] yet even with optimizations it is necessary to prove a statement with 19 clauses conjunction per gate. Cut-and-Choose [18] is another approach for ACT in which the Garbler prepares k circuits in total and the Evaluator can choose t to test and only accepts if all t circuits are valid. Yet the preparation of many circuits and the testing for validity yield concrete inefficiency and become unreasonable when the circuit size is large. Another approach is through Authenticated Garbling [28] which will be our focus due to its efficiency compared with the previous two.⁴

Yet, these classical flavors of security only take into consideration the parties that participate in the MPC protocol itself, i.e. only P_1, \dots, P_n can verify the security of the MPC protocol.

³ Round complexity is an important factor for blockchain model due to bottleneck in block generation time.

⁴ We defer the efficiency analysis of such approaches in blockchain model to later of this Section. The concrete efficiency of this approach will be detailed in the Preliminary Section.

Verifiable MPC. We consider a somewhat “added-on” setting, in which, an external party, let us call it Verifier, can verify the security of the MPC protocol, and we call protocols in this setting Verifiable MPC (V-MPC), the security verification can come in 4 flavors:

Designated Verifier (DV-MPC): A single Verifier who potentially has some private state can verify the protocol execution to be secure.

Collaborative Verifiable (CV-MPC) A set of Verifiers who potentially have some private states can jointly (according to some access structure) verify the security of the protocol.⁵

Multi Verifier (MV-MPC) A set of Verifiers who potentially have some private states can individually verify the protocol’s security.⁶

Publicly Verifiable (PV-MPC) Any external Verifier without any secret state can verify the security of the protocol (possibly with aid from some CRS).

We focus on the last setting which is also the strongest one, i.e. Publicly Verifiable MPC.

Verifiability vs Auditability. Without regard to the verifiability flavor, to make an MPC verifiable, there are two different approaches:

Verifiable Output (VO) The output of the MPC is made verifiable.

Auditable Transcript (AT) The transcript of the MPC is made verifiable.

We distinguish that the difference is that the VO flavor is independent of the MPC protocol while the VT one is dependent on the MPC protocol itself.

Example.[*Verifiability vs Auditability*:] Let us consider the following Shuffle functionality.

The Shuffle functionality runs between n parties P_1, \dots, P_n each with a secret input x_1, \dots, x_n starting as an ordered list of $O_0 = \{x_1, \dots, x_n\}$. The functionality runs in n rounds, in each round R_i , party P_i sends a permutation π_i to Shuffle in which the functionality will apply to obtain $O_i = \pi_i(O_{i-1})$. Finally, the functionality outputs O_n .

Verifiability VO only asks for the quality of the output, i.e. that O_n is a permutation of O_0 while **Auditability** VT asks for the correctness of the process, i.e. that O_n is a permutation of O_0 through a series of permutation π_1, \dots, π_n .

Verifiable MPC. For VO-MPC, we can leverage Collaborative Zero-Knowledge Proof (co-ZKP, between P_1, \dots, P_n) to produce a ZK proof that the output $y = f(x_1, \dots, x_n)$ while keeping the x_1, \dots, x_n private to P_1, \dots, P_n . In the literature, co-ZKP can be achieved by thresholdizing any existing ZKP, demonstrated by Collaborative Zero-Knowledge Succinct Argument of Knowledge (co-zkSNARKs) [25]. Yet, there are potential Co-ZKP based on other ZKP such as: MPC-In-The-Head (MPCitH: seminal work IKOS07 [21], Other MPCitH techniques [13,23,10,27,19,12,1,2,14]), VOLE-IZK (DVZK with fast prover time and small memory: [29,17,9,32,30,5,16,31,6]), or VOLE-In-The-Head [7].⁷

⁵ CV-MPC can be considered as a thresholdized version of DV-MPC.

⁶ Such verification flavor is also called Crowd-Verifiable MPC [4].

⁷ VOLE-In-The-Head is the public coin Verifier version of VOLE-IZK

DV-MPC can leverage any private coin Verifier (Co-) ZKP, CV-MPC is achievable through thresholdizing the Verifier state in the private coin Verifier (Co-) ZKP, MV-MPC can be obtained by running multiple DV-MPC instances, and PV-MPC can utilize any public coin verifier ZKP.

Fortunately, all existing Co-ZKP is constant rounds protocols, thus the round complexity of the VO-MPC only depends on the underlying MPC protocol, i.e. a Secret-Sharing-based [18] (SS) VO-MPC will yield linear round complexity while a Garbled-Circuit-based [18] (GC) VO-MPC will have constant-round complexity.

Auditable Transcript MPC. For AT-MPC, we have to make the transcript auditable while keeping the privacy of the protocol execution. In general, for both SS-based and GC-based AT-MPC, the transcript can be made auditable through Additively-Homomorphic Commitment (COM, such as Pedersen, [8]) or Verifiable MAC (V-MAC, [28]) on each computation step. If the COM or V-MAC in the AT-MPC is public verifiable [7] then we have PV-MPC. COM-based has worse concrete efficiency than V-MAC-based AT-MPC due to the first operating on groups while the latter only operates on rings.

One can consider ACT a special case of DV/MV AT-MPC where the Verifier is the party in the MPC protocol. This means that if the ACT mechanism can be publicly verifiable, we obtain PV-MPC as well.

As in VO-MPC, an SS-based AT-MPC will yield linear round complexity while a GC-based VT-MPC can potentially yield a constant round complexity. This last one is also our main research question:

Research Question. *Can we pick a GC-based protocol that is actively secure (such as WRK17 [23], KRRW18 [24], DILO22 [16], or CWYY23 [15]) and make such active security public verifiable and obtain constant round publicly auditable MPC?*

We answer the aforementioned question with definitive. We picked the basic blueprint of the actively secure Garbled Circuit protocol, namely WRK17 [23], which relies on V-MAC, we then adopted VOLEitH [7] (with SoftSpokenOT [26]) to make the utilized V-MAC public auditable. We obtained a Constant Round Publicly Auditable Garbled Circuit Protocol with inherited security and efficiency from both WRK17 and VOLEitH (minimal overheads).⁸

1.1 Blockchain-Public-Auditable-MPC

Boostrapping GC with Blockchain. Our protocol only requires the blockchain as a public bulletin board, i.e. for commitments of inputs before the computation and such commitments become a part of the statement for the publicly verifiable zero-knowledge proof, as in other off-chain protocols.

⁸ We disregard the improvements in KRRW18 [24], DILO22 [16], or CWYY23 [15], due to non-trivial composition of such improvements and VOLEitH, for example they may require Learning-Parity-With-Noise assumption while SoftSpokenOT is incompatible with LPN.

Computation of GC on Blockchain. Blockchain applications such as decentralized applications built on Ethereum are sensitive to round complexity because each interaction round latency is capped by the blockchain’s consensus mechanism, i.e. block generation time. Our protocol which is a constant round protocol is particularly suitable for building such applications. Furthermore, while the MPC protocol is run among a set of participants, to have a meaningful impact on the blockchain, the result has to be made verifiable to the set of blockchain verifiers that are public nodes. Our protocol provides an auditable transcript which is an even stronger property. There is only one caveat that is the communication cost is linear to the size of the circuit. In Ethereum, it was a problem in the past due to calldata cost but, with data blobs made available, such an issue is elevated. Furthermore, the concrete computational complexity of our (public) verification protocol is low due to we only require calls to PRF (hashes) and operations on rings.

Exploiting the characteristics of the blockchain for blockchain-MPC: Together with the bootstrapping, the necessary computation in MPC, such as data sanitization check, e.g. the input should be positive, can be replaced with ZKP leveraging the pre-committed data, i.e. the inputs into the MPC can be committed along with a proof of quality, as such, there is no need for checking inside the MPC hence the circuit in MPC can focus only on the application logic.⁹
Security in blockchain model: Blockchain is utilized as a public bulletin board outside of the protocol hence we only analyze the security of our protocol in its own hybrid model.

2 Preliminaries

2.1 Authenticated Bits

Assume that P_A holds a secret bit b . Then, in an interactive protocol between P_A and P_B , b is authenticated if P_A holds $M[b]$ while P_B holds $K[b]$ and a global value Δ_B satisfying

$$M[b] = K[b] \oplus b \cdot \Delta_B.$$

If b , kept by P_A (respectively, P_B), is authenticated with respect to Δ_B (respectively, Δ_A), we write $\llbracket b; \Delta_B \rrbracket_A$ (respectively, $\llbracket b; \Delta_A \rrbracket_B$).

Protocol Π_{fix} . We recall from [9] the protocol Π_{fix} in the following Figure 1.

2.2 Publicly Verifiable VOLEs via SoftSpokenOT [26]

SoftSpokenOT proposed by [26] allows two parties to generate an authenticated bit, e.g., $\llbracket b \rrbracket_A$, in a way that each party only locally interacts with the random oracle (RO). This hence leads to publicly verifiable zero-knowledge proofs [7,14,11] which recently attracted a lot of attention from the research community. We

⁹ Such an approach is similar to sanitizing SPDZ shares using ZKP, <https://github.com/Yoii-Inc/zk-mpc>.

Inputs: b and $\llbracket a; \Delta_{p'} \rrbracket_p$. Here $\llbracket a; \Delta_{p'} \rrbracket_p$ is parsed as $M[a] = K[a] \oplus a \cdot \Delta_{p'}$, for some $p \in \{A, B\}$, $p' = A$ if $p = B$, otherwise, $p' = B$, and $a \in \{0, 1\}$. P_p keeps a and $M[a]$ while $P_{p'}$ keeps $K[a]$ and $\Delta_{p'}$.
Output: $\llbracket b; \Delta_{p'} \rrbracket_p$ as $M[b] = K[b] \oplus b \cdot \Delta_{p'}$ where $n \in \{0, 1\}$.
Execution:
 1. Party P_p sends $d := a \oplus b$ to the remaining party $P_{p'}$.
 2. P_p locally sets $M[b] := M[a]$.
 3. $P_{p'}$ locally sets $K[b] := K[a] \oplus d \cdot \Delta_{p'}$.

Fig. 1. Sub-Protocol Π_{fix} .

recall the functionality $\mathcal{F}_{\text{sVOLE}}^{\mathcal{P}, \mathcal{V}, \tau, N}$ and the technique from SoftSpokenOT for realizing $\mathcal{F}_{\text{sVOLE}}^{\mathcal{P}, \mathcal{V}, \tau, N}$ for binary computations, over \mathbb{F}_{2^k} for some $k \in \mathbb{N}$, as follows.

Functionality $\mathcal{F}_{\text{sVOLE}}^{\mathcal{P}, \mathcal{V}, \tau, N}$. We recall the subspace VOLE functionality $\mathcal{F}_{\text{sVOLE}}^{\mathcal{P}, \mathcal{V}, \tau, N}$ [7], adapted from [14], in the following Figure 2.

This functionality is parameterized by \mathbb{F}_2 and its extension \mathbb{F}_{2^τ} for some parameter $\tau \in \mathbb{N}$. We denote by κ the repetition parameter and N the number of random sVOLE correlations.

1. Upon receiving (init) from \mathcal{P} and \mathcal{V} , it works as follows:
 - (a) Sample $\Delta \xleftarrow{\$} \mathbb{F}_{2^\tau}$. Then, sample $\mathbf{u} \xleftarrow{\$} \mathbb{F}_2^N$ and $\mathbf{v} \xleftarrow{\$} \mathbb{F}_{2^\tau}^N$ and compute $\mathbf{w} := \mathbf{v} \oplus \mathbf{u} \cdot \Delta$.
 - (b) If \mathcal{P} is corrupted, receive \mathbf{u}, \mathbf{v} from \mathcal{P} and compute $\mathbf{w} := \mathbf{v} \oplus \mathbf{u} \cdot \Delta$.
 - (c) If \mathcal{V} is corrupted, receive Δ, \mathbf{w} from \mathcal{V} and compute $\mathbf{v} := \mathbf{w} \oplus \mathbf{u} \cdot \Delta$.
 - (d) Send (\mathbf{u}, \mathbf{v}) to \mathcal{P} .
2. Upon receiving (get) from \mathcal{P} and \mathcal{V} , it sends (Δ, \mathbf{w}) to both parties.

Fig. 2. The subspace VOLE functionality $\mathcal{F}_{\text{sVOLE}}^{\mathcal{P}, \mathcal{V}, \tau, N}$.

SoftSpokenOT. Let $\tau \in \mathbb{N}$. Let $t_x \in \{0, 1\}$ for all $x \in \mathbb{F}_{2^\tau}$. SoftSpokenOT works by the observation that

$$\bigoplus_{x \in \mathbb{F}_{2^\tau} \setminus \{\Delta_B\}} t_x \cdot (\Delta_B \oplus x) = \bigoplus_{x \in \mathbb{F}_{2^\tau}} t_x \cdot (\Delta_B \oplus x) = \bigoplus_{x \in \mathbb{F}_{2^\tau}} t_x \cdot x \oplus \Delta_B \cdot \bigoplus_{x \in \mathbb{F}_{2^\tau}} t_x.$$

By the above observation, we can set $b = \bigoplus_{x \in \mathbb{F}_{2^\tau}} t_x$, $M[b] = \bigoplus_{x \in \mathbb{F}_{2^\tau}} t_x \cdot x$ and $K[b] = \bigoplus_{x \in \mathbb{F}_{2^\tau} \setminus \{\Delta_B\}} t_x \cdot (\Delta_B \oplus x)$. We have the following discussions.

- For $x \in \mathbb{F}_{2^\tau} \setminus \{0\}$, t_x can be generated randomly while t_0 is uniquely determined by $t_0 := b \oplus \bigoplus_{x \in \mathbb{F}_{2^\tau} \setminus \{0\}} t_x$.
- The computation of $M[b]$ does not involve Δ_B .
- Party P_B , when computing $K[b]$, requires the knowledge of $2^\tau - 1$ (out of 2^τ) elements in $\{t_x\}_{x \in \mathbb{F}_{2^\tau}}$. The only element excluded is t_{Δ_B} . Moreover, P_B is not allowed to know t_{Δ_B} . Otherwise, P_B can recover b which is a secret of P_A . Therefore, P_A sends to P_B a vector commitment vc that commits to $\{t_x\}_{x \in \mathbb{F}_{2^\tau}}$. P_B then obtains Δ_B from RO with input containing vc . Once Δ_B is determined, P_A opens $2^\tau - 1$ (out of 2^τ) positions in vc with respect to $\{t_x\}_{x \in \mathbb{F}_{2^\tau} \setminus \{\Delta_B\}}$. Hence, P_B now has sufficient information to determine $K[b]$.

3 Technical Overview

We first recall the authenticated garbling WRK protocol [28] in Section 3.1. Then, we discuss our technique for making WRK protocol publicly auditable in Section 3.2.

3.1 Authenticated Garbling from WRK Protocol [28]

We recall the authenticated garbling technique from WRK protocol [28] (recalled in [24]) without clearly discussing it in detail. Consider a circuit containing AND (\cdot) and XOR (\oplus) gates. We denote each gate by the tuple $(\alpha, \beta, \gamma, \text{op})$ where $\text{op} \in \{\cdot, \oplus\}$ is the operation, and α , β , and γ are the indices of the left, right, and output wires, respectively. As remarked in [28,24], XOR gates can be handled for free. Regarding AND gates, consider the tuple $(\alpha, \beta, \gamma, \cdot)$. Let P_A and P_B respectively play the roles of garbler and authenticator. We first recall a few notations before discussing the garbled circuits.

The value at each wire w is denoted by $z_w \in \{0, 1\}$. To mask z_w , we use a mask $\lambda_w \in \{0, 1\}$ and computed the masked value $\hat{z}_w = z_w \oplus \lambda_w$. In [28], at each wire w , λ_w is split into shared random masks r_w and s_w held by P_A and P_B , respectively, such that $\lambda_w = r_w \oplus s_w$. Hence, $\hat{z}_w = z_w \oplus \lambda_w$.

The input wires to the circuit are split into two separate sets \mathcal{I}_A and \mathcal{I}_B such that P_A (respectively, P_B) knows each z_w for $w \in \mathcal{I}_A$ (respectively, $w \in \mathcal{I}_B$). Therefore, in the beginning (at the phase **Input Preprocessing**), P_B (respectively, P_A) must send s_w (respectively, r_w) to P_A (respectively, P_B) for computing \hat{z}_w for $w \in \mathcal{I}_A$ (respectively, $w \in \mathcal{I}_B$). P_A and P_B then broadcast all \hat{z}_w for $w \in \mathcal{I}_A \cup \mathcal{I}_B$. Then, the evaluation of the circuit, following a topological ordering, is hence considered in two types of gates:

- For each XOR gate $(\alpha, \beta, \gamma, \oplus)$, the computation is free. Having \hat{z}_α and \hat{z}_β , each party (P_A and P_B) can locally compute $\hat{z}_\gamma := \hat{z}_\alpha \oplus \hat{z}_\beta = (z_\alpha \oplus z_\beta) \oplus (\lambda_\alpha \oplus \lambda_\beta)$. This is hence understood that $z_\gamma = z_\alpha \oplus z_\beta$ and $\lambda_\gamma = \lambda_\alpha \oplus \lambda_\beta$.
- For each AND gate $(\alpha, \beta, \gamma, \cdot)$, the computation is not that simple. In fact, having $\hat{z}_\alpha = z_\alpha \oplus \lambda_\alpha$ and $\hat{z}_\beta = z_\beta \oplus \lambda_\beta$, the parties need to obtain $\hat{z}_\gamma = z_\alpha \cdot z_\beta \oplus \lambda_\gamma$ where $\lambda_\gamma = r_\gamma \oplus s_\gamma$ with r_γ and s_γ independently held by P_A and P_B , respectively. To do this, we first observe that

$$\begin{aligned} \hat{z}_\alpha \cdot \hat{z}_\beta &= (z_\alpha \oplus \lambda_\alpha) \cdot (z_\beta \oplus \lambda_\beta) \\ &= z_\alpha \cdot z_\beta \oplus \lambda_\alpha \cdot z_\beta \oplus \lambda_\beta \cdot z_\alpha \oplus \lambda_\alpha \cdot \lambda_\beta \\ &= z_\alpha \cdot z_\beta \oplus \lambda_\alpha \cdot \underbrace{(z_\beta \oplus \lambda_\beta)}_{\hat{z}_\beta} \oplus \lambda_\beta \cdot \underbrace{(z_\alpha \oplus \lambda_\alpha)}_{\hat{z}_\alpha} \oplus \lambda_\alpha \cdot \lambda_\beta. \end{aligned}$$

Hence, $\lambda_\alpha \cdot \lambda_\beta \oplus \lambda_\gamma \oplus \lambda_\alpha \cdot \hat{z}_\beta \oplus \lambda_\beta \cdot \hat{z}_\alpha \oplus \hat{z}_\alpha \cdot \hat{z}_\beta = z_\alpha \cdot z_\beta \oplus \lambda_\gamma$. Notice that, $\lambda_\alpha \cdot \hat{z}_\beta = r_\alpha \cdot \hat{z}_\beta \oplus s_\alpha \cdot \hat{z}_\beta$, $\lambda_\beta \cdot \hat{z}_\alpha = r_\beta \cdot \hat{z}_\alpha \oplus s_\beta \cdot \hat{z}_\alpha$, and $\lambda_\gamma = r_\gamma \oplus s_\gamma$ such that P_A can compute $r_\alpha \cdot \hat{z}_\beta$ and $r_\beta \cdot \hat{z}_\alpha$ while P_B can compute $s_\alpha \cdot \hat{z}_\beta$ and $s_\beta \cdot \hat{z}_\alpha$. However, regarding $\lambda_\alpha \cdot \lambda_\beta$, [28] suggests to use additional shared random masks r'_γ and s'_γ respectively held by P_A and P_B such that $r'_\gamma \oplus s'_\gamma =$

$\lambda_\alpha \cdot \lambda_\beta$. Then, P_A can construct the garbled table corresponding to the four possibilities of $(\hat{z}_\alpha, \hat{z}_\beta)$, i.e., by encrypting to the following four rows where the k -th row is

$$r_{\gamma,k} = r'_\gamma \oplus r_\gamma \oplus r_\alpha \cdot k_1 \oplus r_\beta \cdot k_0 \quad (1)$$

for $k \in [0, 3]$ with $(k_0, k_1) = \text{bin}(k)$ (i.e., $k = k_0 + 2k_1$ and $k_0, k_1 \in \{0, 1\}$). Here, the k -th row assumes that $(\hat{z}_\alpha, \hat{z}_\beta) = (k_0, k_1) = \text{bin}(k)$. Regarding P_B , if $k \in [0, 3]$, i.e., $(\hat{z}_\alpha, \hat{z}_\beta) = (k_0, k_1) = k$, is the selected row, then P_B can compute

$$s_{\gamma,k} = s'_\gamma \oplus s_\gamma \oplus s_\alpha \cdot k_1 \oplus r_\beta \cdot k_0 \oplus k_0 \cdot k_1 \quad (2)$$

and decrypt the corresponding row to obtain $r'_\gamma \oplus r_\gamma \oplus r_\alpha \cdot k_1 \oplus r_\beta \cdot k_0$. Taking sum achieves $\hat{z}_\gamma = z_\alpha \cdot z_\beta \oplus \lambda_\gamma$ as desired.

We also note that encrypting requires the associated labels $L_{w,0}$ and $L_{w,1}$ corresponding to the assumptions $\hat{z}_w = 0$ and $\hat{z}_w = 1$. To be compatible with free XORs (i.e., $\hat{z}_\gamma = \hat{z}_\alpha \oplus \hat{z}_\beta$ for any XOR gate $(\alpha, \beta, \gamma, \oplus)$), [28] enforces $L_{w,0} \oplus \Delta_A = L_{w,1}$ where Δ_A is only known by P_A . This means that, for each wire w , P_B can achieve either $L_{w,0}$ or $L_{w,1}$ and cannot achieve both. Hence, for each XOR gate $(\alpha, \beta, \gamma, \oplus)$, by enforcing $L_{\gamma,0} = L_{\alpha,0} \oplus L_{\beta,0}$, we can see that

$$\begin{aligned} L_{\gamma,\hat{z}_\gamma} &= L_{\gamma,0} \oplus \hat{z}_\gamma \cdot \Delta_A \\ &= L_{\alpha,0} \oplus L_{\beta,0} \oplus (\hat{z}_\alpha \oplus \hat{z}_\beta) \cdot \Delta_A \\ &= (L_{\alpha,0} \oplus \hat{z}_\alpha \cdot \Delta_A) \oplus (L_{\beta,0} \oplus \hat{z}_\beta \cdot \Delta_A) \\ &= L_{\alpha,\hat{z}_\alpha} \oplus L_{\beta,\hat{z}_\beta}. \end{aligned}$$

However, for each AND gate $(\alpha, \beta, \gamma, \cdot)$, we cannot have $L_{\gamma,0}$ related to $L_{\alpha,0}$ and $L_{\alpha,1}$. Hence, $L_{\gamma,0}$ is hence independently sampled by P_A . We also would like to maintain that $L_{\gamma,\hat{z}_\gamma} = L_{\gamma,0} \oplus \hat{z}_\gamma \cdot \Delta_A$. At this point, it seems impossible to P_B to obtain $L_{\gamma,\hat{z}_\gamma}$ without the knowledge of Δ_A . We will discuss how to deal with this after discussing authenticating shared random masks below.

Authenticating Shared Random Masks. As noted by [28], this design's privacy is guaranteed against malicious P_A . Nevertheless, P_A can violate the correctness of the garbled table by changing the orders of rows in garbled tables or encrypting incorrect rows. To cope with this issue, [28] enforces every shared random mask from each party to be authenticated by using subfield VOLE. In particular, P_A and P_B keep Δ_A and Δ_B , respectively, such that, for each r_w and s_w held by P_A and P_B , respectively, it should hold that

$$M[r_w] = K[r_w] \oplus r_w \cdot \Delta_B \text{ and } M[s_w] = K[s_w] \oplus s_w \cdot \Delta_A$$

where $(r_w, M[r_w], K[s_w])$ is held by P_A while $(s_w, M[s_w], K[r_w])$ is held by P_B . By applying protocol Π_{fix} in Figure 1, for each garbled table corresponding to each AND gate $(\alpha, \beta, \gamma, \cdot)$, for $k \in [0, 3]$, P_A and P_B can respectively achieve

$$(r_{\gamma,k}, M[r_{\gamma,k}], K[s_{\gamma,k}]) \text{ and } (s_{\gamma,k}, M[s_{\gamma,k}], K[r_{\gamma,k}]).$$

With the above employment of subfield VOLE to authenticate shared random masks, we are guaranteed that a malicious P_A cannot take advantage in causing the protocol to deviate from its proper purpose. We now turn back to discussing how to make P_B obtain $L_{\gamma, \hat{z}_\gamma} = L_{\gamma, 0} \oplus \hat{z}_\gamma \cdot \Delta_A$ by leveraging the authenticated shared random masks. Hence, in each row of the garbled table for each AND gate $(\alpha, \beta, \gamma, \cdot)$, P_A additionally encrypts $M[r_{\gamma, k}]$ for P_B to subsequently verify the authenticity of the decrypted $r_{\gamma, k}$.

Computing Labels from Authenticated Shared Random Masks. Since $L_{\gamma, \hat{z}_\gamma} = L_{\gamma, 0} \oplus \hat{z}_\gamma \cdot \Delta_A$, P_A holds $r_{\gamma, k}$ and $K[s_{\gamma, k}]$ while P_B holds $M[s_{\gamma, k}]$, P_A can compute

$$L_{\gamma, 0} \oplus r_{\gamma, k} \cdot \Delta_A \oplus K[s_{\gamma, k}].$$

Since $M[s_{\gamma, k}] = K[s_{\gamma, k}] \oplus s_{\gamma, k} \cdot \Delta_A$, this implies that

$$\underbrace{L_{\gamma, 0} \oplus r_{\gamma, k} \cdot \Delta_A \oplus K[s_{\gamma, k}]}_{\text{computed by } P_A \text{ above}} \oplus M[s_{\gamma, k}] = L_{\gamma, 0} \oplus r_{\gamma, k} \cdot \Delta_A \oplus s_{\gamma, k} \cdot \Delta_A \quad (3)$$

$$= L_{\gamma, 0} \oplus \hat{z}_{\gamma, k} \cdot \Delta_A.$$

Hence, in each row of the garbled table for each AND gate $(\alpha, \beta, \gamma, \cdot)$, P_A additionally encrypts $L_{\gamma, 0} \oplus r_{\gamma, k} \cdot \Delta_A \oplus K[s_{\gamma, k}]$ for P_B to subsequently compute $L_{\gamma, 0} \oplus \hat{z}_{\gamma, k} \cdot \Delta_A$ according to (3). Notice that P_B does not know $L_{\gamma, 0}$, Δ_A , and $K[s_{\gamma, k}]$. Therefore, even P_B can decrypt to obtain $L_{\gamma, \hat{z}_\gamma}$, P_B only knows either $L_{\gamma, 0}$ or $L_{\gamma, 1}$ (depending on the value of \hat{z}_γ) and cannot know both. Knowing both helps P_B to decrypt all four rows of the garbled table which is strictly prohibited since it compromises the privacy of P_A .

Putting All Together. With the above discussions, for each garbled table corresponding to each AND gate $(\alpha, \beta, \gamma, \cdot)$, each encrypted row must include $r_{\gamma, k}$, $M[r_{\gamma, k}]$, and $L_{\gamma, 0} \oplus r_{\gamma, k} \cdot \Delta_A \oplus K[s_{\gamma, k}]$. The discussion is realized in Figure 4, namely, WRK protocol. This protocol employs functionality \mathcal{F}_{pre} in Figure 3 as a subroutine for generating authenticated shared random masks and obtaining shared authenticated AND triples, i.e., authenticated r_1, r_2, r_3 (held by P_A) and s_1, s_2, s_3 (held by P_B) satisfying

$$(r_1 \oplus s_1) \cdot (r_2 \oplus s_2) = r_3 \oplus s_3.$$

This functionality is described as follows:

1. Assuming no Δ_A, Δ_B stored, upon receiving Δ_A from P_A and init from P_B , sample $\Delta_B \xleftarrow{\$} \{0, 1\}^\rho$, where ρ is the statistical security parameter, and store Δ_A and Δ_B . Then, send Δ_B to P_B .
2. Upon receiving $(\text{random}, r, M[r], K[s])$ from P_A and random from P_B , sample $s \xleftarrow{\$} \{0, 1\}$, and compute $M[s] := K[s] \oplus s \cdot \Delta_A$ and $K[r] := M[r] \oplus r \cdot \Delta_B$. Then, send $(s, M[s], K[r])$ to P_B .
3. Upon receiving $(\text{and}, (r_1, M[r_1], K[s_1]), (r_2, M[r_2], K[s_2]), (r_3, M[r_3], K[s_3]))$ from P_A and $(\text{and}, (s_1, M[s_1], K[r_1]), (s_2, M[s_2], K[r_2]))$ from P_B , send *cheat* to P_B if it does not hold that $M[r_i] = K[r_i] \oplus r_i \cdot \Delta_B$ and $M[s_i] = K[s_i] \oplus s_i \cdot \Delta_A$ for $i \in \{1, 2\}$. Otherwise, compute $s_3 = ((r_1 \oplus s_1) \cdot (r_2 \oplus s_2)) \oplus r_3$ and $M[s_3] := K[s_3] \oplus s_3 \cdot \Delta_A$. Send $(s_3, M[s_3], K[r_3])$ to P_B .

Fig. 3. Functionality \mathcal{F}_{pre} (Figure 1 in [28]).

Inputs: Both parties P_A and P_B agree on a common circuit for function $f : \{0, 1\}^{|\mathcal{I}_A|} \times \{0, 1\}^{|\mathcal{I}_B|} \rightarrow \{0, 1\}^{|\mathcal{O}|}$. In the input-preprocessing phase, P_A and P_B holds $\mathbf{x} = (z_i)_{i \in \mathcal{I}_A} \in \{0, 1\}^{|\mathcal{I}_A|}$ and $\mathbf{y} = (z_i)_{i \in \mathcal{I}_B} \in \{0, 1\}^{|\mathcal{I}_B|}$, respectively.

Function-Independent Preprocessing:

- P_A and P_B send init to \mathcal{F}_{pre} to receives Δ_A and Δ_B , respectively.
- For each $w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}$, both P_A and P_B send random to \mathcal{F}_{pre} . In return, \mathcal{F}_{pre} sends $(r_w, M[r_w], K[s_w])$ and $(s_w, M[s_w], K[r_w])$ to P_A and P_B , respectively. Define $\lambda_w := r_w \oplus s_w$. P_A picks a uniform κ -bit string $L_{w,0}$ where κ denotes the computational security parameter.

Function-Dependent Preprocessing:

- For each XOR gate $(\alpha, \beta, \gamma, \oplus)$, P_A computes $(r_\gamma, M[r_\gamma], K[s_\gamma]) := (r_\alpha \oplus r_\beta, M[r_\alpha] \oplus M[r_\beta], K[s_\alpha] \oplus K[s_\beta])$ and $L_{\gamma,0} := L_{\alpha,0} \oplus L_{\beta,0}$. P_B computes $(s_\gamma, M[s_\gamma], K[r_\gamma]) := (s_\alpha \oplus s_\beta, M[s_\alpha] \oplus M[s_\beta], K[r_\alpha] \oplus K[r_\beta])$.
- For each AND gate $(\alpha, \beta, \gamma, \cdot)$:
 - P_A (respectively, P_B) sends $(\text{and}, (r_\alpha, M[r_\alpha], K[s_\alpha]), (r_\beta, M[r_\beta], K[s_\beta]))$ (respectively, $(\text{and}, (s_\alpha, M[s_\alpha], K[r_\alpha]), (s_\beta, M[s_\beta], K[r_\beta])))$ to \mathcal{F}_{pre} . In return, \mathcal{F}_{pre} sends $(r'_\gamma, M[r'_\gamma], K[s'_\gamma])$ and $(s'_\gamma, M[s'_\gamma], K[r'_\gamma])$ respectively to P_A and P_B . Here, $r'_\gamma \oplus s'_\gamma = \lambda_\alpha \cdot \lambda_\beta$.
 - $\forall k \in [0, 3]$, let $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$, i.e., $k = k_0 + 2 \cdot k_1$, and P_A computes

$$r_{\gamma,k} := r'_\gamma \oplus r_\gamma \oplus k_0 \cdot r_\alpha \oplus k_1 \cdot r_\beta,$$

$$M[r_{\gamma,k}] := M[r'_\gamma] \oplus M[r_\gamma] \oplus k_0 \cdot M[r_\beta] \oplus k_1 \cdot M[r_\alpha],$$

$$K[r_{\gamma,k}] := K[r'_\gamma] \oplus K[r_\gamma] \oplus k_0 \cdot K[r_\beta] \oplus k_1 \cdot K[r_\alpha] \oplus k_0 \cdot k_1 \cdot \Delta_A.$$
 - $\forall k \in [0, 3]$, let $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$, i.e., $k = k_0 + 2 \cdot k_1$, and P_B computes

$$s_{\gamma,k} := s'_\gamma \oplus s_\gamma \oplus k_0 \cdot s_\beta \oplus k_1 \cdot s_\alpha \oplus k_0 \cdot k_1,$$

$$M[s_{\gamma,k}] := M[s'_\gamma] \oplus M[s_\gamma] \oplus k_0 \cdot M[s_\beta] \oplus k_1 \cdot M[s_\alpha],$$

$$K[s_{\gamma,k}] := K[s'_\gamma] \oplus K[s_\gamma] \oplus k_0 \cdot K[s_\beta] \oplus k_1 \cdot K[s_\alpha].$$
 - P_A computes $L_{\alpha,1} := L_{\alpha,0} \oplus \Delta_A$ and $L_{\beta,1} := L_{\beta,0} \oplus \Delta_A$. P_A sends the following to P_B , for all $k \in [0, 3]$ with $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$:

$$G_{\gamma,k} := H(L_{\alpha,k_0}, L_{\beta,k_1}, \gamma, k) \oplus (r_{\gamma,k}, M[r_{\gamma,k}], L_{\gamma,k} \oplus K[s_{\gamma,k}] \oplus r_{\gamma,k} \cdot \Delta_A).$$

Input Processing:

- For $w \in \mathcal{I}_A$, P_A sends $(r_w, M[r_w])$ to P_B . P_B checks whether $M[r_w] = K[r_w] \oplus r_w \cdot \Delta_B$. P_B then sends $\hat{z}_w = z_w \oplus \lambda_w = z_w \oplus r_w \oplus s_w$ to P_A . Finally, P_A sends L_{w,\hat{z}_w} to P_B .
- For $w \in \mathcal{I}_B$, P_B sends $(s_w, M[s_w])$ to P_A . P_A checks whether $M[s_w] = K[s_w] \oplus s_w \cdot \Delta_A$. P_A then sends $\hat{z}_w = z_w \oplus \lambda_w = z_w \oplus r_w \oplus s_w$ and L_{w,\hat{z}_w} to P_B .

Circuit Evaluation:

- P_B evaluates the circuit following a topological order. For each gate $(\alpha, \beta, \gamma, \text{op})$ where $\text{op} \in \{\cdot, \oplus\}$, P_B holds $(\hat{z}_\alpha, L_{\alpha,\hat{z}_\alpha})$ and $(\hat{z}_\beta, L_{\beta,\hat{z}_\beta})$ where $\hat{z}_\alpha = z_\alpha \oplus \lambda_\alpha$ and $\hat{z}_\beta = z_\beta \oplus \lambda_\beta$.
 - If $\text{op} = \oplus$, P_B computes $\hat{z}_\gamma := \hat{z}_\alpha \oplus \hat{z}_\beta$ and $L_{\gamma,\hat{z}_\gamma} := L_{\alpha,\hat{z}_\alpha} \oplus L_{\beta,\hat{z}_\beta}$.
 - If $\text{op} = \cdot$, P_B computes $i := 2\hat{z}_\alpha + \hat{z}_\beta$. Then, P_B computes $(r_{\gamma,i}, M[r_{\gamma,i}], L_{\gamma,0} \oplus K[s_{\gamma,i}] \oplus r_{\gamma,i} \cdot \Delta_A) := G_{\gamma,i} \oplus H(L_{\alpha,\hat{z}_\alpha}, L_{\beta,\hat{z}_\beta}, \gamma, i)$. Then, P_B checks whether $M[r_{\gamma,i}] = K[r_{\gamma,i}] \oplus r_{\gamma,i} \cdot \Delta_B$. If so, P_B computes $\hat{z}_\gamma = s_{\gamma,i} \oplus r_{\gamma,i}$ and $L_{\gamma,\hat{z}_\gamma} := (L_{\gamma,0} \oplus K[s_{\gamma,i}] \oplus r_{\gamma,i} \cdot \Delta_A) \oplus M[s_{\gamma,i}]$.

Output Determination:

- For $w \in \mathcal{O}$, P_A sends $(r_w, M[r_w])$ to P_B who checks whether $M[r_w] = L[r_w] \oplus r_w \cdot \Delta_B$. If so, P_B computes $z_w := \hat{z}_w \oplus r_w \oplus s_w$.

Fig. 4. Protocol $\Pi_{2\text{PC}}$ (Figure 2 in [28]).

Theorem 1 (Security of $\Pi_{2\text{PC}}$). *If H is modeled as a random oracle, the protocol $\Pi_{2\text{PC}}$ in Figure 4, securely computes f against malicious adversaries with statistical security $2^{-\rho}$ in the \mathcal{F}_{pre} -hybrid model.*

3.2 Making Authenticated Garbling from [28] Publicly Auditable

Our purpose is to make WRK protocol publicly auditable. Our approach is to follow the recent VOLEith approach [7,14,11] by applying SoftSpokenOT [26] for making VOLE protocols (e.g., Wolverine [29] and Quicksilver [32]) publicly verifiable.

Verifier \mathcal{V} . To make WRK protocol publicly auditable, we modify the protocol in Figure 4 to include an additional party, called verifier \mathcal{V} . This party will play the role of verifying the transcript communicated between P_A and P_B .

Issues. We now discuss the employment of SoftSpokenOT [26] into WRK protocol. Assume that we transform all authenticated randomness of P_A and P_B into the forms specified by SoftSpokenOT (see Section 2.2). The transformation requires Δ_A and Δ_B to be public. However, making public Δ_A and Δ_B incurs the following issues:

- For each wire w , P_B is allowed to know only either $L_{w,0}$ or $L_{w,1}$. However, since Δ_A is public and $L_{w,1} = L_{w,0} \oplus \Delta_B$, knowing either $L_{w,0}$ or $L_{w,1}$ implies knowledge of both labels. This hence violates the security of the original WRK protocol.
- Using only Δ_A and Δ_B for public verification is not convenient for amplifying soundness. We notice that VOLEitH systems [7,14,11] usually require repeating the protocol with a few distinct global keys to reduce soundness error.
- Verification shared AND triples, e.g., for an AND gate $(\alpha, \beta, \gamma, \cdot)$, both P_A and P_B should allow the verifier to believe that

$$\underbrace{(r_\alpha \oplus s_\alpha)}_{\lambda_\alpha} \cdot \underbrace{(r_\beta \oplus s_\beta)}_{\lambda_\beta} = \underbrace{r'_\gamma \oplus s'_\gamma}_{\lambda_\alpha \cdot \lambda_\beta}.$$

Here, in WRK protocol, P_A and P_B rely on the outputs of \mathcal{F}_{pre} which guarantee the above identity holds. However, when making public auditability.

Our Approach. We briefly sketch our approach for coping with the above-mentioned issues as follows.

Repurposing Δ_A . We first recall that Δ_A employed in WRK protocol is for two purposes: (i) authenticating P_B 's shared random mask and (ii) allowing P_B to compute $L_{\gamma, \hat{z}_\gamma}$ for any AND gate $(\alpha, \beta, \gamma, \cdot)$. As discussed above, making Δ_A public violates P_A 's privacy. Therefore, in our design, we still keep Δ_A private to protect P_A 's privacy. To authenticate P_B 's shared random mask, we instead use Δ'_A generated from using SoftSpokenOT [26]. The idea is as follows:

- For each wire w , assume that s_w is authenticated in a way that P_A keeps Δ_A and $K[s_w]$ while P_B keeps s_w and $M[s_w]$.
- By applying SoftSpokenOT (c.f. Section 2.2), P_B can first obtain a random value \bar{s}_w and $M'[\bar{s}_w]$. Then, P_B and P_A apply steps 1 and 2 of protocol Π_{fix} presented in Figure 1 to obtain $M'[\bar{s}_w]$. In particular, P_B and P_A respectively play the roles of P_p and $P_{p'}$. P_B first publishes $\hat{s}_w := s_w \oplus \bar{s}_w$ as in step 1. Then, P_B sets $M'[s_w] := M'[\bar{s}_w]$ as in step 1.
- At some point of the protocol, verifier \mathcal{V} sends Δ'_A which helps every party to determine $K'[s_w]$. P_A hence can run step 3 by setting $K'[s_w] := K'[s_w] \oplus \hat{s}_w \cdot \Delta'_A$.

We note that, as specified by SoftSpokenOT, both Δ'_A and $K'[\bar{s}_w]$ are publicly known by every party. Since Δ'_A is not employed to determine $L_{\gamma, \hat{z}_\gamma}$ for any AND gate $(\alpha, \beta, \gamma, \cdot)$, as long as Δ_A is not leaked, P_A 's privacy is hence guaranteed. On the other hand, any attempt from P_A to violate the correctness of the protocol is hence captured by the authenticated shared random masks via Δ'_A , instead of Δ_A , to make the protocol publicly auditable.

Symmetrically, we use Δ'_B to authenticate r_w for any wire w for public audibility purposes.

Recall that, for each AND gate $(\alpha, \beta, \gamma, \cdot)$, we additionally use r'_γ and s'_γ such that $r'_\gamma \oplus s'_\gamma = \lambda_\alpha \cdot \lambda_\beta$. We also use Δ'_B and Δ'_A to authenticate r'_γ and s'_γ , respectively.

Amplifying Soundness for Public Auditability. To amplify soundness for the public audibility version, we simply follow previous VOLEitH systems [7,14,11] repeat κ times where $\kappa \in \mathbb{N}$ is set sufficiently large to guarantee the soundness of the output transcript. Hence, rather than authenticating each s_w for any wire w by Δ'_A , we can authenticate by $\Delta'_{A,j}$ for $j \in [\kappa]$ with the associated IT-MAC tag $M'_j[s_w]$ and key $K'_j[s_w]$ such that

$$M'_j[s_w] = K'_j[s_w] \oplus s_w \cdot \Delta'_{A,j} \quad \forall j \in [\kappa].$$

Similarly, we also authenticate r_w for any wire w by $\Delta'_{B,j}$, kept by P_B , to make it auditable and sound. Hence, we have

$$M'_j[r_w] = K'_j[r_w] \oplus r_w \cdot \Delta'_{B,j} \quad \forall j \in [\kappa].$$

Here, $M'_j[r_w]$ and $K'_j[r_w]$ are the pair of IT-MAC tag and key kept by P_A and P_B , respectively.

Similarly, for any AND gate $(\alpha, \beta, \gamma, \cdot)$, we also authenticate r'_γ and s'_γ by $\{\Delta'_{B,j}\}_{j \in [\kappa]}$ and $\{\Delta'_{A,j}\}_{j \in [\kappa]}$, respectively.

Verifying Shared AND Triples. In our design above, for any AND gate $(\alpha, \beta, \gamma, \cdot)$, we use shared AND triples $(r_\alpha, r_\beta, r'_\gamma)$ and $(s_\alpha, s_\beta, s'_\gamma)$, respectively kept by P_A and P_B and authenticated via $\{\Delta'_{B,j}\}_{j \in [\kappa]}$ and $\{\Delta'_{A,j}\}_{j \in [\kappa]}$, to ensure that

$$\underbrace{(r_\alpha \oplus s_\alpha)}_{\lambda_\alpha} \cdot \underbrace{(r_\beta \oplus s_\beta)}_{\lambda_\beta} = \underbrace{(r_\gamma \oplus s_\gamma)}_{\lambda_\alpha \cdot \lambda_\beta}. \quad (4)$$

Since we are trying to make the transcript publicly auditable, any public verifier \mathcal{V} must believe that (4) holds. Our approach is as follows. For any $j \in [\kappa]$, assume that we have authenticated shared AND triples $(a_{A,j}, b_{A,j}, c_{A,j})$ and $(a_{B,j}, b_{B,j}, c_{B,j})$ respectively kept by P_A and P_B such that

$$(a_{A,j} \oplus a_{B,j}) \cdot (b_{A,j} \oplus b_{B,j}) = c_{A,j} \oplus c_{B,j}.$$

We adapt the checking multiplication trick [3,29] to check (4) as follows.

1. P_A (respectively, P_B) computes $d_{A,j} := r_\alpha \oplus a_{A,j}$ (respective, $d_{B,j} := s_\alpha \oplus a_{B,j}$) and $e_{A,j} := r_\beta \oplus b_{A,j}$ (respective, $e_{B,j} := s_\beta \oplus b_{B,j}$). P_A then publishes $d_{A,j}$ and $e_{A,j}$ while P_B publishes $d_{B,j}$ and $e_{B,j}$.

2. Each party locally computes $d_j := d_{A,j} \oplus d_{B,j}$ and $e_j := e_{A,j} \oplus e_{B,j}$.
3. P_A and P_B then can compute the authenticated values $\tilde{z}_{A,j}$ and $\tilde{z}_{B,j}$, respectively. P_A (respectively, P_B) then publishes $\tilde{z}_{A,j} \oplus r'_\gamma$ (respectively, $\tilde{z}_{B,j} \oplus s'_\gamma$). Then, any public party can check $\tilde{z}_{A,j} \oplus r'_\gamma \oplus \tilde{z}_{B,j} \oplus s'_\gamma$.

However, we are not dealing with checking a single AND gate. In our protocol, there are W such AND gates. To make such a check for all W AND gates, we generate L pairs of shared random AND triples. Then, we apply the cut-and-choose method [18] to check as follows:

- We select W random pairs from those L pairs for checking the AND gates.
- For the remaining $L - W$ from the L pairs, P_A and P_B simply reveal the corresponding authenticated triples for checking whether they are valid $L - W$ pairs of authenticated AND triples.

See Figure 7 for the realization of checking authenticated AND triples that we just discussed above.

Handling Opened Authenticated Shares at Output Wires of the Circuit. In the phase **Output Determination** in Figure 4, the parties need to reveal r_w for $w \in \mathcal{O}$ and the respective IT-MAC tags for checking authenticity. Since we follow the VOLEitH approach, IT-MAC tags should not be revealed after knowing $\{\Delta'_{A,j}\}_{j \in [\kappa]}$ and $\{\Delta'_{B,j}\}_{j \in [\kappa]}$. Therefore, we enforce P_A to use a hiding and binding commitment scheme to commit to $(r_w, \{M'_j[r_w]\}_{j \in [\kappa]})$ to obtain \mathbf{cmo}_w for $w \in \mathcal{O}$. P_A then publishes \mathbf{cmo}_w before knowing $\{\Delta'_{A,j}\}_{j \in [\kappa]}$ and $\{\Delta'_{B,j}\}_{j \in [\kappa]}$ from verifier \mathcal{V} . By the binding property of the commitment scheme, P_A is unable to manipulate the committed messages behind the commitments. Therefore, after knowing $\{\Delta'_{A,j}\}_{j \in [\kappa]}$ and $\{\Delta'_{B,j}\}_{j \in [\kappa]}$, P_A can open the commitments to obtain $(r_w, \{M'_j[r_w]\}_{j \in [\kappa]})$. Hence, any public party, e.g., \mathcal{V} , can check the authenticity of $\{r_w\}_{w \in \mathcal{O}}$.

4 Publicly Auditable 2PC

In this section, we present the public auditable 2PC protocol following the technical overview presented in Section 3.2.

In Section 4.1, we introduce the functionality $\mathcal{F}_{\text{sVOLE-2PC}}$ and its realization, namely, protocol $\Pi_{\text{sVOLE-2PC}}$. Then, in Section 4.2, we present our protocols for publicly auditable 2PC.

4.1 Functionality $\mathcal{F}_{\text{sVOLE-2PC}}$ and Protocol $\Pi_{2\text{PC}}$

We define the functionality $\mathcal{F}_{\text{sVOLE-2PC}}$ in Figure 5 as a subroutine for generating suitable global values, IT-MAC tags, and corresponding keys. This functionality is suitable with SoftSpokenOT [26]. Hence, when realizing $\mathcal{F}_{\text{sVOLE-2PC}}$ into protocol $\Pi_{\text{sVOLE-2PC}}$ in Figure 5, we can be in $\mathcal{F}_{\text{sVOLE}}$ -hybrid model (c.f. Figure 2, discussed in Section 2.2)

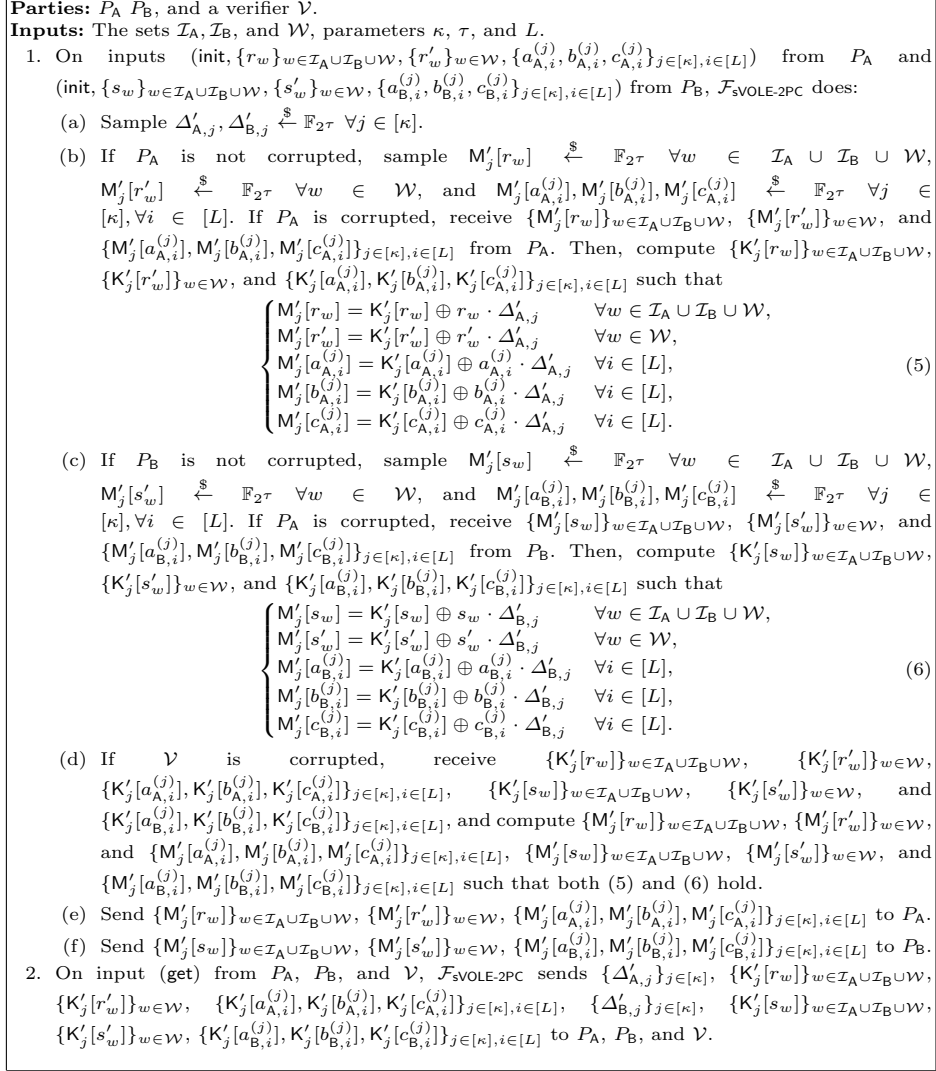


Fig. 5. Functionality $\mathcal{F}_{\text{sVOLE-2PC}}$.

Communication Cost of $\Pi_{\text{sVOLE-2PC}}$ (c.f. Figure 6). We first notice that we need to run in total 2κ times the functionality $\mathcal{F}_{\text{sVOLE}}^{\mathcal{P}, \mathcal{V}, \tau, N}$. When realizing this functionality by SoftSpokenOT, it requires $2 \cdot \kappa$ vector commitments and openings which subsequently determine the keys of authenticated values. We denote by $\text{cost}_{\text{vc-total}}$ to be the size of each vector commitment and its respective $N - 1$ openings. Hence, in total, it requires $2 \cdot \kappa \cdot (\text{cost}_{\text{vc-total}} +)$ for this task.

Using protocol Π_{fix} costs $2 \cdot \kappa \cdot (|\mathcal{I}_A| + |\mathcal{I}_B| + 2 \cdot |\mathcal{W}|)$ bits for publishing $\hat{r}_{w,j}$, $\hat{s}_{w,j}$ (for $w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}$ and $j \in [\kappa]$), $\hat{r}'_{w,j}$ and $\hat{s}'_{w,j}$ (for $w \in \mathcal{W}$ and $j \in [\kappa]$).

Parties: P_A , P_B , and a verifier \mathcal{V} .
Inputs: The sets \mathcal{I}_A , \mathcal{I}_B , and \mathcal{W} , parameters κ , τ , L , and $N = |\mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}| + |\mathcal{W}| + 3L$.
Execution:

- On inputs $(\text{init}, \{r_w\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}, \{r'_w\}_{w \in \mathcal{W}}, \{a_{A,i}^{(j)}, b_{A,i}^{(j)}, c_{A,i}^{(j)}\}_{j \in [\kappa], i \in [L]})$ from P_A and $(\text{init}, \{s_w\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}, \{s'_w\}_{w \in \mathcal{W}}, \{a_{B,i}^{(j)}, b_{B,i}^{(j)}, c_{B,i}^{(j)}\}_{j \in [\kappa], i \in [L]})$ from P_B , $\Pi_{\text{sVOLE-2PC}}$ works as follows:
 - P_A and \mathcal{V} send init to κ instances of $\mathcal{F}_{\text{sVOLE}}^{P_A, \mathcal{V}, \tau, N}$. Besides, P_B and \mathcal{V} send init to κ instances of $\mathcal{F}_{\text{sVOLE}}^{P_B, \mathcal{V}, \tau, N}$.
 - P_A receives outputs

$$\mathbf{u}_j = ((\bar{r}_{w,j})_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}, j \in [\kappa]} \| (\bar{r}'_{w,j})_{w \in \mathcal{W}, j \in [\kappa]} \| (\bar{a}_{A,j}^{(i)}, \bar{b}_{A,j}^{(i)}, \bar{c}_{A,j}^{(i)})_{i \in [L]}) \quad j \in [\kappa],$$

$$\mathbf{v}_j = ((M'_j[\bar{r}_{w,j}])_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}, j \in [\kappa]} \| (M'_j[\bar{r}'_{w,j}])_{w \in \mathcal{W}, j \in [\kappa]} \|$$

$$(M'_j[\bar{a}_{A,j}^{(i)}], M'_j[\bar{b}_{A,j}^{(i)}], M'_j[\bar{c}_{A,j}^{(i)}])_{i \in [L]}) \quad j \in [\kappa]$$
 from κ instances of $\mathcal{F}_{\text{sVOLE}}^{P_A, \mathcal{V}, \tau, N}$ which can be parsed as $\{(\bar{r}_{w,j}, M'_j[\bar{r}_{w,j}])\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}, j \in [\kappa]}$, $\{(\bar{r}'_{w,j}, M'_j[\bar{r}'_{w,j}])\}_{w \in \mathcal{W}, j \in [\kappa]}$, and $\{(\bar{a}_{A,j}^{(i)}, M'_j[\bar{a}_{A,j}^{(i)}], \bar{b}_{A,j}^{(i)}, M'_j[\bar{b}_{A,j}^{(i)}], \bar{c}_{A,j}^{(i)}, M'_j[\bar{c}_{A,j}^{(i)}])\}_{i \in [L], j \in [\kappa]}$. Similarly, receives outputs from κ instances of $\mathcal{F}_{\text{sVOLE}}^{P_B, \mathcal{V}, \tau, N}$ which can be parsed as $\{(\bar{s}_{w,j}, M'_j[\bar{s}_{w,j}])\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}, j \in [\kappa]}$, $\{(\bar{s}'_{w,j}, M'_j[\bar{s}'_{w,j}])\}_{w \in \mathcal{W}, j \in [\kappa]}$, and $\{(\bar{a}_{B,j}^{(i)}, M'_j[\bar{a}_{B,j}^{(i)}], \bar{b}_{B,j}^{(i)}, M'_j[\bar{b}_{B,j}^{(i)}], \bar{c}_{B,j}^{(i)}, M'_j[\bar{c}_{B,j}^{(i)}])\}_{i \in [L], j \in [\kappa]}$.
 - For $j \in [\kappa]$ and $w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}$, P_A and P_B use protocol Π_{fix} to respectively obtain $(r_w, M'_j[r_w])$, from inputs r_w and $(\bar{r}_{w,j}, M'_j[\bar{r}_{w,j}])$, and $(s_w, M'_j[s_w])$, from inputs s_w and $(\bar{s}_{w,j}, M'_j[\bar{s}_{w,j}])$. Doing this requires each $\hat{r}_{w,j} := \bar{r}_{w,j} \oplus r_w$ and $\hat{s}_{w,j} := \bar{s}_{w,j} \oplus s_w$ to be published.
 - For $j \in [\kappa]$ and $w \in \mathcal{W}$, P_A and P_B use protocol Π_{fix} to respectively obtain $(r'_w, M'_j[r'_w])$, from inputs r'_w and $(\bar{r}'_{w,j}, M'_j[\bar{r}'_{w,j}])$, and $(s'_w, M'_j[s'_w])$, from inputs s'_w and $(\bar{s}'_{w,j}, M'_j[\bar{s}'_{w,j}])$. Doing this requires each $\hat{r}'_{w,j} := \bar{r}'_{w,j} \oplus r'_w$ and $\hat{s}'_{w,j} := \bar{s}'_{w,j} \oplus s'_w$ to be published.
 - For $j \in [\kappa]$ and $i \in [L]$, P_A and P_B use Π_{fix} to achieve $M'_j[a_{A,j}^{(i)}], M'_j[b_{A,j}^{(i)}], M'_j[c_{A,j}^{(i)}], M'_j[a_{B,j}^{(i)}], M'_j[b_{B,j}^{(i)}], M'_j[c_{B,j}^{(i)}]$. Doing this requires $\hat{a}_{p,j}^{(i)} := \bar{a}_{p,j}^{(i)} \oplus a_{p,j}^{(i)}$, $\hat{b}_{p,j}^{(i)} := \bar{b}_{p,j}^{(i)} \oplus b_{p,j}^{(i)}$, $\hat{c}_{p,j}^{(i)} := \bar{c}_{p,j}^{(i)} \oplus c_{p,j}^{(i)}$ for $p \in \{A, B\}$ to be published.
- On input **(get)** from P_A , P_B , and \mathcal{V} , $\Pi_{\text{sVOLE-2PC}}$ works as follows:
 - P_A and \mathcal{V} send **(get)** to κ instances of $\mathcal{F}_{\text{sVOLE}}^{P_A, \mathcal{V}, \tau, N}$ to receive outputs which can be parsed as $\{\Delta'_{A,j}\}_{j \in [\kappa]}$, $\{K'_j[\bar{r}_{w,j}]\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}, j \in [\kappa]}$, $\{K'_j[\bar{r}'_{w,j}]\}_{w \in \mathcal{W}, j \in [\kappa]}$, $\{K'_j[\bar{a}_{A,j}^{(i)}], K'_j[\bar{b}_{A,j}^{(i)}], K'_j[\bar{c}_{A,j}^{(i)}]\}_{i \in [L], j \in [\kappa]}$. Similarly, P_B and \mathcal{V} send **(get)** to κ instances of $\mathcal{F}_{\text{sVOLE}}^{P_B, \mathcal{V}, \tau, N}$ to receive outputs which can be parsed as $\{\Delta'_{B,j}\}_{j \in [\kappa]}$, $\{K'_j[\bar{s}_{w,j}]\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}, j \in [\kappa]}$, $\{K'_j[\bar{s}'_{w,j}]\}_{w \in \mathcal{W}, j \in [\kappa]}$, and $\{K'_j[\bar{a}_{B,j}^{(i)}], K'_j[\bar{b}_{B,j}^{(i)}], K'_j[\bar{c}_{B,j}^{(i)}]\}_{i \in [L], j \in [\kappa]}$.
 - Any party can compute locally, by applying protocol Π_{fix} for all $j \in [\kappa]$, as follows:

$$\forall w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}, K'_j[r_{w,j}] := K'_j[\bar{r}_{w,j}] \oplus \hat{r}_{w,j} \cdot \Delta'_{B,j}, K'_j[s_{w,j}] := K'_j[\bar{s}_{w,j}] \oplus \hat{s}_{w,j} \cdot \Delta'_{A,j};$$

$$\forall w \in \mathcal{W}, K'_j[r'_{w,j}] := K'_j[\bar{r}'_{w,j}] \oplus \hat{r}'_{w,j} \cdot \Delta'_{B,j}, K'_j[s'_{w,j}] := K'_j[\bar{s}'_{w,j}] \oplus \hat{s}'_{w,j} \cdot \Delta'_{A,j};$$

$$\forall i \in [L], K'_j[a_{A,j}^{(i)}] := K'_j[\bar{a}_{A,j}^{(i)}] \oplus \hat{a}_{A,j}^{(i)} \cdot \Delta'_{B,j}, K'_j[a_{B,j}^{(i)}] := K'_j[\bar{a}_{B,j}^{(i)}] \oplus \hat{a}_{B,j}^{(i)} \cdot \Delta'_{A,j};$$

$$\forall i \in [L], K'_j[b_{A,j}^{(i)}] := K'_j[\bar{b}_{A,j}^{(i)}] \oplus \hat{b}_{A,j}^{(i)} \cdot \Delta'_{B,j}, K'_j[b_{B,j}^{(i)}] := K'_j[\bar{b}_{B,j}^{(i)}] \oplus \hat{b}_{B,j}^{(i)} \cdot \Delta'_{A,j};$$

$$\forall i \in [L], K'_j[c_{A,j}^{(i)}] := K'_j[\bar{c}_{A,j}^{(i)}] \oplus \hat{c}_{A,j}^{(i)} \cdot \Delta'_{B,j}, K'_j[c_{B,j}^{(i)}] := K'_j[\bar{c}_{B,j}^{(i)}] \oplus \hat{c}_{B,j}^{(i)} \cdot \Delta'_{A,j}.$$

Fig. 6. Protocol $\Pi_{\text{sVOLE-2PC}}$.

Hence, in total, public transcript of $\Pi_{\text{sVOLE-2PC}}$ costs

$$2 \cdot \kappa \cdot (\text{cost}_{\text{vc-total}} + |\mathcal{I}_A| + |\mathcal{I}_B| + 2 \cdot |\mathcal{W}|)$$

bits of communication.

4.2 Public Auditability 2PC

In this section, we first present the protocol $\Pi_{\text{check-AND}}$, as discussed in Section 3.2, in Figure 7 for checking satisfaction of authenticated shared AND triples. Then, we present the protocols $\Pi_{\text{pa-2PC-pre}}$ (for preprocessing) and $\Pi_{\text{pa-2PC-eval}}$ (for evaluating) in Figures 8 and 9, respectively. These protocols employ $\Pi_{\text{check-AND}}$ as a subroutine for executing and in the $\mathcal{F}_{\text{sVOLE-2PC}}$ -hybrid model. We denote by $\Pi_{\text{pa-2PC}}$ the concatenation of $\Pi_{\text{pa-2PC-pre}}$ and $\Pi_{\text{pa-2PC-eval}}$.

Non-Interactivity and Public Verifiability. Notice that, although Δ_A and Δ_B are not revealed, as explained in Section 3.2, we can verify authenticity of all shared masks via $\{\Delta'_{A,j}, \Delta'_{B,j}\}_{j \in [\kappa]}$ by following the paradigm from [7]. Since $\{\Delta'_{A,j}, \Delta'_{B,j}\}_{j \in [\kappa]}$ are generated via $\mathcal{F}_{\text{sVOLE}}$, as from [7], the protocol realizing $\mathcal{F}_{\text{sVOLE}}$ can be public coin, i.e., $\{\Delta'_{A,j}, \Delta'_{B,j}\}_{j \in [\kappa]}$ can be generated uniformly. Hence, by applying the Fiat-Shamir paradigm [20] for generating $\{\mathcal{I}_j\}_{j \in [\kappa]}$ and $\{\Delta'_{A,j}, \Delta'_{B,j}\}_{j \in [\kappa]}$, we can transform the entire transcript of the concatenated protocol $\Pi_{\text{pa-2PC}}$ obtained from concatenating $\Pi_{\text{pa-2PC-pre}}$ and $\Pi_{\text{pa-2PC-eval}}$ into a non-interactive ZKP, hence making it publicly auditable.

Theorem 2 (Security of $\Pi_{\text{sVOLE-2PC}}$). *The protocol $\Pi_{\text{sVOLE-2PC}}$ in Figure 6 securely realizes $\mathcal{F}_{\text{sVOLE-2PC}}$ in Figure 5 against malicious adversaries with statistical security $2^{-\rho}$ in the $\mathcal{F}_{\text{sVOLE}}$ -hybrid model.*

Theorem 3 (Security of $\Pi_{\text{pa-2PC}}$). *If H is modeled as a random oracle, the combined protocol from concatenating $\Pi_{\text{pa-2PC-pre}}$ and $\Pi_{\text{pa-2PC-eval}}$ in Figures 8 and 9, respectively, securely computes f against malicious adversaries with statistical security $2^{-\rho}$ in the $\mathcal{F}_{\text{sVOLE-2PC}}$ -hybrid model.*

Communication Cost of $\Pi_{\text{check-AND}}$. In step 1, for any $j \in [\kappa]$ and any $i \in [W]$, the total cost is $6 \cdot (2^\tau + 1)$ for published bits and IT-MAC tags. Hence, the total cost in this phase is $\kappa \cdot W \cdot (2^\tau + 1)$. In step 2, there is nothing published. Hence, in total, this protocol costs

$$\kappa \cdot W \cdot (2^\tau + 1)$$

bits of communication.

Communication Cost of $\Pi_{\text{pa-2PC}}$. We now analyze communication cost of $\Pi_{\text{pa-2PC}}$ via $\Pi_{\text{pa-2PC-pre}}$ and $\Pi_{\text{pa-2PC-eval}}$.

For $\Pi_{\text{pa-2PC-pre}}$ in Figure 8, the cost of this protocol mainly concentrates on Phases 3 and 4. In particular, each garbled table costs $4 \cdot (1 + (\kappa + 2) \cdot 2^\tau)$. On the other hand, each commitment from P_B costs $|\text{cma}|$. Hence, in total, the cost for both phases is $4 \cdot |\mathcal{W}| \cdot (1 + (\kappa + 2) \cdot 2^\tau + |\text{cma}|)$.

For $\Pi_{\text{pa-2PC-eval}}$ in Figure 9, publishing for checking authenticated shared AND triples according to $j \in \kappa, i \in [L] \setminus \mathcal{I}_j$ costs in total $6 \cdot \kappa \cdot |\mathcal{W}| \cdot (1 + 2^\tau)$ bits. The cost for the phase **input processing** is $(|\mathcal{I}_A| + |\mathcal{I}_B|) \cdot (1 + (\kappa + 2) \cdot 2^\tau)$. In **circuit evaluation**, the cost is $|\mathcal{W}| \cdot 2 \cdot (1 + (\kappa + 2) \cdot 2^\tau)$. In **output determination**, the cost is $|\mathcal{O}| \cdot (1 + (\kappa + 1) \cdot 2^\tau)$. In **output determination**, the cost is $|\mathcal{O}| \cdot (1 + (\kappa + 1) \cdot 2^\tau)$. Hence, in total, the cost for the entire protocol is $6 \cdot \kappa \cdot |\mathcal{W}| \cdot (1 + 2^\tau) + (|\mathcal{I}_A| + |\mathcal{I}_B|) \cdot (1 + (\kappa + 2) \cdot 2^\tau) + 2 \cdot |\mathcal{W}| \cdot (1 + (\kappa + 2) \cdot 2^\tau) + |\mathcal{O}| \cdot (1 + (\kappa + 1) \cdot 2^\tau)$.

We assume that authenticated P_A 's values (respectively, P_B 's values) are with respect to $\Delta'_{B,j}$ (respectively, $\Delta'_{A,j}$) for $j \in [\kappa]$. E.g., $\llbracket u; \Delta'_{B,j} \rrbracket_A$ is parsed as $M'[u] = K'[u] \oplus u \cdot \Delta'_{B,j}$.

Inputs: $\kappa, W \in \mathbb{N}$.

Execution:

1. On inputs

$$(\text{init}, \{x_{A,j}^{(i)}, y_{A,j}^{(i)}, z_{A,j}^{(i)}, M'_j[x_{A,j}^{(i)}], M'_j[y_{A,j}^{(i)}], M'_j[z_{A,j}^{(i)}]\}_{j \in [\kappa], i \in [W]}, \\ \{a_{A,j}^{(i)}, b_{A,j}^{(i)}, c_{A,j}^{(i)}, M'_j[a_{A,j}^{(i)}], M'_j[b_{A,j}^{(i)}], M'_j[c_{A,j}^{(i)}]\}_{j \in [\kappa], i \in [W]})$$

from P_A and

$$(\text{init}, \{x_{B,j}^{(i)}, y_{B,j}^{(i)}, z_{B,j}^{(i)}, M'_j[x_{B,j}^{(i)}], M'_j[y_{B,j}^{(i)}], M'_j[z_{B,j}^{(i)}]\}_{j \in [\kappa], i \in [W]}, \\ \{a_{B,j}^{(i)}, b_{B,j}^{(i)}, c_{B,j}^{(i)}, M'_j[a_{B,j}^{(i)}], M'_j[b_{B,j}^{(i)}], M'_j[c_{B,j}^{(i)}]\}_{j \in [\kappa], i \in [W]})$$

from P_B , for $j \in [\kappa]$, $i \in [W]$, it works as follows:

- (a) P_A locally computes and sends $d_{A,j}^{(i)} := x_{A,j}^{(i)} \oplus a_{A,j}^{(i)}$, $e_{A,j}^{(i)} := y_{A,j}^{(i)} \oplus b_{A,j}^{(i)}$, $M'_j[d_{A,j}^{(i)}] := M'_j[x_{A,j}^{(i)}] \oplus M'_j[a_{A,j}^{(i)}]$, $M'_j[e_{A,j}^{(i)}] := M'_j[y_{A,j}^{(i)}] \oplus M'_j[b_{A,j}^{(i)}]$ to P_B and \mathcal{V} .
 - (b) P_B locally computes and sends $d_{B,j}^{(i)} := x_{B,j}^{(i)} \oplus a_{B,j}^{(i)}$, $e_{B,j}^{(i)} := y_{B,j}^{(i)} \oplus b_{B,j}^{(i)}$, $M'_j[d_{B,j}^{(i)}] := M'_j[x_{B,j}^{(i)}] \oplus M'_j[a_{B,j}^{(i)}]$, $M'_j[e_{B,j}^{(i)}] := M'_j[y_{B,j}^{(i)}] \oplus M'_j[b_{B,j}^{(i)}]$ to P_A and \mathcal{V} .
 - (c) Each party (P_A , P_B , and \mathcal{V}) locally computes $d_j^{(i)} := d_{A,j}^{(i)} \oplus d_{B,j}^{(i)}$ and $e_j^{(i)} := e_{A,j}^{(i)} \oplus e_{B,j}^{(i)}$.
 - (d) P_A locally computes and sends $\tilde{z}_{A,j}^{(i)} := z_{A,j}^{(i)} \oplus c_{A,j}^{(i)} \oplus d_j^{(i)} \cdot b_{A,j}^{(i)} \oplus e_j^{(i)} \cdot a_{A,j}^{(i)}$ and $M'_j[\tilde{z}_{A,j}^{(i)}] := M'_j[z_{A,j}^{(i)}] \oplus M'_j[c_{A,j}^{(i)}] \oplus d_j^{(i)} \cdot M'_j[b_{A,j}^{(i)}] \oplus e_j^{(i)} \cdot M'_j[a_{A,j}^{(i)}]$ to P_B and \mathcal{V} .
 - (e) P_B locally computes and sends $\tilde{z}_{B,j}^{(i)} := z_{B,j}^{(i)} \oplus c_{B,j}^{(i)} \oplus d_j^{(i)} \cdot b_{B,j}^{(i)} \oplus e_j^{(i)} \cdot a_{B,j}^{(i)}$ and $M'_j[\tilde{z}_{B,j}^{(i)}] := M'_j[z_{B,j}^{(i)}] \oplus M'_j[c_{B,j}^{(i)}] \oplus d_j^{(i)} \cdot M'_j[b_{B,j}^{(i)}] \oplus e_j^{(i)} \cdot M'_j[a_{B,j}^{(i)}]$ to P_A and \mathcal{V} .
 - (f) Each party (P_A , P_B , and \mathcal{V}) checks whether $\tilde{z}_{A,j}^{(i)} \oplus \tilde{z}_{B,j}^{(i)} \oplus d_j^{(i)} \cdot e_j^{(i)} = 0$.
2. On common inputs $\{\Delta'_{A,j}, \Delta'_{B,j}\}_{j \in [\kappa]}$ and inputs
- $$(\text{check}, \{K'_j[x_{A,j}^{(i)}], K'_j[y_{A,j}^{(i)}], K'_j[z_{A,j}^{(i)}]\}_{j \in [\kappa], i \in [W]}, \{K'_j[a_{A,j}^{(i)}], K'_j[b_{A,j}^{(i)}], K'_j[c_{A,j}^{(i)}]\}_{j \in [\kappa], i \in [W]})$$
- from P_A and
- $$(\text{check}, \{K'_j[x_{B,j}^{(i)}], K'_j[y_{B,j}^{(i)}], K'_j[z_{B,j}^{(i)}]\}_{j \in [\kappa], i \in [W]}, \{K'_j[a_{B,j}^{(i)}], K'_j[b_{B,j}^{(i)}], K'_j[c_{B,j}^{(i)}]\}_{j \in [\kappa], i \in [W]})$$
- from P_B , for $j \in [\kappa]$, $i \in [W]$, it works as follows:
- (a) P_A sends $K'_j[x_{A,j}^{(i)}]$, $K'_j[y_{A,j}^{(i)}]$, $K'_j[z_{A,j}^{(i)}]$, $\{K'_j[a_{A,j}^{(i)}], K'_j[b_{A,j}^{(i)}], K'_j[c_{A,j}^{(i)}]\}$ to P_B and \mathcal{V} .
 - (b) P_B sends $K'_j[x_{B,j}^{(i)}]$, $K'_j[y_{B,j}^{(i)}]$, $K'_j[z_{B,j}^{(i)}]$, $\{K'_j[a_{B,j}^{(i)}], K'_j[b_{B,j}^{(i)}], K'_j[c_{B,j}^{(i)}]\}$ to P_A and \mathcal{V} .
 - (c) Each party (P_A , P_B , and \mathcal{V}) computes $K'_j[d_{A,j}^{(i)}] := K'_j[x_{A,j}^{(i)}] \oplus K'_j[a_{A,j}^{(i)}]$, $K'_j[e_{A,j}^{(i)}] := K'_j[y_{A,j}^{(i)}] \oplus K'_j[b_{A,j}^{(i)}]$ and checks whether $M'_j[d_{A,j}^{(i)}] = K'_j[d_{A,j}^{(i)}] \oplus d_{A,j}^{(i)} \cdot \Delta'_{B,j}$, $M'_j[e_{A,j}^{(i)}] = K'_j[e_{A,j}^{(i)}] \oplus e_{A,j}^{(i)} \cdot \Delta'_{B,j}$.
 - (d) Each party (P_A , P_B , and \mathcal{V}) computes $K'_j[d_{B,j}^{(i)}] := K'_j[x_{B,j}^{(i)}] \oplus K'_j[a_{B,j}^{(i)}]$, $K'_j[e_{B,j}^{(i)}] := K'_j[y_{B,j}^{(i)}] \oplus K'_j[b_{B,j}^{(i)}]$ and checks whether $M'_j[d_{B,j}^{(i)}] = K'_j[d_{B,j}^{(i)}] \oplus d_{B,j}^{(i)} \cdot \Delta'_{A,j}$, $M'_j[e_{B,j}^{(i)}] = K'_j[e_{B,j}^{(i)}] \oplus e_{B,j}^{(i)} \cdot \Delta'_{A,j}$.
 - (e) Each party (P_A , P_B , and \mathcal{V}) computes $K'_j[\tilde{z}_{A,j}^{(i)}] := K'_j[z_{A,j}^{(i)}] \oplus K'_j[c_{A,j}^{(i)}] \oplus d_j^{(i)} \cdot K'_j[b_{A,j}^{(i)}] \oplus e_j^{(i)} \cdot K'_j[a_{A,j}^{(i)}]$ and checks whether $M'_j[\tilde{z}_{A,j}^{(i)}] = K'_j[\tilde{z}_{A,j}^{(i)}] \oplus \tilde{z}_{A,j}^{(i)} \cdot \Delta'_{B,j}$.
 - (f) Each party (P_A , P_B , and \mathcal{V}) computes $K'_j[\tilde{z}_{B,j}^{(i)}] := K'_j[z_{B,j}^{(i)}] \oplus K'_j[c_{B,j}^{(i)}] \oplus d_j^{(i)} \cdot K'_j[b_{B,j}^{(i)}] \oplus e_j^{(i)} \cdot K'_j[a_{B,j}^{(i)}]$ and checks whether $M'_j[\tilde{z}_{B,j}^{(i)}] = K'_j[\tilde{z}_{B,j}^{(i)}] \oplus \tilde{z}_{B,j}^{(i)} \cdot \Delta'_{A,j}$.

Fig. 7. Sub-Protocol $\Pi_{\text{check-AND}}$.

Inputs: Both parties P_A and P_B agree on a common circuit for function $f : \{0, 1\}^{|\mathcal{I}_A|} \times \{0, 1\}^{|\mathcal{I}_B|} \rightarrow \{0, 1\}^{|\mathcal{O}|}$. In the input-preprocessing phase, P_A and P_B holds $\mathbf{x} = (z_i)_{i \in \mathcal{I}_A} \in \{0, 1\}^{|\mathcal{I}_A|}$ and $\mathbf{y} = (z_i)_{i \in \mathcal{I}_B} \in \{0, 1\}^{|\mathcal{I}_B|}$, respectively. P_A and P_B also keeps common parameters κ and L . **There is a verifier \mathcal{V} verifying the computation of P_A and P_B .**

Function-Independent Preprocessing:

1. P_A and P_B send init to \mathcal{F}_{pre} to receives Δ_A and Δ_B , respectively.
2. For each $w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}$, both P_A and P_B send random to \mathcal{F}_{pre} . In return, \mathcal{F}_{pre} sends $(r_w, M[r_w], K[s_w])$ and $(s_w, M[s_w], K[r_w])$ to P_A and P_B , respectively. Define $\lambda_w := r_w \oplus s_w$. P_A picks a uniform κ -bit string $L_{w,0}$ where κ denotes the computational security parameter.
3. Both P_A and P_B invokes \mathcal{F}_{pre} L times for L pairs of triples $(a_{A,j}^{(i)}, b_{A,j}^{(i)}, c_{A,j}^{(i)})$ and $(a_{B,j}^{(i)}, b_{B,j}^{(i)}, c_{B,j}^{(i)})$ satisfying $(a_{A,j}^{(i)} \oplus a_{B,j}^{(i)}) \cdot (b_{A,j}^{(i)} \oplus b_{B,j}^{(i)}) = c_{A,j}^{(i)} \oplus c_{B,j}^{(i)}$ for $i \in [L]$ and $j \in [\kappa]$.
4. P_A and P_B respectively send (init, $\{r_w\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}, \{r'_w\}_{w \in \mathcal{W}}, \{a_{A,i}^{(j)}, b_{A,i}^{(j)}, c_{A,i}^{(j)}\}_{j \in [\kappa], i \in [L]}$) and (init, $\{s_w\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}, \{s'_w\}_{w \in \mathcal{W}}, \{a_{B,i}^{(j)}, b_{B,i}^{(j)}, c_{B,i}^{(j)}\}_{j \in [\kappa], i \in [L]}$) to $\mathcal{F}_{\text{SVOLE-2PC}}$. Then, P_A (respectively, P_B) receives $\{M'_j[r_w]\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}, \{M'_j[r'_w]\}_{w \in \mathcal{W}}, \{M'_j[a_{A,i}^{(j)}], M'_j[b_{A,i}^{(j)}], M'_j[c_{A,i}^{(j)}]\}_{j \in [\kappa], i \in [L]}$ (respectively, $\{M'_j[s_w]\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}, \{M'_j[s'_w]\}_{w \in \mathcal{W}}, \{M'_j[a_{B,i}^{(j)}], M'_j[b_{B,i}^{(j)}], M'_j[c_{B,i}^{(j)}]\}_{j \in [\kappa], i \in [L]}$).

Function-Dependent Preprocessing: This is divided into two phases:

(i) *Phase 1:* Following the topological ordering of the circuit, P_A and P_B work as follows:

1. For each XOR gate $(\alpha, \beta, \gamma, \oplus)$, P_A computes $(r_\gamma, M[r_\gamma], K[s_\gamma]) := (r_\alpha \oplus r_\beta, M[r_\alpha] \oplus M[r_\beta], K[s_\alpha] \oplus K[s_\beta])$ and $L_{\gamma,0} := L_{\alpha,0} \oplus L_{\beta,0}$. P_B computes $(s_\gamma, M[s_\gamma], K[r_\gamma]) := (s_\alpha \oplus s_\beta, M[s_\alpha] \oplus M[s_\beta], K[r_\alpha] \oplus K[r_\beta])$.
2. For each AND gate $(\alpha, \beta, \gamma, \cdot)$:
 - P_A (respectively, P_B) sends (and, $(r_\alpha, M[r_\alpha], K[s_\alpha]), (r_\beta, M[r_\beta], K[s_\beta])$) (respectively, (and, $(s_\alpha, M[s_\alpha], K[r_\alpha]), (s_\beta, M[s_\beta], K[r_\beta])$)) to \mathcal{F}_{pre} . In return, \mathcal{F}_{pre} sends $(r'_\gamma, M[r'_\gamma], K[s'_\gamma])$ and $(s'_\gamma, M[s'_\gamma], K[r'_\gamma])$ respectively to P_A and P_B . Here, $r'_\gamma \oplus s'_\gamma = \lambda_\alpha \cdot \lambda_\beta$.
 - $\forall k \in [0, 3]$, let $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$, i.e., $k = k_0 + 2 \cdot k_1$, and P_A computes
$$r_{\gamma,k} := r'_\gamma \oplus r_\gamma \oplus k_0 \cdot r_\alpha \oplus k_1 \cdot r_\beta,$$

$$M[r_{\gamma,k}] := M[r'_\gamma] \oplus M[r_\gamma] \oplus k_0 \cdot M[r_\beta] \oplus k_1 \cdot M[r_\alpha],$$

$$K[r_{\gamma,k}] := K[r'_\gamma] \oplus K[r_\gamma] \oplus k_0 \cdot K[r_\beta] \oplus k_1 \cdot K[r_\alpha] \oplus k_0 \cdot k_1 \cdot \Delta_A.$$
 - $\forall k \in [0, 3]$, let $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$, i.e., $k = k_0 + 2 \cdot k_1$, and P_B computes
$$s_{\gamma,k} := s'_\gamma \oplus s_\gamma \oplus k_0 \cdot s_\beta \oplus k_1 \cdot s_\alpha \oplus k_0 \cdot k_1,$$

$$M[s_{\gamma,k}] := M[s'_\gamma] \oplus M[s_\gamma] \oplus k_0 \cdot M[s_\beta] \oplus k_1 \cdot M[s_\alpha],$$

$$K[s_{\gamma,k}] := K[s'_\gamma] \oplus K[s_\gamma] \oplus k_0 \cdot K[s_\beta] \oplus k_1 \cdot K[s_\alpha].$$

(ii) *Phase 2 (for Making VOLEith Proof and Garbled Tables):* For $w \in \mathcal{W}$, P_A and P_B use protocol Π_{fix} to respectively obtain $(r'_w, M[r'_w])$ and $(s'_w, M[s'_w])$ where r'_w and s'_w are already achieved in Phase 1. Doing this requires each $\bar{r}'_w \oplus r'_w$ and $\bar{s}'_w \oplus s'_w$ to be published. Then, both parties run as follows:

- For each XOR gate $(\alpha, \beta, \gamma, \oplus)$, for $j \in [\kappa]$, P_A (respectively, P_B) computes $M'_j[r_\gamma] := M'_j[r_\alpha] \oplus M'_j[r_\beta]$ (respectively, $M'_j[s_\gamma] := M'_j[s_\alpha] \oplus M'_j[s_\beta]$).
- For each AND gate $(\alpha, \beta, \gamma, \cdot)$:
 - $\forall k \in [0, 3], \forall j \in [\kappa]$, let $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$, i.e., $k = k_0 + 2 \cdot k_1$, and P_A computes
$$M'_j[r_{\gamma,k}] := M'_j[r'_\gamma] \oplus M'_j[r_\gamma] \oplus k_0 \cdot M'_j[r_\beta] \oplus k_1 \cdot M'_j[r_\alpha].$$
 - $\forall k \in [0, 3], \forall j \in [\kappa]$, let $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$, i.e., $k = k_0 + 2 \cdot k_1$, and P_B computes
$$M'_j[s_{\gamma,k}] := M'_j[s'_\gamma] \oplus M'_j[s_\gamma] \oplus k_0 \cdot M'_j[s_\beta] \oplus k_1 \cdot M'_j[s_\alpha].$$

(iii) *Phase 3 (for Encrypting and Committing):* For each AND gate $(\alpha, \beta, \gamma, \cdot)$,

- P_A computes $L_{\alpha,1} := L_{\alpha,0} \oplus \Delta_A$ and $L_{\beta,1} := L_{\beta,0} \oplus \Delta_A$. P_A publishes the following, for all $k \in [0, 3]$ with $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$:

$$G_{\gamma,k} := H(L_{\alpha,k_0}, L_{\beta,k_1}, \gamma, k) \oplus (r_{\gamma,k}, M[r_{\gamma,k}], \{M'_j[r_{\gamma,k}]\}_{j \in [\kappa]}, L_{\gamma,k} \oplus K[s_{\gamma,k}] \oplus r_{\gamma,k} \cdot \Delta_A).$$

- For $k \in [0, 3]$, P_B commits to $(r_{\gamma,k}, \{M'_j[r_{\gamma,k}]\}_{j \in [\kappa]})$ to obtain and publish $\text{cmab}_{\gamma,k}$.

(iv) *Phase 4:* For $w \in \mathcal{O}$, P_A commits to $(r_w, \{M'_j[r_w]\}_{j \in [\kappa]})$ to obtain and publish $\text{cmo}_{A,w}$.

Fig. 8. Protocol $\Pi_{\text{pa-2PC-pre}}$ for Preprocessing for Publicly Auditable 2PC.

Proof Preparation:

1. \mathcal{V} samples and sends $\{\mathcal{I}_j\}_{j \in [\kappa]}$, where $\mathcal{I}_j \subseteq [L]$ and $|\mathcal{I}_j| = |\mathcal{W}|$ for $j \in [\kappa]$.
2. P_A publishes $\{a_{A,i}^{(j)}, b_{A,i}^{(j)}, c_{A,i}^{(j)}, M'_j[a_{A,i}^{(j)}], M'_j[b_{A,i}^{(j)}], M'_j[c_{A,i}^{(j)}]\}_{j \in [\kappa], i \in [L] \setminus \mathcal{I}_j}$.
3. P_B publishes $\{a_{B,i}^{(j)}, b_{B,i}^{(j)}, c_{B,i}^{(j)}, M'_j[a_{B,i}^{(j)}], M'_j[b_{B,i}^{(j)}], M'_j[c_{B,i}^{(j)}]\}_{j \in [\kappa], i \in [L] \setminus \mathcal{I}_j}$.
4. For each $j \in [\kappa]$ and each $i \in [L] \setminus \mathcal{I}_j$, each party (P_A , P_B , and \mathcal{V}) locally checks whether
$$(a_{A,j}^{(i)} \oplus a_{B,j}^{(i)}) \cdot (b_{A,j}^{(i)} \oplus b_{B,j}^{(i)}) = c_{A,j}^{(i)} \oplus c_{B,j}^{(i)}.$$
5. P_A and P_B respectively send
$$(\text{init}, \{r_\alpha, r_\beta, r'_\gamma, M'_j[r_\alpha], M'_j[r_\beta], M'_j[r'_\gamma]\}_{j \in [\kappa]}, \text{all } (\alpha, \beta, \gamma, \cdot)),$$

$$\{a_{A,j}^{(i)}, b_{A,j}^{(i)}, c_{A,j}^{(i)}, M'_j[a_{A,j}^{(i)}], M'_j[b_{A,j}^{(i)}], M'_j[c_{A,j}^{(i)}]\}_{j \in [\kappa], i \in \mathcal{I}_j} \text{ and}$$

$$(\text{init}, \{s_\alpha, s_\beta, s'_\gamma, M'_j[s_\alpha], M'_j[s_\beta], M'_j[s'_\gamma]\}_{j \in [\kappa]}, \text{all } (\alpha, \beta, \gamma, \cdot)),$$

$$\{a_{B,j}^{(i)}, b_{B,j}^{(i)}, c_{B,j}^{(i)}, M'_j[a_{B,j}^{(i)}], M'_j[b_{B,j}^{(i)}], M'_j[c_{B,j}^{(i)}]\}_{j \in [\kappa], i \in \mathcal{I}_j}$$
to protocol $\Pi_{\text{check-AND}}$.
6. P_A , P_B , and \mathcal{V} send (get) to $\mathcal{F}_{\text{3VOLE-2PC}}$ to obtain $\{\Delta'_{A,j}\}_{j \in [\kappa]}$, $\{K'_j[r_w]\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}$, $\{K'_j[r_w]\}_{w \in \mathcal{W}}$, $\{K'_j[a_{0,i}^{(j)}], K'_j[b_{0,i}^{(j)}], K'_j[c_{0,i}^{(j)}]\}_{j \in [\kappa], i \in [L]}$, $\{\Delta'_{B,j}\}_{j \in [\kappa]}$, $\{K'_j[s_w]\}_{w \in \mathcal{I}_A \cup \mathcal{I}_B \cup \mathcal{W}}$, $\{K'_j[s_w]\}_{w \in \mathcal{W}}$, $\{K'_j[a_{1,i}^{(j)}], K'_j[b_{1,i}^{(j)}], K'_j[c_{1,i}^{(j)}]\}_{j \in [\kappa], i \in [L]}$.
7. For each $j \in [\kappa]$, each $i \in [L] \setminus \mathcal{I}_j$, each $p \in \{A, B\}$, each party (P_A , P_B , and \mathcal{V}) checks whether $M'_j[a_{p,j}^{(i)}] = K'_j[a_{p,j}^{(i)}] \oplus a_{p',j}^{(i)} \cdot \Delta'_{p',j}$, $M'_j[b_{p,j}^{(i)}] = K'_j[b_{p,j}^{(i)}] \oplus b_{p',j}^{(i)} \cdot \Delta'_{p',j}$ and $M'_j[c_{p,j}^{(i)}] = K'_j[c_{p,j}^{(i)}] \oplus c_{p',j}^{(i)} \cdot \Delta'_{p',j}$ where $p' = B$, if $p = A$, and $p' = A$, otherwise.
8. The parties work as follows:
 - (a) For each XOR gate $(\alpha, \beta, \gamma, \oplus)$, $\forall j \in [\kappa]$, each party (P_A , P_B , and \mathcal{V}) computes $K'_j[r_\gamma] := K'_j[r_\alpha] \oplus K'_j[r_\beta]$ (respectively, $K'_j[s_\gamma] := K'_j[s_\alpha] \oplus K'_j[s_\beta]$).
 - (b) For each AND gate $(\alpha, \beta, \gamma, \cdot)$, $\forall k \in [0, 3]$, let $(k_0, k_1) := \text{bin}(k) \in \{0, 1\}^2$, i.e., $k = k_0 + 2 \cdot k_1$, $\forall j \in [\kappa]$, each party (P_A , P_B , and \mathcal{V}) computes
$$K'_j[r_{\gamma,k}] := K'_j[r'_\gamma] \oplus K'_j[r_\gamma] \oplus k_0 \cdot M'_j[r_\alpha] \oplus k_1 \cdot K'_j[r_\beta],$$

$$K'_j[s_{\gamma,k}] := K'_j[s'_\gamma] \oplus K'_j[s_\gamma] \oplus k_0 \cdot M'_j[s_\alpha] \oplus k_1 \cdot K'_j[s_\beta].$$

Input Processing:

1. For $w \in \mathcal{I}_A$, P_A publishes $(r_w, M[r_w], (M'_j[r_w])_{j \in [\kappa]})$ to P_B . P_B checks whether $M[r_w] = K[r_w] \oplus r_w \cdot \Delta_B$ and $M'_j[r_w] = K'_j[r_w] \oplus r_w \cdot \Delta_{B,j} \forall j \in [\kappa]$. P_B then publishes $\hat{z}_w = z_w \oplus \lambda_w = z_w \oplus r_w \oplus s_w$ to P_A . Finally, P_A publishes L_{w, \hat{z}_w} .
2. For $w \in \mathcal{I}_B$, P_B publishes $(s_w, M[s_w], (M'_j[s_w])_{j \in [\kappa]})$ to P_A . P_A checks whether $M[s_w] = K[s_w] \oplus s_w \cdot \Delta_A$ and $M'_j[s_w] = K'_j[s_w] \oplus s_w \cdot \Delta_{A,j} \forall j \in [\kappa]$. P_A then publishes $\hat{z}_w = z_w \oplus \lambda_w = z_w \oplus r_w \oplus s_w$ and L_{w, \hat{z}_w} .

Circuit Evaluation:

- P_B evaluates the circuit following a topological order. For each gate $(\alpha, \beta, \gamma, \text{op})$ where $\text{op} \in \{\cdot, \oplus\}$, P_B holds $(\hat{z}_\alpha, L_{\alpha, \hat{z}_\alpha})$ and $(\hat{z}_\beta, L_{\beta, \hat{z}_\beta})$ where $\hat{z}_\alpha = z_\alpha \oplus \lambda_\alpha$ and $\hat{z}_\beta = z_\beta \oplus \lambda_\beta$.
 - If $\text{op} = \oplus$, P_B computes $\hat{z}_\gamma := \hat{z}_\alpha \oplus \hat{z}_\beta$ and $L_{\gamma, \hat{z}_\gamma} := L_{\alpha, \hat{z}_\alpha} \oplus L_{\beta, \hat{z}_\beta}$.
 - If $\text{op} = \cdot$, P_B computes $k := 2\hat{z}_\alpha + \hat{z}_\beta$. Then, P_B computes $(r_{\gamma,k}, M[r_{\gamma,k}], \{M'_j[r_{\gamma,k}]\}_{j \in [\kappa]}, L_{\gamma,0} \oplus K[s_{\gamma,k}] \oplus r_{\gamma,k} \cdot \Delta_A) := G_{\gamma,k} \oplus H(L_{\alpha, \hat{z}_\alpha}, L_{\beta, \hat{z}_\beta}, \gamma, k)$. Then, P_B checks whether $M'_j[r_{\gamma,k}] = K'_j[r_{\gamma,k}] \oplus r_{\gamma,k} \cdot \Delta'_{B,j} \forall j \in [\kappa]$. If so, P_B computes and publishes $\hat{z}_\gamma = s_{\gamma,k} \oplus r_{\gamma,k}$ and $L_{\gamma, \hat{z}_\gamma} := (L_{\gamma,0} \oplus K[s_{\gamma,k}] \oplus r_{\gamma,k} \cdot \Delta_A) \oplus M[s_{\gamma,k}]$. P_B opens commitment $\text{cm}_{B, \gamma, k}$ to publish $(s_{\gamma,k}, \{M'_j[s_{\gamma,k}]\}_{j \in [\kappa]})$. Then, P_A and \mathcal{V} checks whether $M'_j[s_{\gamma,k}] = K'_j[s_{\gamma,k}] \oplus s_{\gamma,k} \cdot \Delta'_{B,j} \forall j \in [\kappa]$.

Output Determination:

- For $w \in \mathcal{O}$, P_A opens cmo_w to obtain $(r_w, \{M'_j[r_w]\}_{j \in [\kappa]})$ and publishes $(r_w, M[r_w], \{M'_j[r_w]\}_{j \in [\kappa]})$. P_B and \mathcal{V} check whether $M[r_w] = L[r_w] \oplus r_w \cdot \Delta_B$ and $M'_j[r_w] = L'_j[r_w] \oplus r_w \cdot \Delta'_{B,j} \forall j \in [\kappa]$, and whether $\{M'_j[r_w]\}_{j \in [\kappa]}$ is a valid opening of cmo_w . If so, P_B computes $z_w := \hat{z}_w \oplus r_w \oplus s_w$.
- P_A and P_B respectively send
$$(\text{check}, \{K'_j[r_\alpha], K'_j[r_\beta], K'_j[r'_\gamma]\}_{j \in [\kappa]}, \text{all } (\alpha, \beta, \gamma, \cdot), \{K'_j[a_{A,j}^{(i)}], K'_j[b_{A,j}^{(i)}], K'_j[c_{A,j}^{(i)}]\}_{j \in [\kappa], i \in \mathcal{I}_j}) \text{ and}$$

$$(\text{check}, \{K'_j[s_\alpha], K'_j[s_\beta], K'_j[s'_\gamma]\}_{j \in [\kappa]}, \text{all } (\alpha, \beta, \gamma, \cdot), \{K'_j[a_{B,j}^{(i)}], K'_j[b_{B,j}^{(i)}], K'_j[c_{B,j}^{(i)}]\}_{j \in [\kappa], i \in \mathcal{I}_j})$$
to $\Pi_{\text{check-AND}}$ for verifying the validity of authenticated values used for proving AND gates.

Fig. 9. Protocol $\Pi_{\text{pa-2PC-eval}}$ for Evaluation for Publicly Auditable 2PC.

References

1. Aguilar-Melchor, C., Gama, N., Howe, J., Hülsing, A., Joseph, D., Yue, D.: The Return of the SDitH. In: *Advances in Cryptology – EUROCRYPT 2023. Lecture Notes in Computer Science*, vol. 14008, pp. 564–596. Springer Nature Switzerland (2023). https://doi.org/10.1007/978-3-031-30589-4_20
2. Aguilar-Melchor, C., Hülsing, A., Joseph, D., Majenz, C., Ronen, E., Yue, D.: SDitH in the QROM. In: *Advances in Cryptology – ASIACRYPT 2023. Lecture Notes in Computer Science*, vol. 14444, pp. 317–350. Springer Nature Singapore (2023). https://doi.org/10.1007/978-981-99-8739-9_11
3. Araki, T., Barak, A., Furukawa, J., Lichter, T., Lindell, Y., Nof, A., Ohara, K., Watzman, A., Weinstein, O.: Optimized Honest-Majority MPC for Malicious Adversaries — Breaking the 1 Billion-Gate Per Second Barrier. In: *2017 IEEE Symposium on Security and Privacy – S&P 2017*. pp. 843–862. IEEE (2017). <https://doi.org/10.1109/SP.2017.15>
4. Baldimtsi, F., Kiayias, A., Zacharias, T., Zhang, B.: Crowd Verifiable Zero-Knowledge and End-to-End Verifiable Multiparty Computation. In: *Advances in Cryptology – ASIACRYPT 2020. Lecture Notes in Computer Science*, vol. 12493, pp. 717–748. Springer International Publishing (2020). https://doi.org/10.1007/978-3-030-64840-4_24
5. Baum, C., Braun, L., Munch-Hansen, A., Razet, B., Scholl, P.: Appenzeller to Brie: Efficient Zero-Knowledge Proofs for Mixed-Mode Arithmetic and Z2k. In: *Proceedings of the 2021 ACM SIGSAC Conference on Computer and Communications Security – CCS 2021*. p. 192–211. Association for Computing Machinery (2021). <https://doi.org/10.1145/3460120.3484812>
6. Baum, C., Braun, L., Munch-Hansen, A., Scholl, P.: Moz \mathbb{Z}_{2^k} arella: Efficient Vector-OLE and Zero-Knowledge Proofs over \mathbb{Z}_{2^k} . In: *Advances in Cryptology – CRYPTO 2022. Lecture Notes in Computer Science*, vol. 13510, pp. 329–358. Springer Nature Switzerland (2022). https://doi.org/10.1007/978-3-031-15985-5_12
7. Baum, C., Braun, L., de Saint Guilhem, C.D., Kloof, M., Orsini, E., Roy, L., Scholl, P.: Publicly Verifiable Zero-Knowledge and Post-Quantum Signatures from VOLE-in-the-Head. In: *Advances in Cryptology – CRYPTO 2023. Lecture Notes in Computer Science*, vol. 14085, pp. 581–615. Springer Nature Switzerland (2023). https://doi.org/10.1007/978-3-031-38554-4_19
8. Baum, C., Damgård, I., Orlandi, C.: Publicly Auditable Secure Multi-Party Computation. In: *Security and Cryptography for Networks – SCN 2014. Lecture Notes in Computer Science*, vol. 8642, pp. 175–196. Springer International Publishing (2014). https://doi.org/10.1007/978-3-319-10879-7_11
9. Baum, C., Malozemoff, A.J., Rosen, M.B., Scholl, P.: Mac’n’Cheese: Zero-knowledge proofs for boolean and arithmetic circuits with nested disjunctions. In: *Advances in Cryptology – CRYPTO 2021. Lecture Notes in Computer Science*, vol. 12828, pp. 92–122. Springer International Publishing (2021). https://doi.org/10.1007/978-3-030-84259-8_4
10. Baum, C., Nof, A.: Concretely-Efficient Zero-Knowledge Arguments for Arithmetic Circuits and Their Application to Lattice-Based Cryptography. In: *Public-Key Cryptography – PKC 2020. Lecture Notes in Computer Science*, vol. 12110, pp. 495–526. Springer International Publishing (2020). https://doi.org/10.1007/978-3-030-45374-9_17
11. Bui, D.: Shorter VOLEitH Signature from Multivariate Quadratic. *Cryptology ePrint Archive, Paper 2024/465* (2024), <https://eprint.iacr.org/2024/465>

12. Carozza, E., Couteau, G., Joux, A.: Short Signatures from Regular Syndrome Decoding in the Head. In: *Advances in Cryptology – EUROCRYPT 2023. Lecture Notes in Computer Science*, vol. 14008, pp. 532–563. Springer Nature Switzerland (2023). https://doi.org/10.1007/978-3-031-30589-4_19
13. Chase, M., Derler, D., Goldfeder, S., Orlandi, C., Ramacher, S., Rechberger, C., Slamanig, D., Zaverucha, G.: Post-Quantum Zero-Knowledge and Signatures from Symmetric-Key Primitives. In: *Proceedings of the 2017 ACM SIGSAC Conference on Computer and Communications Security – CCS 2017*. p. 1825–1842. Association for Computing Machinery (2017). <https://doi.org/10.1145/3133956.3133997>
14. Cui, H., Liu, H., Yan, D., Yang, K., Yu, Y., Zhang, K.: ReSolveD: Shorter Signatures from Regular Syndrome Decoding and VOLE-in-the-Head. In: *Public-Key Cryptography – PKC 2024. Lecture Notes in Computer Science*, vol. 14601, pp. 229–258. Springer Nature Switzerland, Cham (2024). https://doi.org/10.1007/978-3-031-57718-5_8
15. Cui, H., Wang, X., Yang, K., Yu, Y.: Actively Secure Half-Gates with Minimum Overhead Under Duplex Networks. In: *Advances in Cryptology – EUROCRYPT 2023. Lecture Notes in Computer Science*, vol. 14005, pp. 35–67. Springer Nature Switzerland (2023). https://doi.org/10.1007/978-3-031-30617-4_2
16. Dittmer, S., Ishai, Y., Lu, S., Ostrovsky, R.: Improving Line-Point Zero Knowledge: Two Multiplications for the Price of One. In: *Proceedings of the 2022 ACM SIGSAC Conference on Computer and Communications Security – CCS 2022*. p. 829–841. Association for Computing Machinery (2022). <https://doi.org/10.1145/3548606.3559385>
17. Dittmer, S., Ishai, Y., Ostrovsky, R.: Line-Point Zero Knowledge and Its Applications. In: *2nd Conference on Information-Theoretic Cryptography – ITC 2021. Leibniz International Proceedings in Informatics (LIPIcs)*, vol. 199. Schloss Dagstuhl – Leibniz-Zentrum für Informatik (2021). <https://doi.org/10.4230/LIPIcs.ITC.2021.5>
18. Evans, D., Kolesnikov, V., Rosulek, M.: A Pragmatic Introduction to Secure Multiparty Computation. *Found. Trends Priv. Secur.* **2**(2-3), 70–246 (2018). <https://doi.org/10.1561/33000000019>
19. Feneuil, T., Joux, A., Rivain, M.: Syndrome Decoding in the Head: Shorter Signatures from Zero-Knowledge Proofs. In: *Advances in Cryptology – CRYPTO 2022. Lecture Notes in Computer Science*, vol. 13508, pp. 541–572. Springer Nature Switzerland (2022). https://doi.org/10.1007/978-3-031-15979-4_19
20. Fiat, A., Shamir, A.: How To Prove Yourself: Practical Solutions to Identification and Signature Problems. In: *Advances in Cryptology – CRYPTO 1986. Lecture Notes in Computer Science*, vol. 263, pp. 186–194. Springer Berlin Heidelberg (1987). https://doi.org/10.1007/3-540-47721-7_12
21. Ishai, Y., Kushilevitz, E., Ostrovsky, R., Sahai, A.: Zero-knowledge from secure multiparty computation. In: *Proceedings of the Thirty-Ninth Annual ACM Symposium on Theory of Computing – STOC 2007*. p. 21–30. Association for Computing Machinery (2007). <https://doi.org/10.1145/1250790.1250794>
22. Jarecki, S., Shmatikov, V.: Efficient two-party secure computation on committed inputs. In: *Advances in Cryptology – EUROCRYPT 2007. Lecture Notes in Computer Science*, vol. 4515, pp. 97–114. Springer Berlin Heidelberg (2007). https://doi.org/10.1007/978-3-540-72540-4_6
23. Katz, J., Kolesnikov, V., Wang, X.: Improved Non-Interactive Zero Knowledge with Applications to Post-Quantum Signatures. In: *Proceedings of the 2018 ACM SIGSAC Conference on Computer and Communications Security – CCS 2018*.

- p. 525–537. Association for Computing Machinery (2018). <https://doi.org/10.1145/3243734.3243805>
24. Katz, J., Ranellucci, S., Rosulek, M., Wang, X.: Optimizing authenticated garbling for faster secure two-party computation. In: *Advances in Cryptology – CRYPTO 2018. Lecture Notes in Computer Science*, vol. 10993, pp. 365–391. Springer International Publishing (2018). https://doi.org/10.1007/978-3-319-96878-0_13
 25. Ozdemir, A., Boneh, D.: Experimenting with Collaborative zk-SNARKs: Zero-Knowledge Proofs for Distributed Secrets. In: *31st USENIX Security Symposium – USENIX Security 2022*. pp. 4291–4308. USENIX Association (2022), <https://www.usenix.org/conference/usenixsecurity22/presentation/ozdemir>
 26. Roy, L.: SoftSpokenOT: Quieter OT Extension from Small-Field Silent VOLE in the Minicrypt Model. In: *Advances in Cryptology – CRYPTO 2022. Lecture Notes in Computer Science*, vol. 13507, pp. 657–687. Springer Nature Switzerland (2022). https://doi.org/10.1007/978-3-031-15802-5_23
 27. Delpuch de Saint Guilhem, C., Orsini, E., Tanguy, T.: Limbo: Efficient Zero-knowledge MPCitH-based Arguments. In: *Proceedings of the 2021 ACM SIGSAC Conference on Computer and Communications Security – CCS 2021*. p. 3022–3036. Association for Computing Machinery (2021). <https://doi.org/10.1145/3460120.3484595>
 28. Wang, X., Ranellucci, S., Katz, J.: Authenticated Garbling and Efficient Maliciously Secure Two-Party Computation. In: *Proceedings of the 2017 ACM SIGSAC Conference on Computer and Communications Security – CCS 2017*. p. 21–37. Association for Computing Machinery (2017). <https://doi.org/10.1145/3133956.3134053>
 29. Weng, C., Yang, K., Katz, J., Wang, X.: Wolverine: Fast, Scalable, and Communication-Efficient Zero-Knowledge Proofs for Boolean and Arithmetic Circuits. In: *2021 IEEE Symposium on Security and Privacy – S&P 2021*. pp. 1074–1091. IEEE (2021). <https://doi.org/10.1109/SP40001.2021.00056>
 30. Weng, C., Yang, K., Xie, X., Katz, J., Wang, X.: Mystique: Efficient Conversions for Zero-Knowledge Proofs with Applications to Machine Learning. In: *30th USENIX Security Symposium – USENIX Security 2021*. USENIX Association (2021), <https://www.usenix.org/conference/usenixsecurity21/presentation/weng>
 31. Weng, C., Yang, K., Yang, Z., Xie, X., Wang, X.: AntMan: Interactive Zero-Knowledge Proofs with Sublinear Communication. In: *Proceedings of the 2022 ACM SIGSAC Conference on Computer and Communications Security – CCS 2022*. p. 2901–2914. Association for Computing Machinery (2022). <https://doi.org/10.1145/3548606.3560667>
 32. Yang, K., Sarkar, P., Weng, C., Wang, X.: QuickSilver: Efficient and Affordable Zero-Knowledge Proofs for Circuits and Polynomials over Any Field. In: *Proceedings of the 2021 ACM SIGSAC Conference on Computer and Communications Security – CCS 2021*. p. 2986–3001. Association for Computing Machinery (2021). <https://doi.org/10.1145/3460120.3484556>