```
summary.1
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    4    7    2   0000
 memory write::    3->M(   6)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    4    6    4   0000

summary.2
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    3   1111
  At end of instruction 2
    ac    x   sp   pc   cvzn
     5    5    6    4   0000

summary.3
-----------
 memory write:: FFFE->M(   5)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    5    1   0001
 memory write::    5->M(   5)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    5 FFFE    2   0001
  At end of instruction 3
    ac    x   sp   pc   cvzn
     3    5 FFFE    3   0000

summary.4
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
  FFFF    8    9    1   0001
  At end of instruction 2
    ac    x   sp   pc   cvzn
    FE    9    9    2   1000
 memory write::  1FD->M(   8)
  At end of instruction 3
    ac    x   sp   pc   cvzn
    FE    8    9    3   0000
 memory write::  1FE->M(   8)
  At end of instruction 4
    ac    x   sp   pc   cvzn
    FE    8    9    4   0000
 memory write::  1FF->M(   8)
  At end of instruction 5
```

```
    ac    x   sp   pc   cvzn
    FE    9    9    5   0000
 memory write::  200->M(   8)
  At end of instruction 6
    ac    x   sp   pc   cvzn
    FE    9    8    6   0000

summary.5
-----------
 memory write:: FFFE->M(   5)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    1   1001
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    2    7    2   0000
 memory write::    0->M(   3)
  At end of instruction 3
    ac    x   sp   pc   cvzn
     3    2    7    3   1010

summary.6
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    2   1111
 memory write:: FFFE->M(   5)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    6    6    3   0001

summary.7
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     6    5    6    2   0000

summary.8
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     5    5    6    2   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
     5    6    4    3   0000

summary.9
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    2   1101
 memory write::    4->M(   5)
```

```
  At end of instruction 2
    ac    x   sp   pc   cvzn
     2    5    6    3   1101

summary.A
-----------
 memory write::    0->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    2   1010
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    5    6    5   1010

summary.B
-----------
 memory write::    4->M(   6)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    2   0000

summary.C
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    4   0000

summary.D
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     0    3    6    2   0001
  At end of instruction 2
    ac    x   sp   pc   cvzn
     0    3    7    3   0000

summary.E
-----------
 memory write:: FFFD->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     7    5    7    1   1001

summary.F
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    8    2   0000

summary.G
-----------
  At end of instruction 1
```

```
    ac    x   sp   pc   cvzn
     1    5    6    1   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
     1    5    6    4   0000

summary.H
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    2    2   0000

summary.I
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     1    5    6    1   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
     1    5    6    3   0000
  At end of instruction 3
    ac    x   sp   pc   cvzn
     1    5    7    4   0000

summary.J
-----------
 memory write::    0->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    8    2   0010
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    5    8    5   0010

summary.K
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    2   1101
  At end of instruction 2
    ac    x   sp   pc   cvzn
     5    5    6    3   0000

summary.L
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     B FFF9    9    2   1000

summary.M
-----------
  At end of instruction 1
```

```
    ac    x   sp   pc   cvzn
  FFFF FFFF    6    1   0001
  At end of instruction 2
    ac    x   sp   pc   cvzn
  FFFF FFFF    6    4   0001

summary.N
-----------
 memory write::    2->M(   5)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    5    3   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    5    6    2   0000

summary.O
-----------
 memory write:: FFFF->M(   4)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     5    5    6    1   0001
 memory write:: FFFE->M(   5)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     5    5    6    2   0001
  At end of instruction 3
    ac    x   sp   pc   cvzn
     6    5    6    3   0000

summary.P
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     1    3    6    1   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
     1    3    6    4   0000

summary.Q
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    5    1   0000
 memory write::    1->M(   9)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    5    5    3   0000

summary.R
-----------
 memory write:: FFFF->M(   7)
```

```
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    7    1   1001
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    5    7    4   1001

summary.S
-----------
 memory write::    2->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     0    1    7    3   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
     0    8    7    4   0000

summary.T
-----------
 memory write::    2->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    7    3   0000
 memory write:: FFFB->M(   5)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    5    7    4   1001

summary.U
-----------
 memory write:: FFFC->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    7    1   0001
 memory write::    1->M(   5)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    6    7    3   1000

summary.V
-----------
 memory write::    0->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    3   0010

summary.W
-----------
 memory write:: A002->M(   5)
  At end of instruction 1
    ac    x   sp   pc   cvzn
     5    7    6    1   0001
```

```
 memory write:: 8000->M(   6)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     5    7    7    2   0101
  At end of instruction 3
    ac    x   sp   pc   cvzn
     6    7    7    3   0000
 memory write::    0->M(   7)
  At end of instruction 4
    ac    x   sp   pc   cvzn
     6    7    7    4   1010

summary.X
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    7    3   0000

summary.Y
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     5    1    7    0   1101
 memory write:: FFFE->M(   5)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     5    1    7    1   0001
  At end of instruction 3
    ac    x   sp   pc   cvzn
     5    1    7    3   0001

summary.Z
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    5    6    5   1101

summary.a
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    8    6    1   0000
 memory write::    6->M(   8)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    9    6    2   0000
 memory write::    0->M(   6)
  At end of instruction 3
    ac    x   sp   pc   cvzn
     4    9    6    3   1010

summary.b
```

```
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     4    0    6    1   1010
  At end of instruction 2
    ac    x   sp   pc   cvzn
     4    0    6    3   1010

summary.c
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     3    5    6    2   1000

summary.d
-----------
 memory write::    2->M(   7)
  At end of instruction 1
    ac    x   sp   pc   cvzn
  FFF9    5    7    5   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
  FFF9    5    8    2   0000

summary.e
-----------
  At end of instruction 1
    ac    x   sp   pc   cvzn
     7    5    8    2   1101
 memory write:: FFFC->M(   7)
  At end of instruction 2
    ac    x   sp   pc   cvzn
     7    5    8    3   0001
  At end of instruction 3
    ac    x   sp   pc   cvzn
     7    5    8    5   0001
 memory write:: FFFB->M(   8)
  At end of instruction 4
    ac    x   sp   pc   cvzn
     7    5    8    6   0001

summary.f
-----------
 memory write::    2->M(   C)
  At end of instruction 1
    ac    x   sp   pc   cvzn
  FFF9    4    C    9   0000
  At end of instruction 2
    ac    x   sp   pc   cvzn
    79    4    C    B   1000
  At end of instruction 3
    ac    x   sp   pc   cvzn
```

```
  79    4    D    2   1000
At end of instruction 4
  ac    x   sp   pc   cvzn
   0    4    D    4   0010
At end of instruction 5
  ac    x   sp   pc   cvzn
   0    4    D    7   0010
At end of instruction 6
  ac    x   sp   pc   cvzn
   0 FFF7    D    8   1001
```