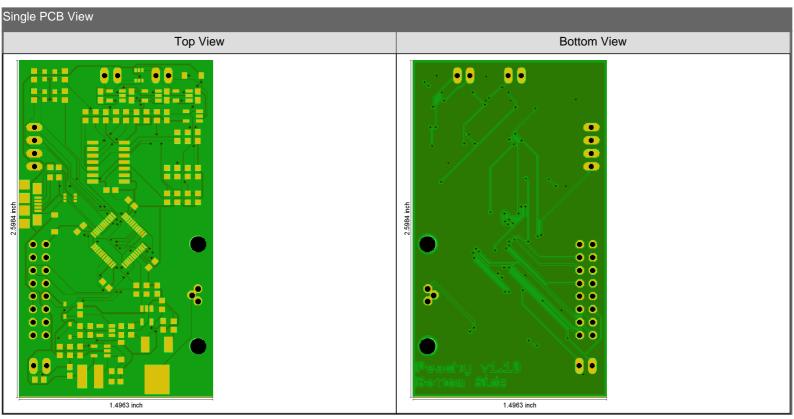


BayArea DFM REPORT | quotes@bacircuits.com or 855-811-1975

Name	yfxgag1g.zip	ld.	33290 - QED Check Todo's
Report Generated on	Apr 21, 2015 10:38:14 PM	Customer	via CRM
Board Id		Tool#	



Summary - General	
PCB Size	1.4963 inch x 2.5984 inch
PCB Thickness	62.00 mil
Customer Panel Size	
SMD Pads Top	222
SMD Pads Bottom	0
SMD Density Top	57 SMD/inch ²
SMD Density Bottom	0 SMD/inch ²
Number of Nets	77
Drill Hole Density	26 Holes/inch ²
Holes in SMD Pads	No
Edge Connectors	No

Copper Layers	2
Solder Mask	Both
Solder Mask Color	Green
Legend	None
Legend Color	
Paste	None
Peeloff Mask	None
Carbon Mask	None
Electrical Test	Single Sided
Max. Aspect Ratio on PTH	5.2
Vias Covered by Solder Mask	Both

<u> </u>											
Summary - Copper Layers											
Layer Type	Min. Line Width	Min. Ring	Min. Clr. to Copper	Min. Clr. to Plated Hole	Min. Clr. to NPTH	Min. Clr. to Outline					
	mil	mil	mil	mil	mil	mil					
Outer	6.00	6.00	9 2.49	¹⁰ 11.99	¹¹ 20.00	4.72					
Inner											

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Summary - Sequences													
Туре	Sequences Tools		Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Clr. Hole to Copper					
			mil	mil		mil	mil	mil					
Blind	0												
Buried	0												
PTH	1	3	12.00	43.00	99	6.00		11.99					
Plated (Total)	1	3	12.00	43.00	99	6.00		11.99					
NPTH	1	1	122.00	122.00	2			20.00					
Total	2	4	12.00	122.00	101	6.00		11.99					

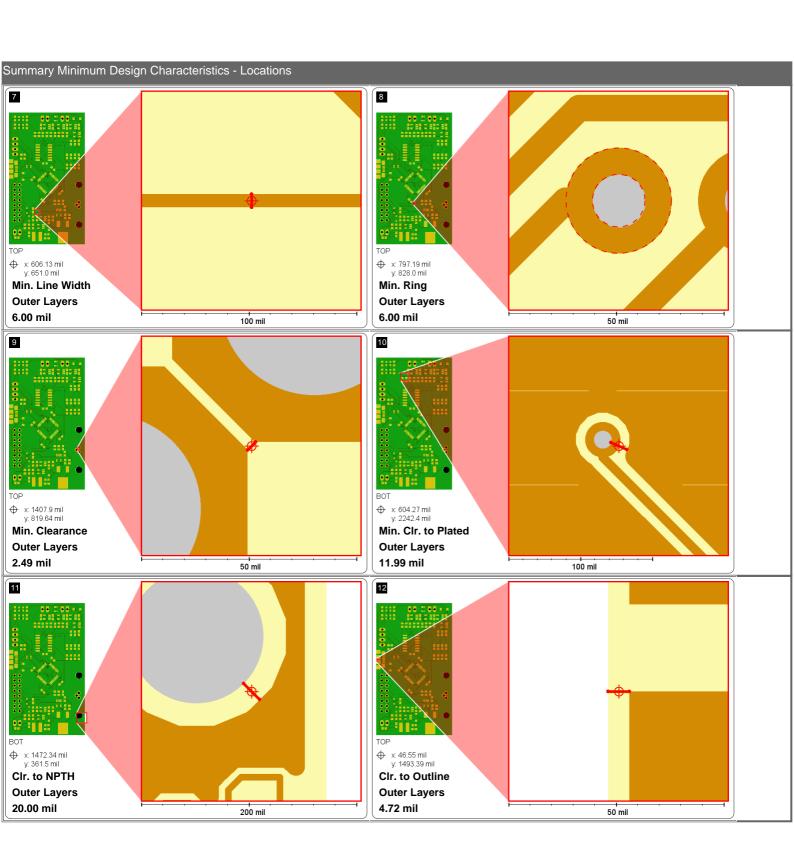






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Board Id		Tool#	



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Name	yfxgag1g.zip	ld.	33290 - QED Check Todo's
Report Generated on	Apr 21, 2015 10:38:15 PM	Customer	via CRM
Board Id		Tool#	

Stackup	
	ТОР
	вот

Copper Layers													
File	Pos.	Min. Line Width	Min. Ring	Min. Clr. to Copper	Min. Clr. Pad to Pad	Min. Clr. Pad to Track	Min. Clr. Track to Track	Min. Self- spacing	Min. Clr. to PTH	Min. Clr. to NPTH	Min. Clr. to Outline	Copper A	rea
		mil	mil	mil	mil	mil	mil	mil	mil	mil	mil	inch ²	%
TOP	1	6.00	6.00	2.49	2.49	6.00	6.00	0.06	12.00	21.03	4.72	0.9130	23
вот	2	6.00	6.00	2.49	2.49	5.99	5.99	5.72	11.99	20.00	20.00	3.3387	86

Drill Tools												
File	Tool Nr.	Span	Туре	Method	End Dia.	Holes (in PCB)	Moves (in PCB)	Double Hits (in File)	Predrill Hits (in File)	Min. Ring on Outer	Min. Ring on Inner	Min. Pad Size
					mil					mil	mil	mil
DRL	1	1-2	PTH	unknown	12.00	70	0	0	0	6.00		24.00
DRL	2	1-2	PTH	unknown	40.00	26	0	0	0	10.00		60.00
DRL	3	1-2	PTH	unknown	43.00	3	0	0	0	10.75		64.50
DRL	4	1-2	NPTH	unknown	122.00	2	0	0	0	> 32.00		

Sequence	s										
Span	Туре	Tools	Min. End Dia.	Max. End Dia.	Holes	Min. Ring on Outer	Min. Ring on Inner	Min. Ring on Outer NPTH	Min. Ring on Inner NPTH	Min. Clr. Hole to Copper	Min. Clr. Hole to Outline
			mil	mil		mil	mil	mil	mil	mil	mil
1-2	PTH	3	12.00	43.00	99	6.00				11.99	86.07
All	Plated	3	12.00	43.00	99	6.00				11.99	86.07
1-2	NPTH	1	122.00	122.00	2			> 32.00		20.00	57.22
All	All	4	12.00	122.00	101	6.00		> 32.00		11.99	57.22

Rout Tools						
File	Tool Nr.	Туре	Tool Dia.	End Dia.	Draw Length	Nibble Count
			mil	mil	mil	

Routed Holes						
File	Hole Nr.	Instances	X Size	Y Size	Draw Length	Nibble Count
			mil	mil	mil	

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Name	yfxgag1g.zip	ld.	33290 - QED Check Todo's
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Board Id		Tool#	

Solder Mask							
Side	Min. Positive Ring	Min. Negative Ring	Min. Clr. Mask to Mask	Min. Web	Min. Clr. Mask to Copper		
	mil	mil	mil	mil	mil		
Тор	2.00		3.87	3.87	3.21		
Bottom	2.00		> 10.00	> 10.00	4.00		

Files								
Initial	Renamed	Format	Function	Position	Color			
mini-v1.10-smash.GTS	TSM	ger274x	mask	top	green			
mini-v1.10-smash.GTL	TOP	ger274x	outer	1				
mini-v1.10-smash.GBL	ВОТ	ger274x	outer	2				
mini-v1.10-smash.GBS	BSM	ger274x	mask	bottom	green			
mini-v1.10-smash.TXT	DRL	excellon2	mixed	1-2				
mini-v1.10-smash.GML	DRAWING	ger274x	drawing	none				

Input Remarks

Gerber import: Zero size aperture 10. 'mini-v1.10-smash.GBL'

Gerber import: Zero size aperture 10. 'mini-v1.10-smash.GBS'

Gerber import: Zero size aperture 10. 'mini-v1.10-smash.GML'

Gerber import: Zero size aperture 10. 'mini-v1.10-smash.GTL'

Gerber import: Zero size aperture 10. 'mini-v1.10-smash.GTS'

Todo's

Please check the stackup

Stackup has no legends. Please check the drawings

Comments

Integr8tor v9.1.1-141209