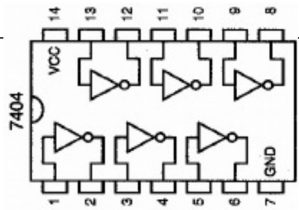
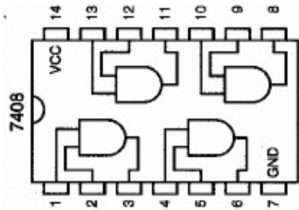
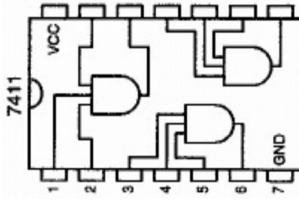
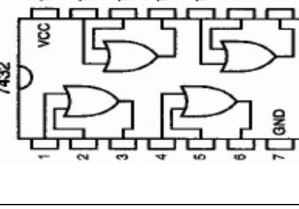
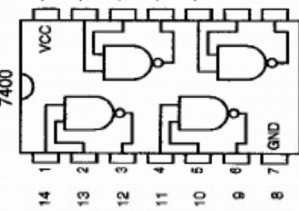
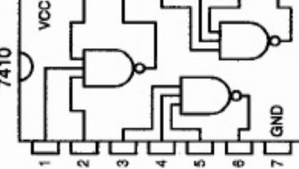
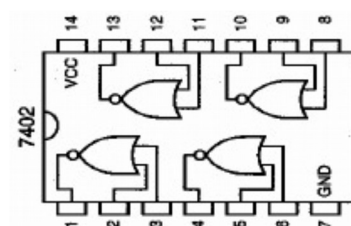
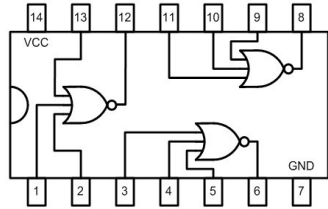
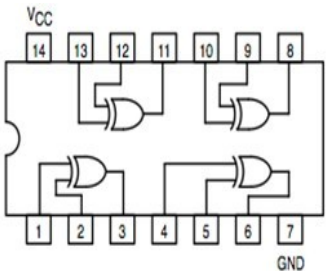
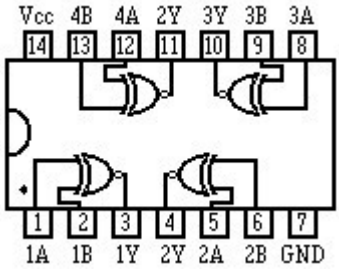


**REFERÊNCIA DOS CIRCUITOS INTEGRADOS (TTL)**

<b>Porta Lógica</b>	<b>Referência</b>	<b>Nº de Portas</b>	<b>Nº de Entradas</b>	<b>C.I</b>
<b>NOT</b>	<b>7404</b>	<b>6</b>	<b>1</b>	
<b>AND</b>	<b>7408</b>	<b>4</b>	<b>2</b>	
	<b>7411</b>	<b>3</b>	<b>3</b>	
<b>OR</b>	<b>7432</b>	<b>4</b>	<b>2</b>	
<b>NAND</b>	<b>7400</b>	<b>4</b>	<b>2</b>	
	<b>7410</b>	<b>3</b>	<b>3</b>	



<b>NOR</b>	<b>7402</b>	<b>4</b>	<b>2</b>	
<b>NOR</b>	<b>7427</b>	<b>3</b>	<b>3</b>	
<b>XOR</b>	<b>7486</b>	<b>4</b>	<b>2</b>	
<b>XNOR</b>	<b>74266</b>	<b>4</b>	<b>2</b>	

## **OUTROS CIRCUITOS INTEGRADOS**

### **AND :**

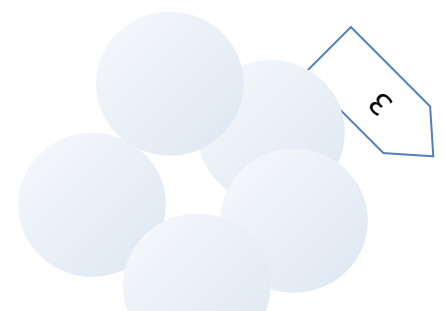
7421 - dupla, 4 entradas;

### **NAND:**

7420 - dupla, 4 entradas;

7430 - simples, 8 entradas;

74133- simples, 13 entradas.



**NOR:**

7425 – simples, 4 entradas.

**MUX:**

74150 – simples, MUX 16 → 1;

74151 – simples, MUX 8 → 1;

74153 – duplo, MUX 4 → 1;

**DMUX:**

74138 – duplo, DMUX 1 → 8;

74154 – simples DMUX 1 → 16;

**CONTADORES:**

7490 – Contador decádico;

72168 – Contador decádico, ascendente/ descendente,

**DESCODIFICADOR:**

7447 – Decodificador DCB – 7 segmentos.

