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November 2014

TIP31A / TIP31C NPN Epitaxial Silicon Transistor

Features

- · Medium Power Linear Switching Applications
- Complementary to TIP32 Series



1.Base 2.Collector 3.Emitter

Ordering Information

Part Number	Top Mark	Package	Packing Method
TIP31A	TIP31A	TO-220 3L (Single Gauge)	Bulk
TIP31C	TIP31C	TO-220 3L (Single Gauge)	Bulk
TIP31CTU	TIP31C	TO-220 3L (Single Gauge)	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value	Unit	
V _{CBO} Collec	Collector-Base Voltage	TIP31A	60	- V	
		TIP31C	100		
V	Collector-Emitter Voltage	TIP31A	60	V	
V _{CEO}		TIP31C	100		
V _{EBO}	Emitter-Base Voltage		5	V	
I _C	Collector Current (DC)		3	Α	
I _{CP}	Collector Current (Pulse)		5	Α	
I _B	Base Current		1	А	
TJ	Junction Temperature	150	°C		
T _{STG}	Storage Temperature Range	-65 to 150	°C		

Thermal Characteristics

Values are at $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter Value		Unit	
Pc	Collector Dissipation (T _A = 25°C)	2	W	
FC	Collector Dissipation (T _C = 25°C)	40	VV	

Electrical Characteristics

Values are at $T_C = 25$ °C unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage ⁽¹⁾	TIP31A	$I_C = 30 \text{ mA}, I_B = 0$	60		V
		TIP31C		100		
I _{CEO} C	Collector Cut-Off Current	TIP31A	$V_{CE} = 30 \text{ V}, I_{B} = 0$		0.3	mA
		TIP31C	$V_{CE} = 60 \text{ V}, I_{B} = 0$		0.3	
I _{CES}	Collector Cut-Off Current	TIP31A	$V_{CE} = 60 \text{ V}, V_{EB} = 0$		200	
		TIP31C V _{CE} = 100 \	V _{CE} = 100 V, V _{EB} = 0		200	μΑ
I _{EBO}	Emitter Cut-Off Current		$V_{EB} = 5 \text{ V}, I_{C} = 0$		1	mA
h _{FE} DC Current	DC Current Gain ⁽¹⁾		$V_{CE} = 4 \text{ V}, I_{C} = 1 \text{ A}$	25		
	Do Current Gain		$V_{CE} = 4 \text{ V}, I_{C} = 3 \text{ A}$	10	50	
V _{CE} (sat)	Collector-Emitter Saturation Voltage ⁽¹⁾		$I_C = 3 \text{ A}, I_B = 375 \text{ mA}$		1.2	V
V _{BE} (on)	Base-Emitter On Voltage ⁽¹⁾		$V_{CE} = 4 \text{ V}, I_{C} = 3 \text{ A}$		1.8	V
f _T	Current Gain Bandwidth Product		$V_{CE} = 10 \text{ V}, I_{C} = 500 \text{ mA},$ f = 1 MHz	3.0		MHz

Note:

1. Pulse test: pw \leq 300 μ s, duty cycle \leq 2%.

Typical Performance Characteristics

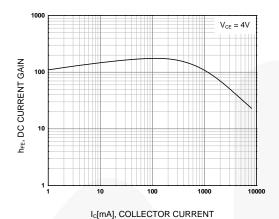


Figure 1. DC Current Gain

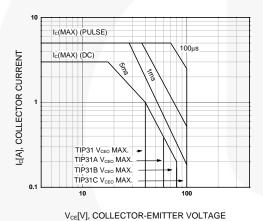


Figure 3. Safe Operating Area

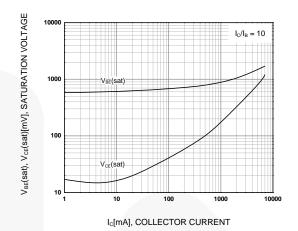


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

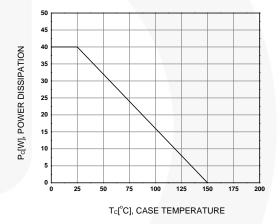
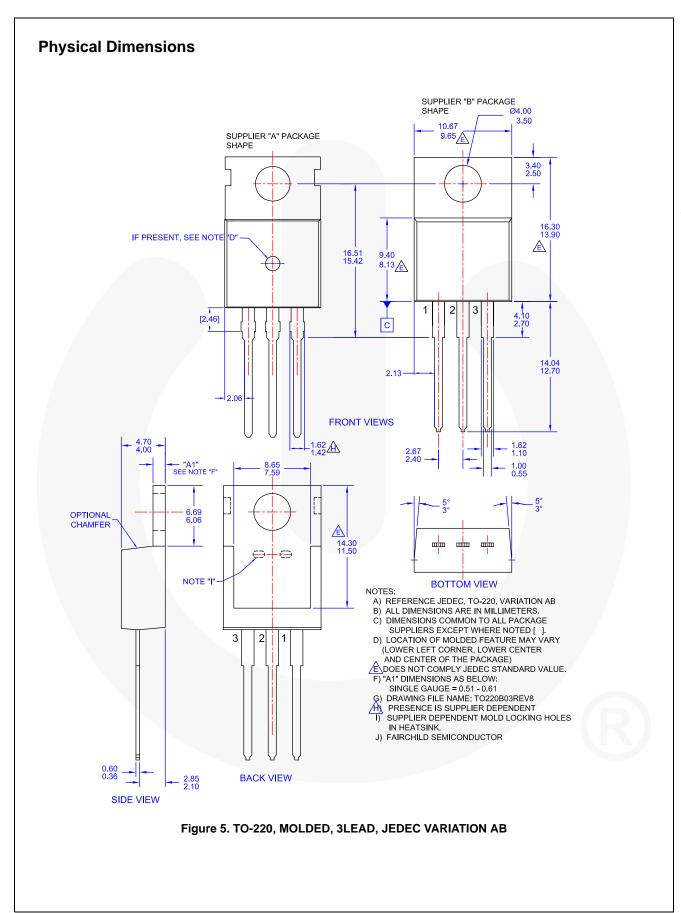


Figure 4. Power Derating







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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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Rev. 172

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