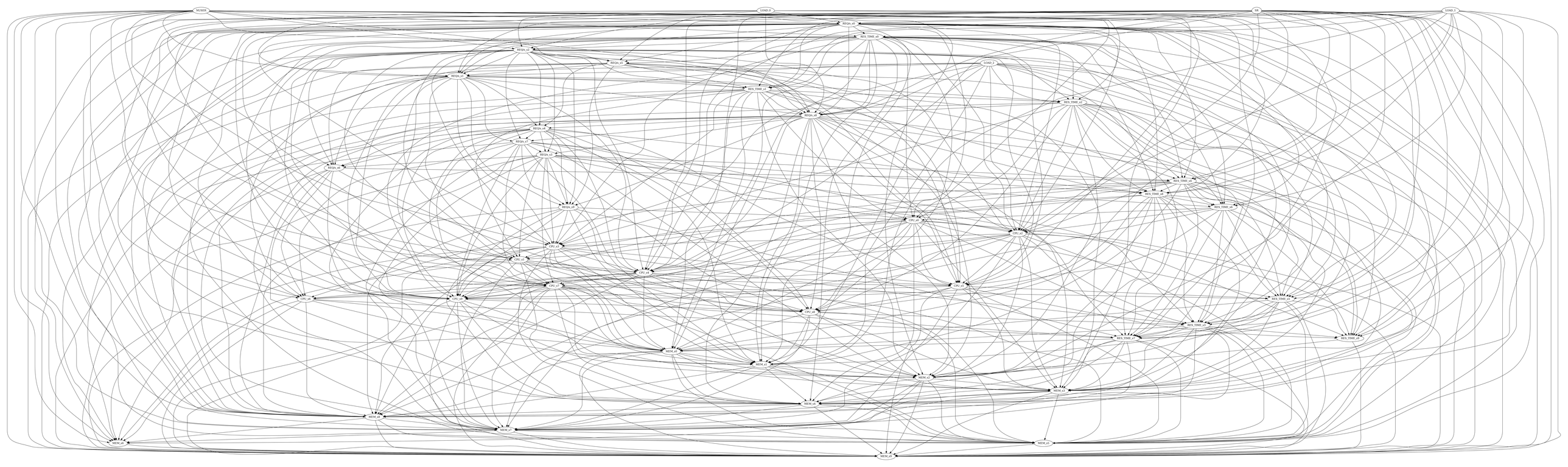
NUMEN
LOAD_0
SB
LOAD_3
REQ(s_0)
RES_TIME_s0
REQ(s_1)
REQ(s_5)
LOAD_2
REQ(s_2)
RES_TIME_s1
RES_TIME_s2
REQ(s_8)
REQ(s_6)
REQ(s_7)
REQ(s_3)
REQ(s_4)
REQ(s_9)
RES_TIME_s4
RES_TIME_s5
RES_TIME_s8
CPU_s0
CPU_s2
CPU_s5
CPU_s1
CPU_s4
CPU_s7
CPU_s6
CPU_s3
CPU_s9
RES_TIME_s3
RES_TIME_s7
RES_TIME_s9
MEM_s0
MEM_s1
MEM_s2
MEM_s3
MEM_s4
MEM_s5
MEM_s6
MEM_s7
MEM_s8
MEM_s9