

1. Autoend will automatically generate stop bits at the end of a transaction. We did not use them as we used chained writes and reads. Such as the restart that wouldn't work properly.
2. For 400 kHz PRESC = 0, SCLL 0x13, SCLH = 0x3, SDADEL = 0x1, SCLDEL = 0x3.
3. TC 6 , NACKF 4, TXIS 1, ARLO 9.
4. 250/500/2000 Dps.
5. We used a different board and as such the addresses were 0x68 and 0x69.