Class:	**CPE100L - 1002**		Semester:	**Spring 2020**
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Instructor's comments:				

Introduction / Theory of operation

Lab 6 is about multiplexers and demultiplexers, where we will design and implement 4-1 multiplexer and demultiplexer circuits on the DE0 board.

Prelab main content

1) 4 to 1 MUX schematics created in Quartus
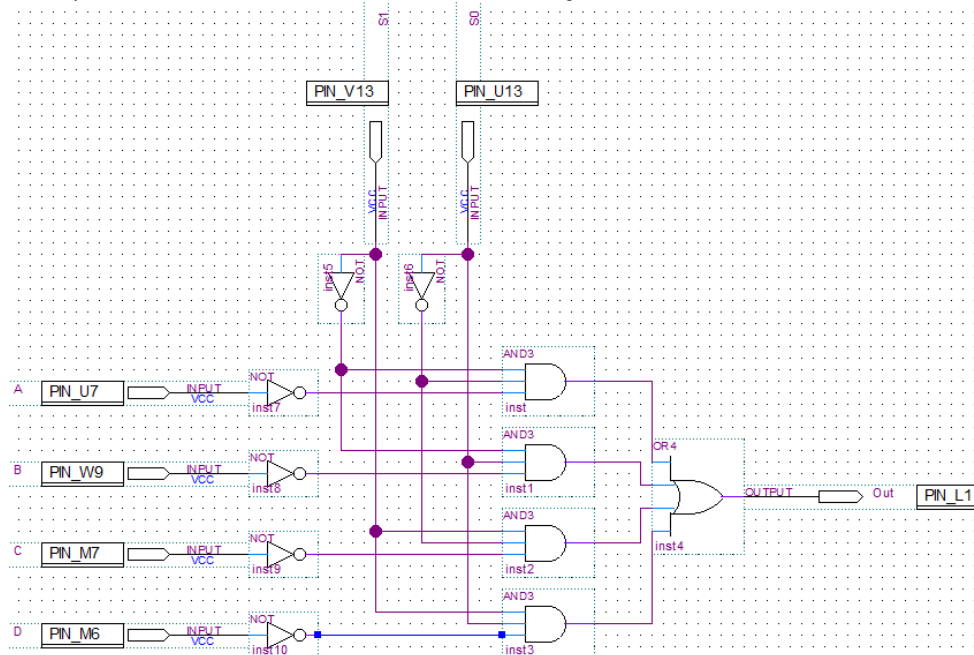
2) Simulation waveform generated by Quartus