Class:	**CPE 100L – 1002**		Semester:	**SPRING 2020**
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Instructor's comments:				

Introduction / Theory of operation

Lab 3 is an introduction to digital logic gates and simple Boolean implementation. This would allow us to understand the function of digital logic gates and implement simple combination circuits and gain experience of design overflow.

Prelab main content

1. XOR schematic created in Quartus
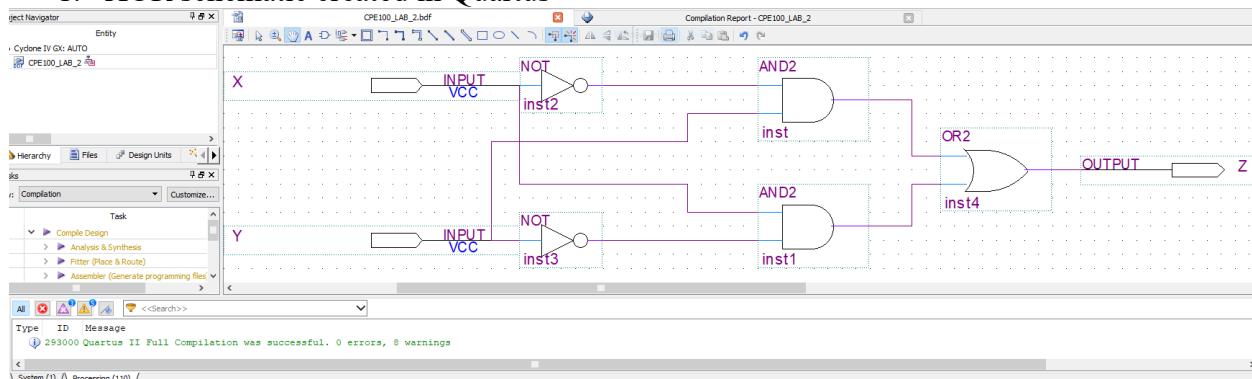
2. Simulation waveform generated by Quartus