UNIVERSITY OF NEVADA LAS VEGAS. DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING LABORATORIES.

Class:	CPE100L - 1002	Semester:	Spring 2020

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		Document topic:	Prelab 8

Instructor's comments:

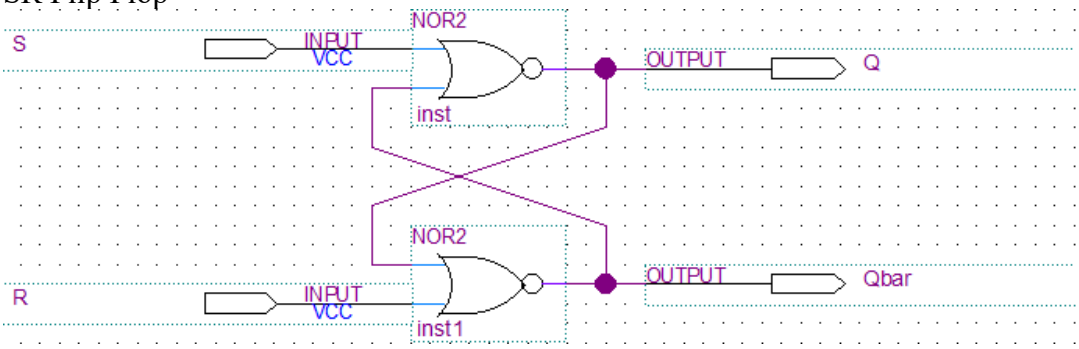
Introduction / Theory of operation

Lab 8 is about getting familiar with sequential elements, such as flip-flops, which state depends on input signals and previous flip-flop states.

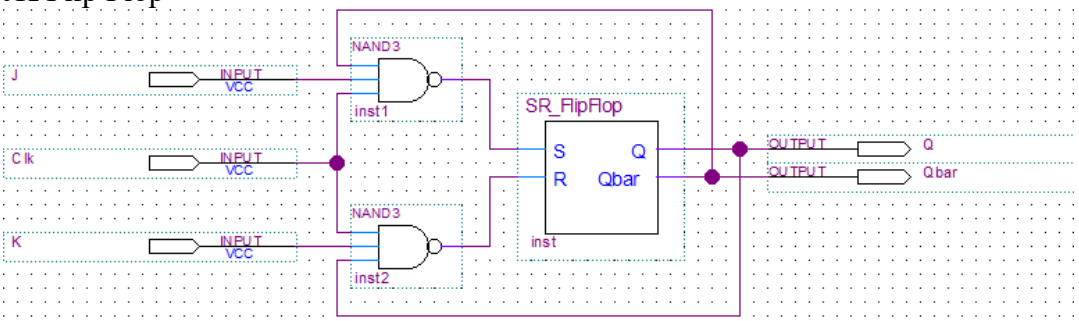
Prelab main content

1) Schematics created in Quartus

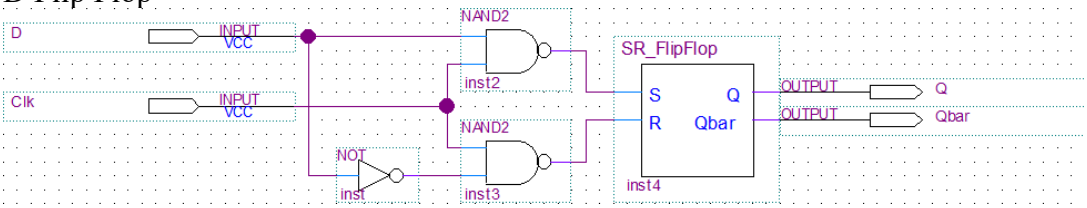
SR Flip Flop



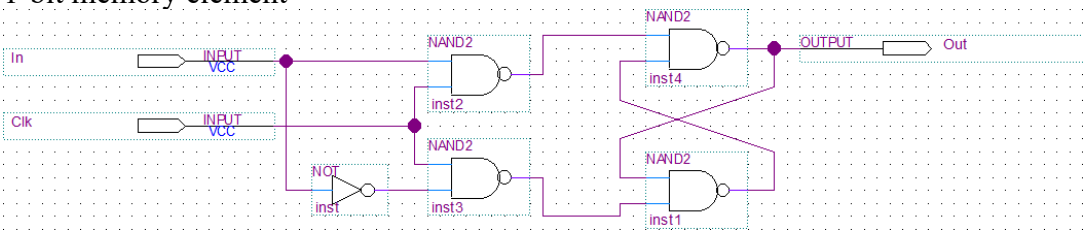
JK Flip Flop



D Flip Flop

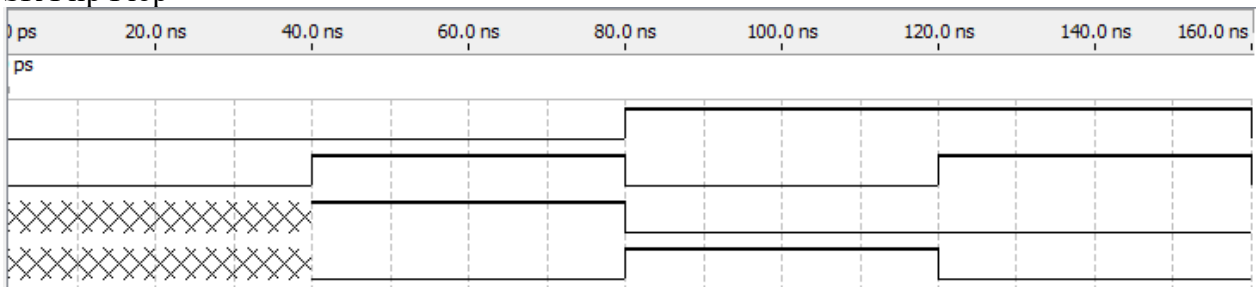


1-bit memory element

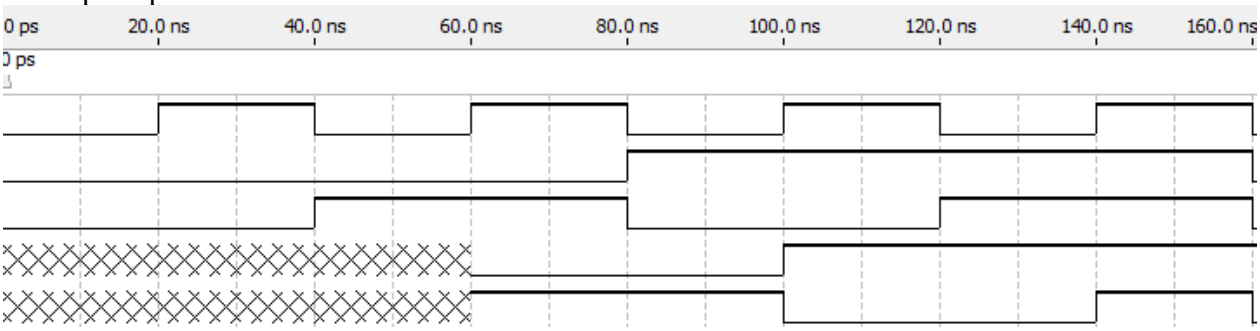


2) Simulation waveforms generated in Quartus

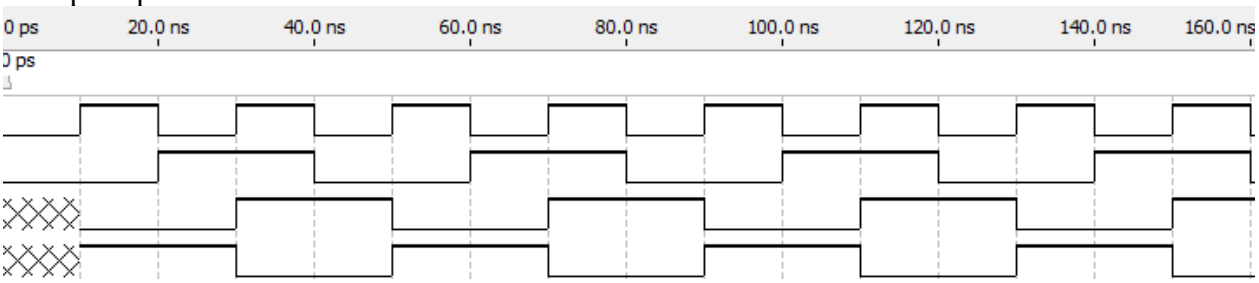
SR Flip Flop



JK Flip Flop



D Flip Flop



1-bit memory element