

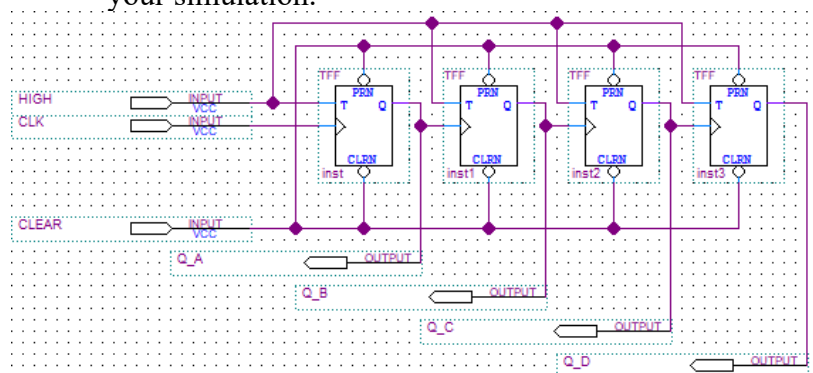
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Instructor's comments:			

## Introduction / Theory of operation

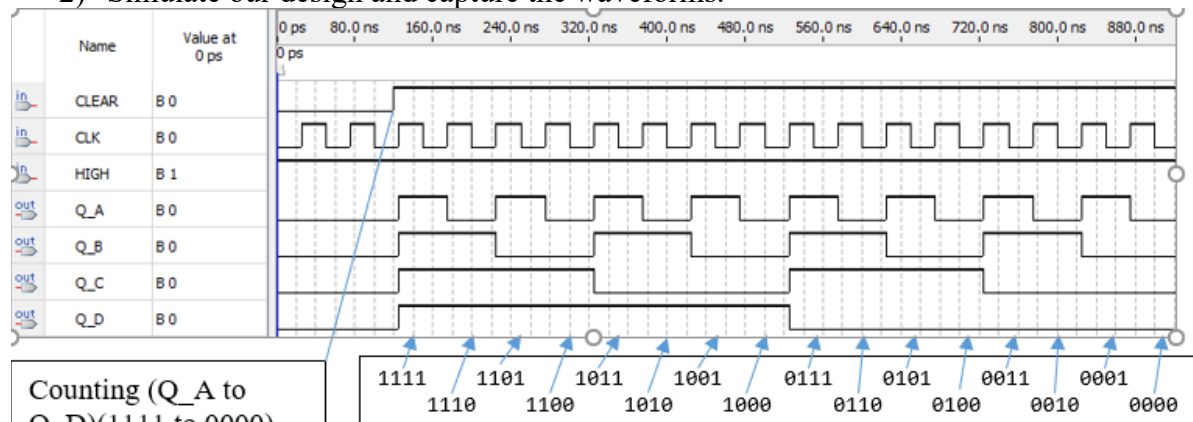
Lab 10 is about sequential circuit design, where we design a simple counter circuit and implement the circuit. In this lab, we will design a 4-bit sequential counter with preset and reset input.

## Prelab main content

- 1) Design a 4 bit binary (0-15) counter and simulate your system and capture waveforms of your simulation.



- 2) Simulate our design and capture the waveforms.



Counting (Q\_A to Q\_D)(1111 to 0000) begins when CLEAR input goes to 1 on the rising edge of CLK.