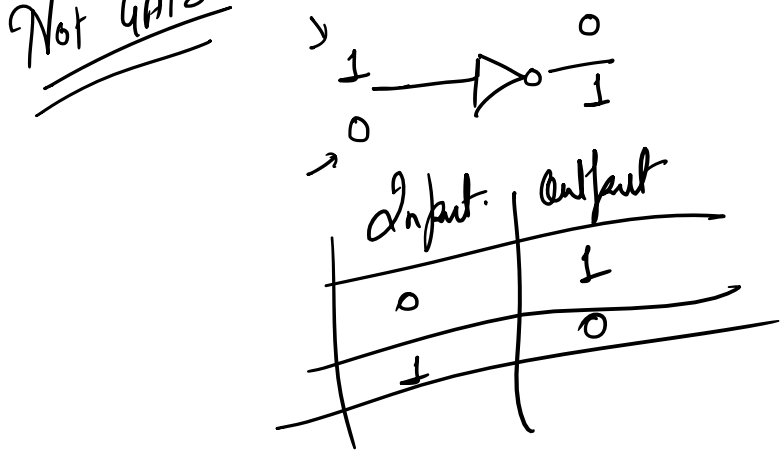
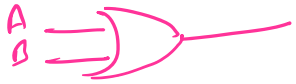
Not GATE



AND GATE

INPUT			OUTPUT
A	B	→	AB
0	0	→	0
0	1	→	0
1	0	→	0
1	1	→	1

OR GATE

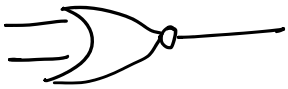


INPUT		OUTPUS
A	B	A+B
0	1	1
0	0	0
1	1	1
1	0	1

NAND → NOT + AND

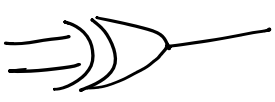
INPUT			OUTPUT
A	B	→	$\overline{AB}$
0	0	→	1
0	1	→	1
1	0	→	1
1	1	→	0

NOR → NOT + OR



INPUT		OUTPUT
A	B	$\overline{A+B}$
0	1	0
0	0	1
1	1	0
1	0	0

Ex-OR



Ex-NOR

INPUT		OUTPUT

INPUT		OUTPUT
A	B	A ⊕ B
0	0	0
0	1	1
1	0	1
1	1	0

INPUT		OUTPUT
A	B	$\overline{A \oplus B}$
0	0	1
0	1	0
1	1	1
1	0	0

Q = y

AND

$\overline{AB} + A$

OR

y