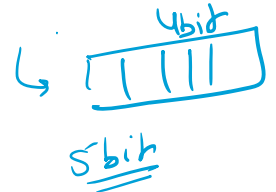


Overflow →
memory bit < data bit

→ 5 bit data (+) (-)
→ 5 bit data (+) (-)
Result 5 bit data
1 extra



Carry → overflow X

Carry ✓ overflow X
Carry X overflow X

Unsigned (+ve)
4 bit Range $0 - (2^n - 1)$
 $0 - 15$

$2^3 \ 2^2 \ 2^1 \ 2^0$
8 → 1 0 0 0
+ 9 → 1 0 0 1

1 0 0 0 1
↑ carry → overflow

Signed Numbers 2's

4 bit Range -2^{n-1} to $(2^{n-1} - 1)$
-8 to 7

$2^3 \ 2^2 \ 2^1 \ 2^0$
→ 0 1 0 1
2's → 1 0 1 1

Add
-5 → 1 1 0 1
+5 → 0 1 0 1

1 0 0 0 0
↑ carry → overflow X

Range 4 bit
(-8 to 7)

-6 → 1 0 1 0
-5 → 1 0 1 1

1 0 1 0 1
↑ ↑
(-11) overflow ✓

4 [] -5
4 [] +5
4 [0 0 0 0] 0
X
5 bit
0 1 0 0 0
↑
5 bit (+8)
4 bit

(-11) $\begin{array}{r} 10101 \\ \text{Carry} \end{array}$

X $\begin{array}{r} 6 \rightarrow 0110 \\ 2 \rightarrow 0010 \\ \hline 8 \rightarrow 1000 \end{array}$ overflow ✓

Carry X

4 bit $\begin{array}{|c|c|c|c|} \hline 1 & 0 & 0 & 0 \\ \hline \end{array}$
msb (-)ve (-8)

(-8 to 7)

$\begin{array}{r} + 5 \rightarrow 0101 \\ + 2 \rightarrow 0010 \\ \hline 7 \rightarrow 0111 \end{array}$
Carry X overflow X

input -ve Result +ve
-ve

input 1 > 0 +ve overflow
1

input +ve > Result (-ve)
+ve overflow

Unsigned
→ Carry ↔ overflow

Signed
↳ Two +ve Number is added and Result is (-ve) ↔ overflow
↳ Two -ve " " " " " (+ve) ↔ "

Q15

2's
 $\begin{array}{r} 11111111 \\ 1010101 \end{array}$
↳ $\begin{array}{r} 11111111 \\ 1011 \end{array}$ -5
msb

-5
4 bit 2's
 $\begin{array}{r} 2^3 2^2 2^1 2^0 \\ 1011 \end{array}$ weight
ms -8 + 2 + 1