

### BANK504

|                      |      |                 |                   |             |
|----------------------|------|-----------------|-------------------|-------------|
| PS_DDR_CK0_504       | W25  | PS_DDR4_CLK0_P  | >>PS_DDR4_CLK0_P  | 10,11,12,13 |
| PS_DDR_CK_N0_504     | W26  | PS_DDR4_CLK0_N  | >>PS_DDR4_CLK0_N  | 10,11,12,13 |
| PS_DDR_CKE0_504      | W28  | PS_DDR4_CKE0    | >>PS_DDR4_CKE0    | 10,11,12,13 |
| PS_DDR_CK1_504       | Y24  |                 |                   |             |
| PS_DDR_CK_N1_504     | Y25  |                 |                   |             |
| PS_DDR_CKE1_504      | Y27  |                 |                   |             |
| PS_DDR_A0_504        | W28  | PS_DDR4_A0      | >>PS_DDR4_A0      | 10,11,12,13 |
| PS_DDR_A1_504        | Y28  | PS_DDR4_A1      | >>PS_DDR4_A1      | 10,11,12,13 |
| PS_DDR_A2_504        | AB28 | PS_DDR4_A2      | >>PS_DDR4_A2      | 10,11,12,13 |
| PS_DDR_A3_504        | AA28 | PS_DDR4_A3      | >>PS_DDR4_A3      | 10,11,12,13 |
| PS_DDR_A4_504        | Y27  | PS_DDR4_A4      | >>PS_DDR4_A4      | 10,11,12,13 |
| PS_DDR_A5_504        | AA27 | PS_DDR4_A5      | >>PS_DDR4_A5      | 10,11,12,13 |
| PS_DDR_A6_504        | Y22  | PS_DDR4_A6      | >>PS_DDR4_A6      | 10,11,12,13 |
| PS_DDR_A7_504        | AA23 | PS_DDR4_A7      | >>PS_DDR4_A7      | 10,11,12,13 |
| PS_DDR_A8_504        | AA22 | PS_DDR4_A8      | >>PS_DDR4_A8      | 10,11,12,13 |
| PS_DDR_A9_504        | AB23 | PS_DDR4_A9      | >>PS_DDR4_A9      | 10,11,12,13 |
| PS_DDR_A10_504       | AA25 | PS_DDR4_A10     | >>PS_DDR4_A10     | 10,11,12,13 |
| PS_DDR_A11_504       | AA26 | PS_DDR4_A11     | >>PS_DDR4_A11     | 10,11,12,13 |
| PS_DDR_A12_504       | AB25 | PS_DDR4_A12     | >>PS_DDR4_A12     | 10,11,12,13 |
| PS_DDR_A13_504       | AB26 | PS_DDR4_A13     | >>PS_DDR4_A13     | 10,11,12,13 |
| PS_DDR_A14_504       | AB24 | PS_DDR4_WE_B    | >>PS_DDR4_WE_B    | 10,11,12,13 |
| PS_DDR_A15_504       | AC24 | PS_DDR4_CAS_B   | >>PS_DDR4_CAS_B   | 10,11,12,13 |
| PS_DDR_A16_504       | AC23 | PS_DDR4_RAS_B   | >>PS_DDR4_RAS_B   | 10,11,12,13 |
| PS_DDR_A17_504       | AC22 |                 |                   |             |
| PS_DDR_CS_N0_504     | W27  | PS_DDR4_CS0_B   | >>PS_DDR4_CS0_B   | 10,11,12,13 |
| PS_DDR_CS_N1_504     | W26  |                 |                   |             |
| PS_DDR_BA0_504       | X23  | PS_DDR4_BA0     | >>PS_DDR4_BA0     | 10,11,12,13 |
| PS_DDR_BA1_504       | W22  | PS_DDR4_BA1     | >>PS_DDR4_BA1     | 10,11,12,13 |
| PS_DDR_BG0_504       | W24  | PS_DDR4_BG0     | >>PS_DDR4_BG0     | 10,11,12,13 |
| PS_DDR_BG1_504       | W22  |                 |                   |             |
| PS_DDR_PARITY_504    | U24  | PS_DDR4_PARITY  | >>PS_DDR4_PARITY  | 10,11,12,13 |
| PS_DDR_RAM_RST_N_504 | U23  | PS_DDR4_RESET_B | >>PS_DDR4_RESET_B | 10,11,12,13 |
| PS_DDR_ACT_N_504     | Y23  | PS_DDR4_ACT_B   | >>PS_DDR4_ACT_B   | 10,11,12,13 |
| PS_DDR_ALERT_N_504   | U25  | PS_DDR4_ALERT_B | >>PS_DDR4_ALERT_B | 10,11,12,13 |
| PS_DDR_ZQ_504        | U24  | PS_DDR4_ZQ      | R42 240 1%        |             |
| PS_DDR_ODT0_504      | U28  | PS_DDR4_ODT0    | >>PS_DDR4_ODT0    | 10,11,12,13 |
| PS_DDR_ODT1_504      | U26  |                 |                   |             |

XCZU3EG-SFVC784-1-I

### BANK504

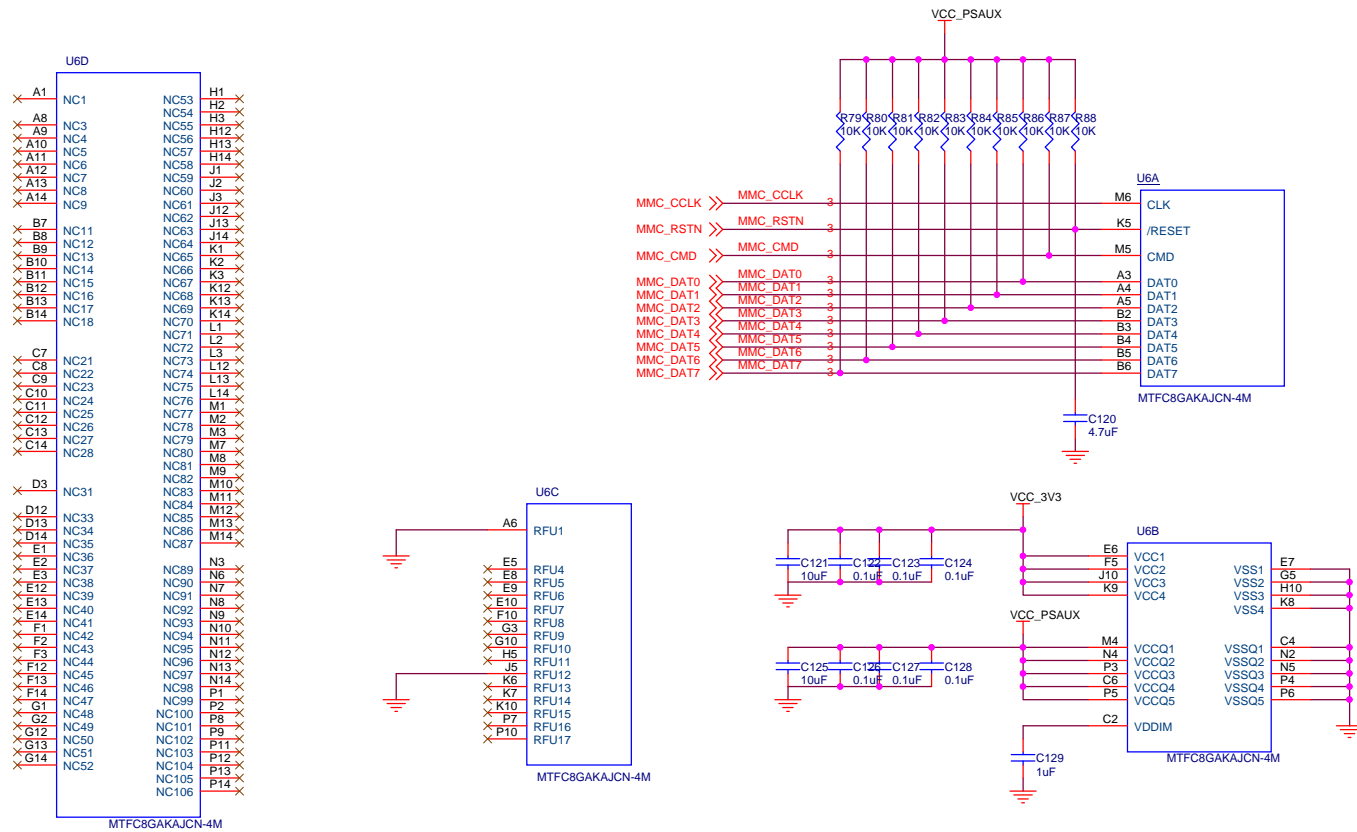
|                  |                 |      |                  |     |              |                |    |
|------------------|-----------------|------|------------------|-----|--------------|----------------|----|
| 10 PS_DDR4_DQ0   | <<PS_DDR4_DQ0   | AD21 | PS_DDR_DQ0_504   | T22 | PS_DDR4_DQ32 | >>PS_DDR4_DQ32 | 12 |
| 10 PS_DDR4_DQ1   | <<PS_DDR4_DQ1   | AE20 | PS_DDR_DQ1_504   | R22 | PS_DDR4_DQ33 | >>PS_DDR4_DQ33 | 12 |
| 10 PS_DDR4_DQ2   | <<PS_DDR4_DQ2   | AD20 | PS_DDR_DQ2_504   | P22 | PS_DDR4_DQ34 | >>PS_DDR4_DQ34 | 12 |
| 10 PS_DDR4_DQ3   | <<PS_DDR4_DQ3   | AF20 | PS_DDR_DQ3_504   | N22 | PS_DDR4_DQ35 | >>PS_DDR4_DQ35 | 12 |
| 10 PS_DDR4_DQ4   | <<PS_DDR4_DQ4   | AH21 | PS_DDR_DQ4_504   | T23 | PS_DDR4_DQ36 | >>PS_DDR4_DQ36 | 12 |
| 10 PS_DDR4_DQ5   | <<PS_DDR4_DQ5   | AH20 | PS_DDR_DQ5_504   | P24 | PS_DDR4_DQ37 | >>PS_DDR4_DQ37 | 12 |
| 10 PS_DDR4_DQ6   | <<PS_DDR4_DQ6   | AH19 | PS_DDR_DQ6_504   | R24 | PS_DDR4_DQ38 | >>PS_DDR4_DQ38 | 12 |
| 10 PS_DDR4_DQ7   | <<PS_DDR4_DQ7   | AG19 | PS_DDR_DQ7_504   | N24 | PS_DDR4_DQ39 | >>PS_DDR4_DQ39 | 12 |
| 10 PS_DDR4_DQ8   | <<PS_DDR4_DQ8   | AF22 | PS_DDR_DQ8_504   | H24 | PS_DDR4_DQ40 | >>PS_DDR4_DQ40 | 12 |
| 10 PS_DDR4_DQ9   | <<PS_DDR4_DQ9   | AH22 | PS_DDR_DQ9_504   | J24 | PS_DDR4_DQ41 | >>PS_DDR4_DQ41 | 12 |
| 10 PS_DDR4_DQ10  | <<PS_DDR4_DQ10  | AE22 | PS_DDR_DQ10_504  | M24 | PS_DDR4_DQ42 | >>PS_DDR4_DQ42 | 12 |
| 10 PS_DDR4_DQ11  | <<PS_DDR4_DQ11  | AD22 | PS_DDR_DQ11_504  | K24 | PS_DDR4_DQ43 | >>PS_DDR4_DQ43 | 12 |
| 10 PS_DDR4_DQ12  | <<PS_DDR4_DQ12  | AH23 | PS_DDR_DQ12_504  | J22 | PS_DDR4_DQ44 | >>PS_DDR4_DQ44 | 12 |
| 10 PS_DDR4_DQ13  | <<PS_DDR4_DQ13  | AH24 | PS_DDR_DQ13_504  | H22 | PS_DDR4_DQ45 | >>PS_DDR4_DQ45 | 12 |
| 10 PS_DDR4_DQ14  | <<PS_DDR4_DQ14  | AE24 | PS_DDR_DQ14_504  | K22 | PS_DDR4_DQ46 | >>PS_DDR4_DQ46 | 12 |
| 10 PS_DDR4_DQ15  | <<PS_DDR4_DQ15  | AG24 | PS_DDR_DQ15_504  | L22 | PS_DDR4_DQ47 | >>PS_DDR4_DQ47 | 12 |
| 11 PS_DDR4_DQ16  | <<PS_DDR4_DQ16  | AC26 | PS_DDR_DQ16_504  | M26 | PS_DDR4_DQ48 | >>PS_DDR4_DQ48 | 13 |
| 11 PS_DDR4_DQ17  | <<PS_DDR4_DQ17  | AD26 | PS_DDR_DQ17_504  | M28 | PS_DDR4_DQ49 | >>PS_DDR4_DQ49 | 13 |
| 11 PS_DDR4_DQ18  | <<PS_DDR4_DQ18  | AD25 | PS_DDR_DQ18_504  | L26 | PS_DDR4_DQ50 | >>PS_DDR4_DQ50 | 13 |
| 11 PS_DDR4_DQ19  | <<PS_DDR4_DQ19  | AD24 | PS_DDR_DQ19_504  | K28 | PS_DDR4_DQ51 | >>PS_DDR4_DQ51 | 13 |
| 11 PS_DDR4_DQ20  | <<PS_DDR4_DQ20  | AG26 | PS_DDR_DQ20_504  | L28 | PS_DDR4_DQ52 | >>PS_DDR4_DQ52 | 13 |
| 11 PS_DDR4_DQ21  | <<PS_DDR4_DQ21  | AH26 | PS_DDR_DQ21_504  | M28 | PS_DDR4_DQ53 | >>PS_DDR4_DQ53 | 13 |
| 11 PS_DDR4_DQ22  | <<PS_DDR4_DQ22  | AG25 | PS_DDR_DQ22_504  | N28 | PS_DDR4_DQ54 | >>PS_DDR4_DQ54 | 13 |
| 11 PS_DDR4_DQ23  | <<PS_DDR4_DQ23  | AH27 | PS_DDR_DQ23_504  | J28 | PS_DDR4_DQ55 | >>PS_DDR4_DQ55 | 13 |
| 11 PS_DDR4_DQ24  | <<PS_DDR4_DQ24  | AH28 | PS_DDR_DQ24_504  | K27 | PS_DDR4_DQ56 | >>PS_DDR4_DQ56 | 13 |
| 11 PS_DDR4_DQ25  | <<PS_DDR4_DQ25  | AF28 | PS_DDR_DQ25_504  | H28 | PS_DDR4_DQ57 | >>PS_DDR4_DQ57 | 13 |
| 11 PS_DDR4_DQ26  | <<PS_DDR4_DQ26  | AG28 | PS_DDR_DQ26_504  | H27 | PS_DDR4_DQ58 | >>PS_DDR4_DQ58 | 13 |
| 11 PS_DDR4_DQ27  | <<PS_DDR4_DQ27  | AC27 | PS_DDR_DQ27_504  | G26 | PS_DDR4_DQ59 | >>PS_DDR4_DQ59 | 13 |
| 11 PS_DDR4_DQ28  | <<PS_DDR4_DQ28  | AD27 | PS_DDR_DQ28_504  | G25 | PS_DDR4_DQ60 | >>PS_DDR4_DQ60 | 13 |
| 11 PS_DDR4_DQ29  | <<PS_DDR4_DQ29  | AD28 | PS_DDR_DQ29_504  | K25 | PS_DDR4_DQ61 | >>PS_DDR4_DQ61 | 13 |
| 11 PS_DDR4_DQ30  | <<PS_DDR4_DQ30  | AC28 | PS_DDR_DQ30_504  | J25 | PS_DDR4_DQ62 | >>PS_DDR4_DQ62 | 13 |
| 11 PS_DDR4_DQ31  | <<PS_DDR4_DQ31  | AC28 | PS_DDR_DQ31_504  | T28 | PS_DDR4_DQ63 | >>PS_DDR4_DQ63 | 13 |
| 10 PS_DDR4_DQ32  | <<PS_DDR4_DQ32  | AE20 | PS_DDR_DQ32_504  | P28 |              |                |    |
| 10 PS_DDR4_DQ33  | <<PS_DDR4_DQ33  | AD20 | PS_DDR_DQ33_504  | P27 |              |                |    |
| 10 PS_DDR4_DQ34  | <<PS_DDR4_DQ34  | AF20 | PS_DDR_DQ34_504  | P26 |              |                |    |
| 10 PS_DDR4_DQ35  | <<PS_DDR4_DQ35  | AH21 | PS_DDR_DQ35_504  | P25 |              |                |    |
| 10 PS_DDR4_DQ36  | <<PS_DDR4_DQ36  | AH20 | PS_DDR_DQ36_504  | P25 |              |                |    |
| 10 PS_DDR4_DQ37  | <<PS_DDR4_DQ37  | AH19 | PS_DDR_DQ37_504  | X25 |              |                |    |
| 10 PS_DDR4_DQ38  | <<PS_DDR4_DQ38  | AG19 | PS_DDR_DQ38_504  |     |              |                |    |
| 10 PS_DDR4_DQ39  | <<PS_DDR4_DQ39  | AF22 | PS_DDR_DQ39_504  |     |              |                |    |
| 10 PS_DDR4_DQ40  | <<PS_DDR4_DQ40  | AH22 | PS_DDR_DQ40_504  |     |              |                |    |
| 10 PS_DDR4_DQ41  | <<PS_DDR4_DQ41  | AE22 | PS_DDR_DQ41_504  |     |              |                |    |
| 10 PS_DDR4_DQ42  | <<PS_DDR4_DQ42  | AD22 | PS_DDR_DQ42_504  |     |              |                |    |
| 10 PS_DDR4_DQ43  | <<PS_DDR4_DQ43  | AH23 | PS_DDR_DQ43_504  |     |              |                |    |
| 10 PS_DDR4_DQ44  | <<PS_DDR4_DQ44  | AH24 | PS_DDR_DQ44_504  |     |              |                |    |
| 10 PS_DDR4_DQ45  | <<PS_DDR4_DQ45  | AE24 | PS_DDR_DQ45_504  |     |              |                |    |
| 10 PS_DDR4_DQ46  | <<PS_DDR4_DQ46  | AG24 | PS_DDR_DQ46_504  |     |              |                |    |
| 10 PS_DDR4_DQ47  | <<PS_DDR4_DQ47  | AC26 | PS_DDR_DQ47_504  |     |              |                |    |
| 10 PS_DDR4_DQ48  | <<PS_DDR4_DQ48  | AD26 | PS_DDR_DQ48_504  |     |              |                |    |
| 10 PS_DDR4_DQ49  | <<PS_DDR4_DQ49  | AD25 | PS_DDR_DQ49_504  |     |              |                |    |
| 10 PS_DDR4_DQ50  | <<PS_DDR4_DQ50  | AD24 | PS_DDR_DQ50_504  |     |              |                |    |
| 10 PS_DDR4_DQ51  | <<PS_DDR4_DQ51  | AG26 | PS_DDR_DQ51_504  |     |              |                |    |
| 10 PS_DDR4_DQ52  | <<PS_DDR4_DQ52  | AH26 | PS_DDR_DQ52_504  |     |              |                |    |
| 10 PS_DDR4_DQ53  | <<PS_DDR4_DQ53  | AG25 | PS_DDR_DQ53_504  |     |              |                |    |
| 10 PS_DDR4_DQ54  | <<PS_DDR4_DQ54  | AH27 | PS_DDR_DQ54_504  |     |              |                |    |
| 10 PS_DDR4_DQ55  | <<PS_DDR4_DQ55  | AH28 | PS_DDR_DQ55_504  |     |              |                |    |
| 10 PS_DDR4_DQ56  | <<PS_DDR4_DQ56  | AF28 | PS_DDR_DQ56_504  |     |              |                |    |
| 10 PS_DDR4_DQ57  | <<PS_DDR4_DQ57  | AG28 | PS_DDR_DQ57_504  |     |              |                |    |
| 10 PS_DDR4_DQ58  | <<PS_DDR4_DQ58  | AC27 | PS_DDR_DQ58_504  |     |              |                |    |
| 10 PS_DDR4_DQ59  | <<PS_DDR4_DQ59  | AD27 | PS_DDR_DQ59_504  |     |              |                |    |
| 10 PS_DDR4_DQ60  | <<PS_DDR4_DQ60  | AD28 | PS_DDR_DQ60_504  |     |              |                |    |
| 10 PS_DDR4_DQ61  | <<PS_DDR4_DQ61  | AC28 | PS_DDR_DQ61_504  |     |              |                |    |
| 10 PS_DDR4_DQ62  | <<PS_DDR4_DQ62  |      | PS_DDR_DQ62_504  |     |              |                |    |
| 10 PS_DDR4_DQ63  | <<PS_DDR4_DQ63  |      | PS_DDR_DQ63_504  |     |              |                |    |
| 10 PS_DDR4_DQ64  | <<PS_DDR4_DQ64  |      | PS_DDR_DQ64_504  |     |              |                |    |
| 10 PS_DDR4_DQ65  | <<PS_DDR4_DQ65  |      | PS_DDR_DQ65_504  |     |              |                |    |
| 10 PS_DDR4_DQ66  | <<PS_DDR4_DQ66  |      | PS_DDR_DQ66_504  |     |              |                |    |
| 10 PS_DDR4_DQ67  | <<PS_DDR4_DQ67  |      | PS_DDR_DQ67_504  |     |              |                |    |
| 10 PS_DDR4_DQ68  | <<PS_DDR4_DQ68  |      | PS_DDR_DQ68_504  |     |              |                |    |
| 10 PS_DDR4_DQ69  | <<PS_DDR4_DQ69  |      | PS_DDR_DQ69_504  |     |              |                |    |
| 10 PS_DDR4_DQ70  | <<PS_DDR4_DQ70  |      | PS_DDR_DQ70_504  |     |              |                |    |
| 10 PS_DDR4_DQ71  | <<PS_DDR4_DQ71  |      | PS_DDR_DQ71_504  |     |              |                |    |
| 10 PS_DDR4_DQ72  | <<PS_DDR4_DQ72  |      | PS_DDR_DQ72_504  |     |              |                |    |
| 10 PS_DDR4_DQ73  | <<PS_DDR4_DQ73  |      | PS_DDR_DQ73_504  |     |              |                |    |
| 10 PS_DDR4_DQ74  | <<PS_DDR4_DQ74  |      | PS_DDR_DQ74_504  |     |              |                |    |
| 10 PS_DDR4_DQ75  | <<PS_DDR4_DQ75  |      | PS_DDR_DQ75_504  |     |              |                |    |
| 10 PS_DDR4_DQ76  | <<PS_DDR4_DQ76  |      | PS_DDR_DQ76_504  |     |              |                |    |
| 10 PS_DDR4_DQ77  | <<PS_DDR4_DQ77  |      | PS_DDR_DQ77_504  |     |              |                |    |
| 10 PS_DDR4_DQ78  | <<PS_DDR4_DQ78  |      | PS_DDR_DQ78_504  |     |              |                |    |
| 10 PS_DDR4_DQ79  | <<PS_DDR4_DQ79  |      | PS_DDR_DQ79_504  |     |              |                |    |
| 10 PS_DDR4_DQ80  | <<PS_DDR4_DQ80  |      | PS_DDR_DQ80_504  |     |              |                |    |
| 10 PS_DDR4_DQ81  | <<PS_DDR4_DQ81  |      | PS_DDR_DQ81_504  |     |              |                |    |
| 10 PS_DDR4_DQ82  | <<PS_DDR4_DQ82  |      | PS_DDR_DQ82_504  |     |              |                |    |
| 10 PS_DDR4_DQ83  | <<PS_DDR4_DQ83  |      | PS_DDR_DQ83_504  |     |              |                |    |
| 10 PS_DDR4_DQ84  | <<PS_DDR4_DQ84  |      | PS_DDR_DQ84_504  |     |              |                |    |
| 10 PS_DDR4_DQ85  | <<PS_DDR4_DQ85  |      | PS_DDR_DQ85_504  |     |              |                |    |
| 10 PS_DDR4_DQ86  | <<PS_DDR4_DQ86  |      | PS_DDR_DQ86_504  |     |              |                |    |
| 10 PS_DDR4_DQ87  | <<PS_DDR4_DQ87  |      | PS_DDR_DQ87_504  |     |              |                |    |
| 10 PS_DDR4_DQ88  | <<PS_DDR4_DQ88  |      | PS_DDR_DQ88_504  |     |              |                |    |
| 10 PS_DDR4_DQ89  | <<PS_DDR4_DQ89  |      | PS_DDR_DQ89_504  |     |              |                |    |
| 10 PS_DDR4_DQ90  | <<PS_DDR4_DQ90  |      | PS_DDR_DQ90_504  |     |              |                |    |
| 10 PS_DDR4_DQ91  | <<PS_DDR4_DQ91  |      | PS_DDR_DQ91_504  |     |              |                |    |
| 10 PS_DDR4_DQ92  | <<PS_DDR4_DQ92  |      | PS_DDR_DQ92_504  |     |              |                |    |
| 10 PS_DDR4_DQ93  | <<PS_DDR4_DQ93  |      | PS_DDR_DQ93_504  |     |              |                |    |
| 10 PS_DDR4_DQ94  | <<PS_DDR4_DQ94  |      | PS_DDR_DQ94_504  |     |              |                |    |
| 10 PS_DDR4_DQ95  | <<PS_DDR4_DQ95  |      | PS_DDR_DQ95_504  |     |              |                |    |
| 10 PS_DDR4_DQ96  | <<PS_DDR4_DQ96  |      | PS_DDR_DQ96_504  |     |              |                |    |
| 10 PS_DDR4_DQ97  | <<PS_DDR4_DQ97  |      | PS_DDR_DQ97_504  |     |              |                |    |
| 10 PS_DDR4_DQ98  | <<PS_DDR4_DQ98  |      | PS_DDR_DQ98_504  |     |              |                |    |
| 10 PS_DDR4_DQ99  | <<PS_DDR4_DQ99  |      | PS_DDR_DQ99_504  |     |              |                |    |
| 10 PS_DDR4_DQ100 | <<PS_DDR4_DQ100 |      | PS_DDR_DQ100_504 |     |              |                |    |
| 10 PS_DDR4_DQ101 | <<PS_DDR4_DQ101 |      | PS_DDR_DQ101_504 |     |              |                |    |
| 10 PS_DDR4_DQ102 | <<PS_DDR4_DQ102 |      | PS_DDR_DQ102_504 |     |              |                |    |
| 10 PS_DDR4_DQ103 | <<PS_DDR4_DQ103 |      | PS_DDR_DQ103_504 |     |              |                |    |
| 10 PS_DDR4_DQ104 | <<PS_DDR4_DQ104 |      | PS_DDR_DQ104_504 |     |              |                |    |
| 10 PS_DDR4_DQ105 | <<PS_DDR4_DQ105 |      | PS_DDR_DQ105_504 |     |              |                |    |
| 10 PS_DDR4_DQ106 | <<PS_DDR4_DQ106 |      | PS_DDR_DQ106_504 |     |              |                |    |
| 10 PS_DDR4_DQ107 | <<PS_DDR4_DQ107 |      | PS_DDR_DQ107_504 |     |              |                |    |
| 10 PS_DDR4_DQ108 | <<PS_DDR4_DQ108 |      | PS_DDR_DQ108_504 |     |              |                |    |
| 10 PS_DDR4_DQ109 | <<PS_DDR4_DQ109 |      | PS_DDR_DQ109_504 |     |              |                |    |
| 10 PS_DDR4_DQ110 | <<PS_DDR4_DQ110 |      | PS_DDR_DQ110_504 |     |              |                |    |
| 10 PS_DDR4_DQ111 | <<PS_DDR4_DQ111 |      | PS_DDR_DQ111_504 |     |              |                |    |
| 10 PS_DDR4_DQ112 | <<PS_DDR4_DQ112 |      | PS_DDR_DQ112_504 |     |              |                |    |
| 10 PS_DDR4_DQ113 | <<PS_DDR4_DQ113 |      | PS_DDR_DQ113_504 |     |              |                |    |
| 10 PS_DDR4_DQ114 | <<PS_DDR4_DQ114 |      | PS_DDR_DQ114_504 |     |              |                |    |
| 10 PS_DDR4_DQ115 | <<PS_DDR4_DQ115 |      | PS_DDR_DQ115_504 |     |              |                |    |
| 10 PS_DDR4_DQ116 | <<PS_DDR4_DQ116 |      | PS_DDR_DQ116_504 |     |              |                |    |
| 10 PS_DDR4_DQ117 | <<PS_DDR4_DQ117 |      | PS_DDR_DQ117_504 |     |              |                |    |
| 10 PS_DDR4_DQ118 | <<PS_DDR4_DQ118 |      | PS_DDR_DQ118_504 |     |              |                |    |
| 10 PS_DDR4_DQ119 | <<PS_DDR4_DQ119 |      | PS_DDR_DQ119_504 |     |              |                |    |
| 10 PS_DDR4_DQ120 | <<PS_DDR4_DQ120 |      | PS_DDR_DQ120_504 |     |              |                |    |
| 10 PS_DDR4_DQ121 | <<PS_DDR4_DQ121 |      | PS_DDR_DQ121_504 |     |              |                |    |
| 10 PS_DDR4_DQ122 | <<PS_DDR4_DQ122 |      | PS_DDR_DQ122_504 |     |              |                |    |
| 10 PS_DDR4_DQ123 | <<PS_DDR4_DQ123 |      | PS_DDR_DQ123_504 |     |              |                |    |

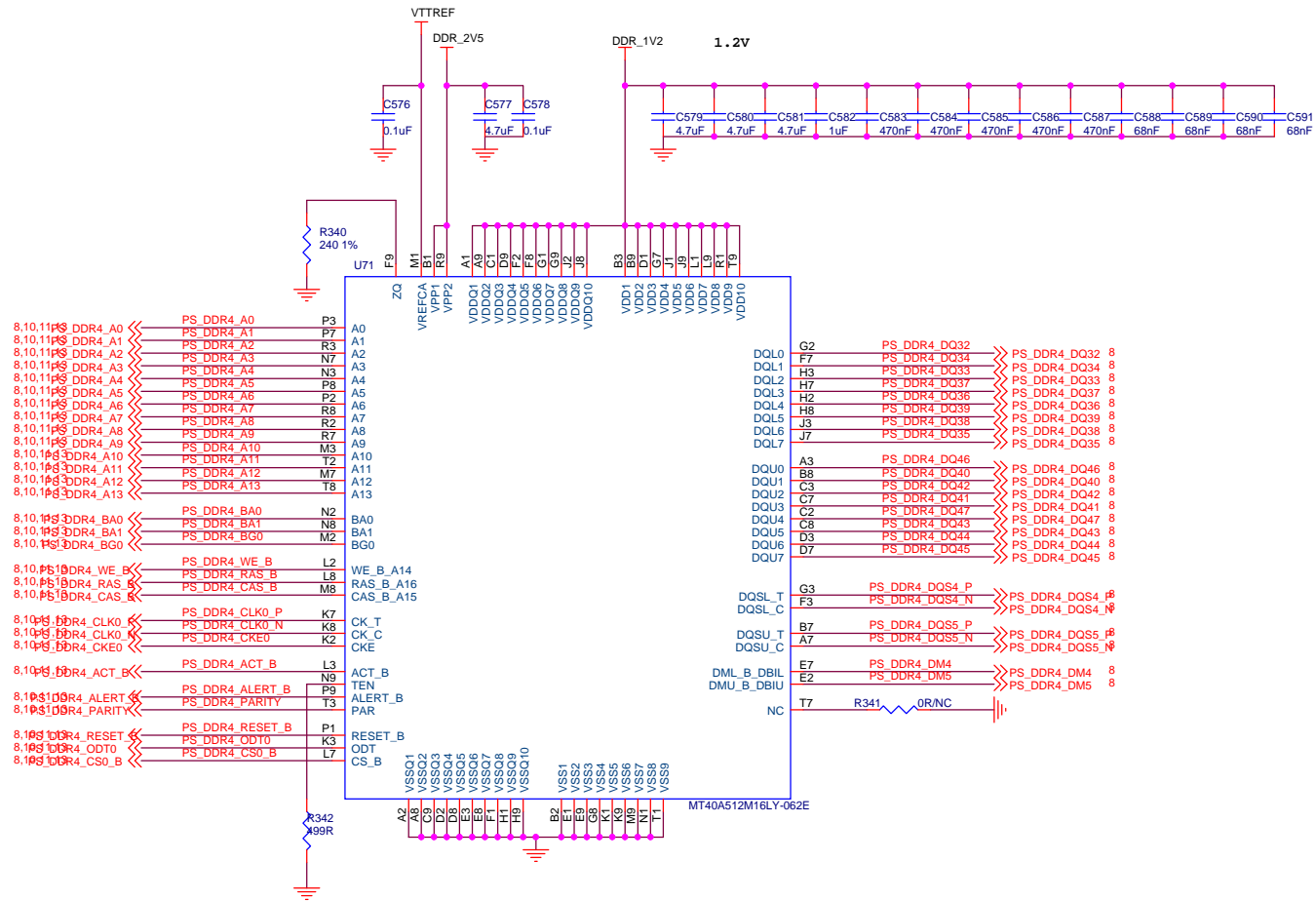












Power Sequence: 3.3V ----> 1.2V

1.2V

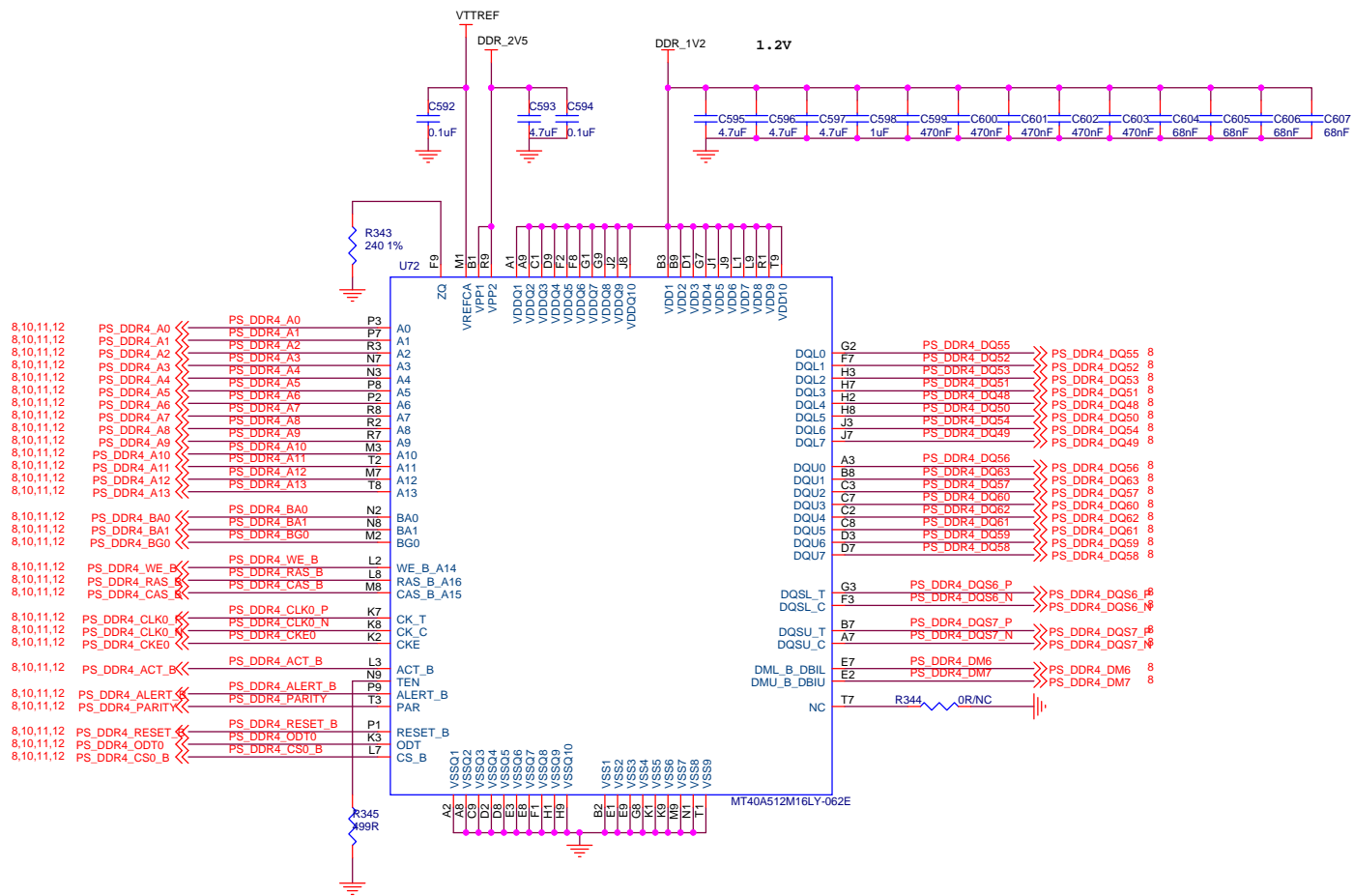
Ethernet Port

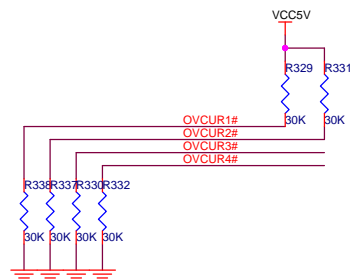
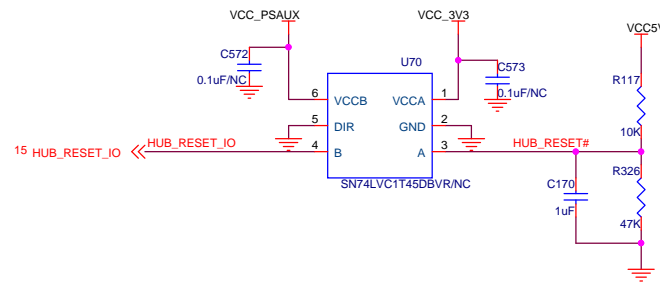
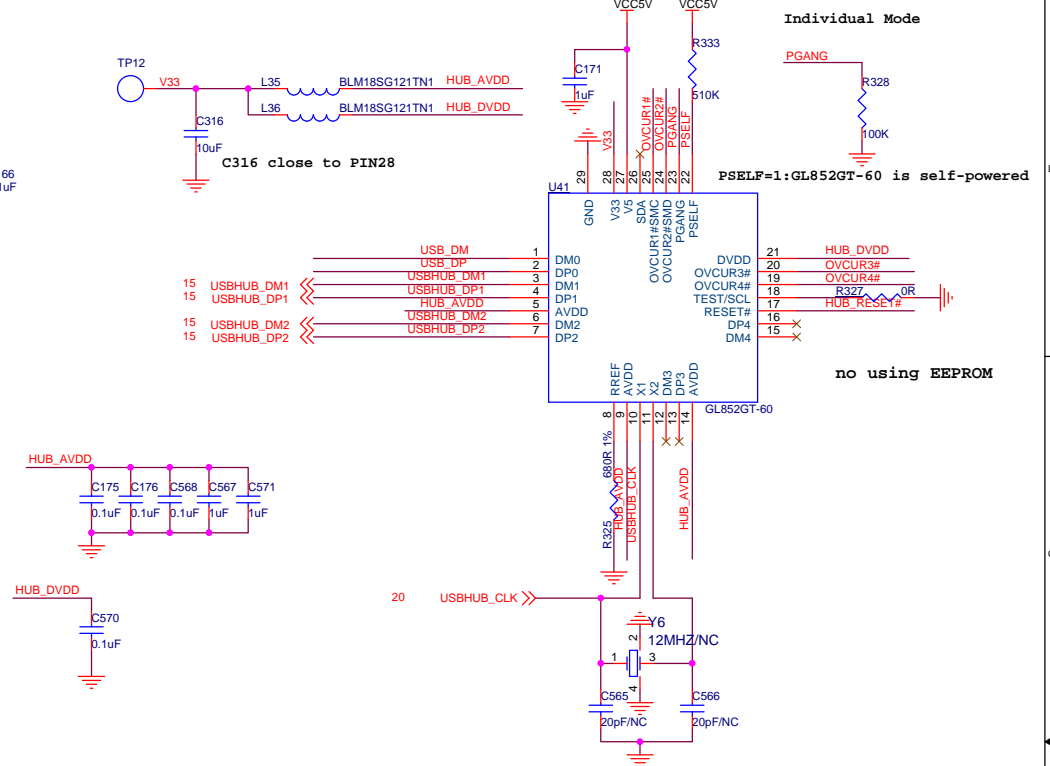
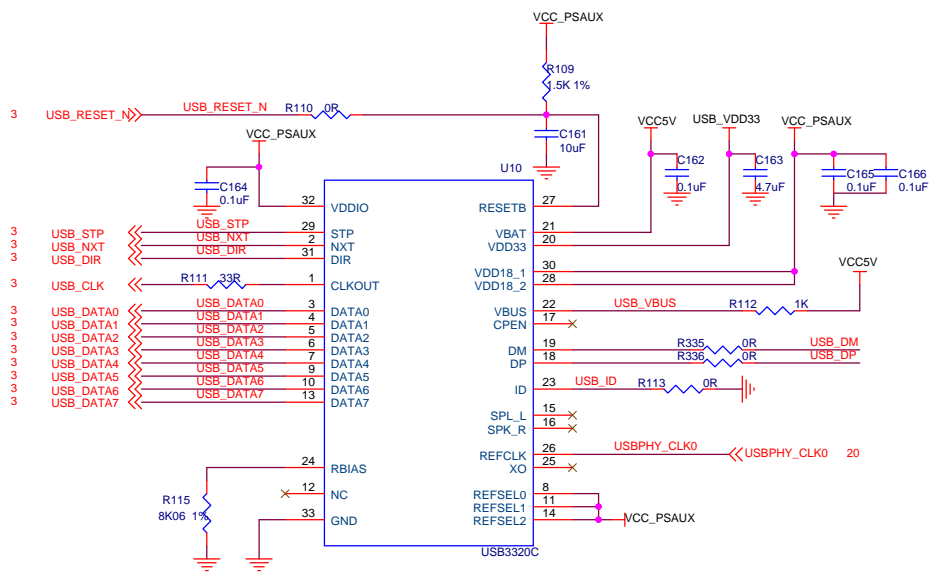
KSZ9031RXN  
48-pin QFN

Paddle Ground  
(bottom of chip)

KSZ9031RXN

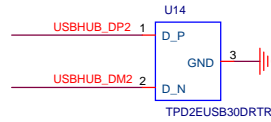
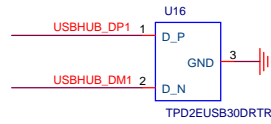
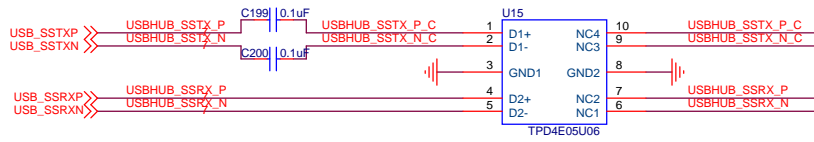
Enable 125Mhz Clock out    Single-LED mode    RGMII mode - Advertise all capabilities    PHY Address is 001



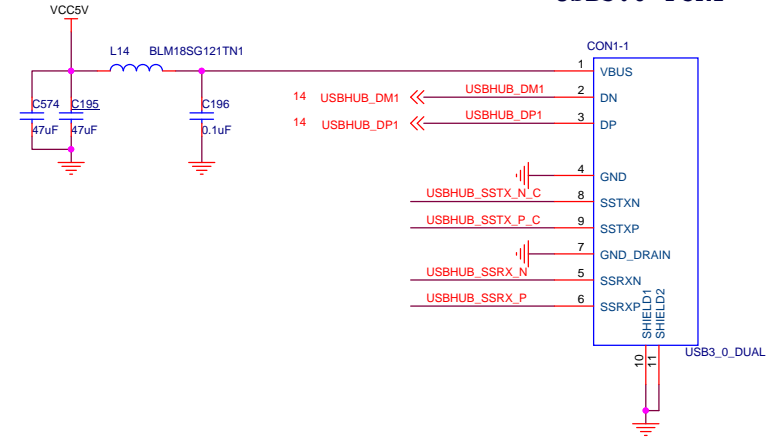




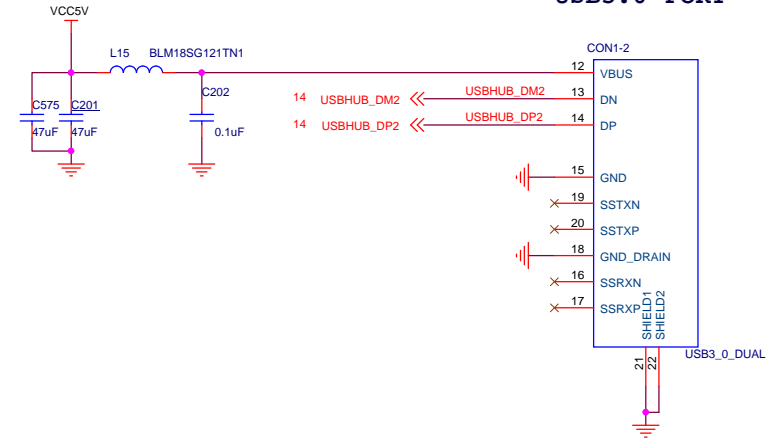
# AC Couple For TX

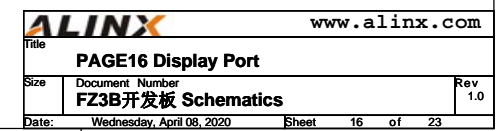
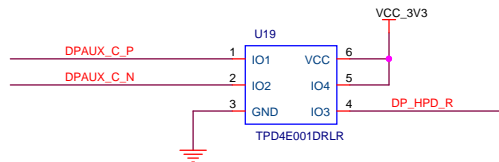
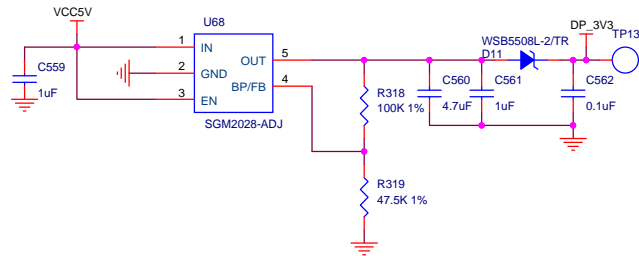


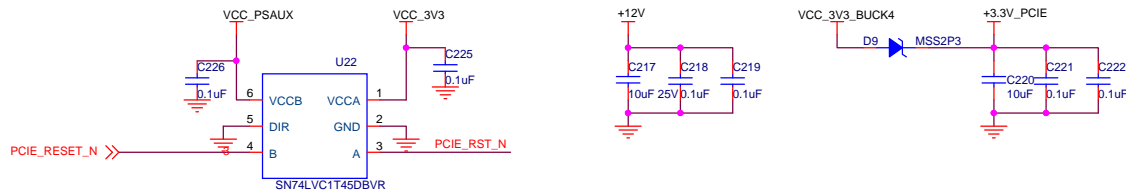
# USB3.0 PORT



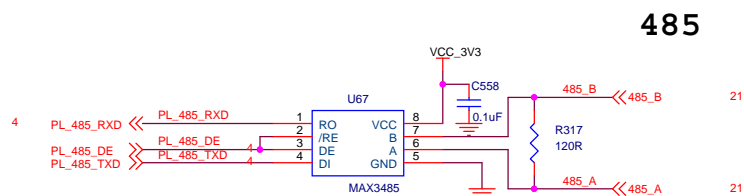
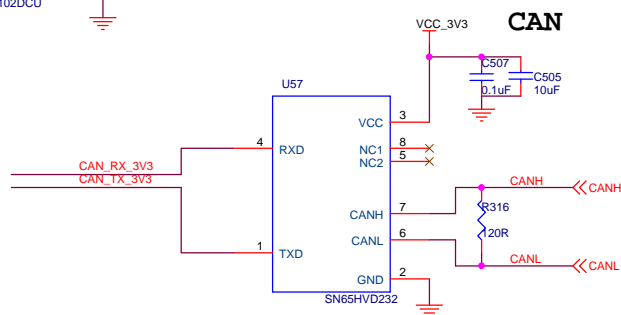
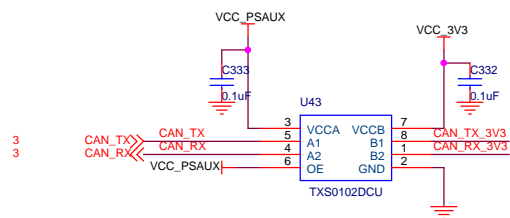
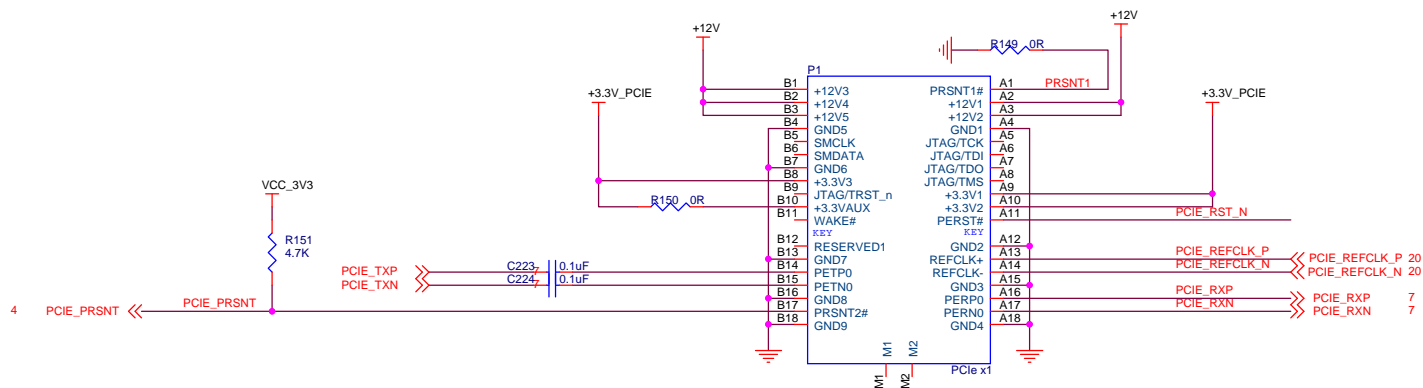
# USB3.0 PORT







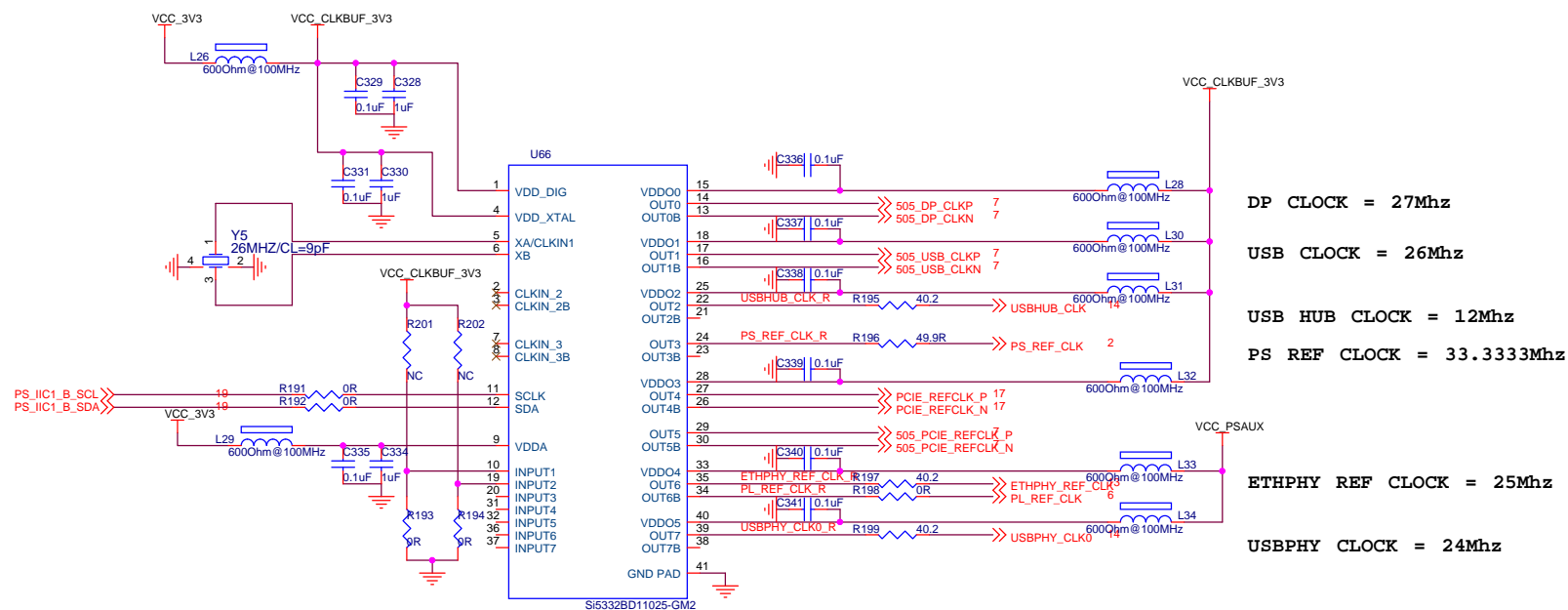
## PCIE X1 Socket



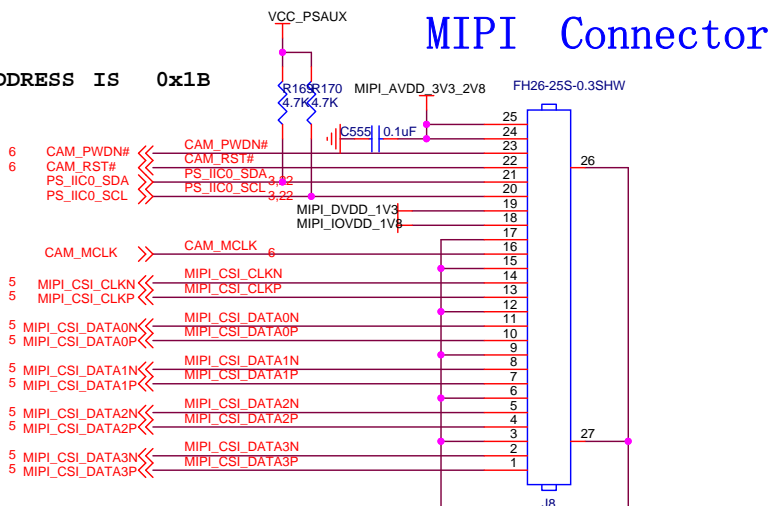
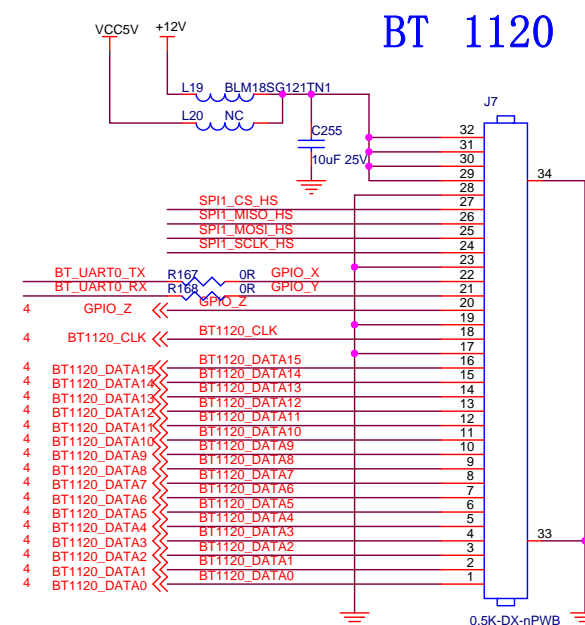
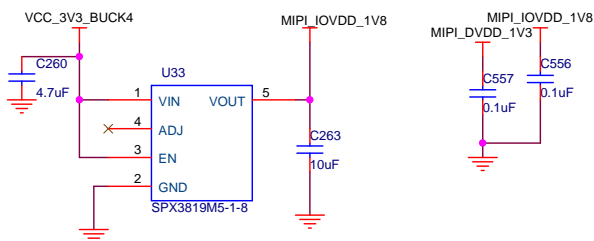
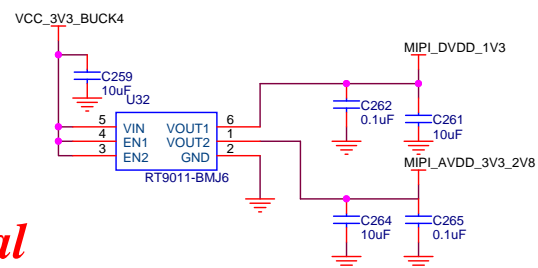
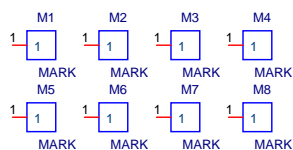
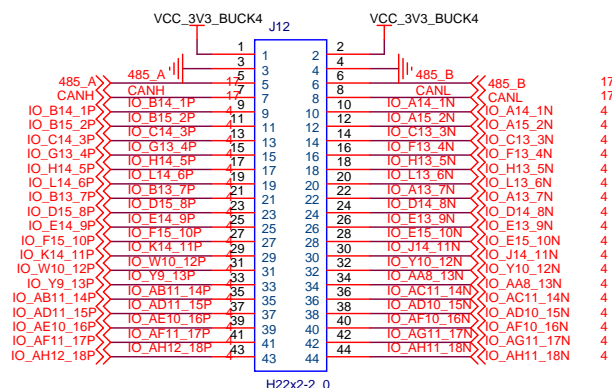
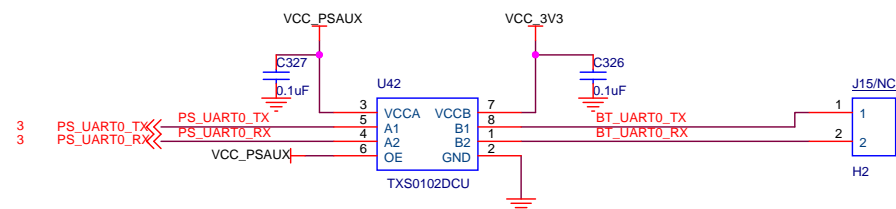
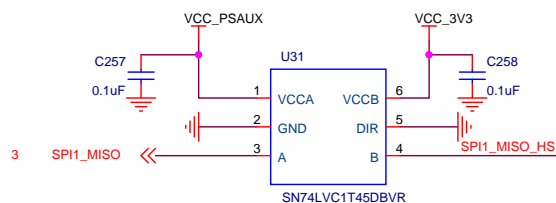
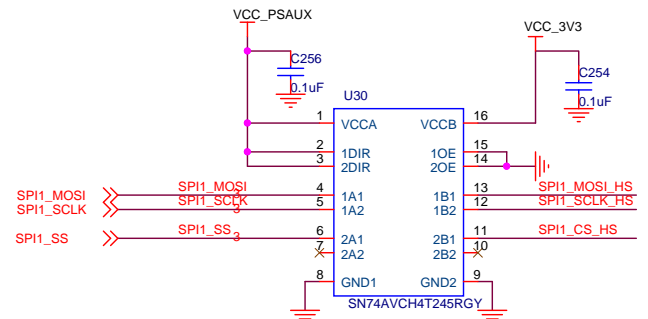




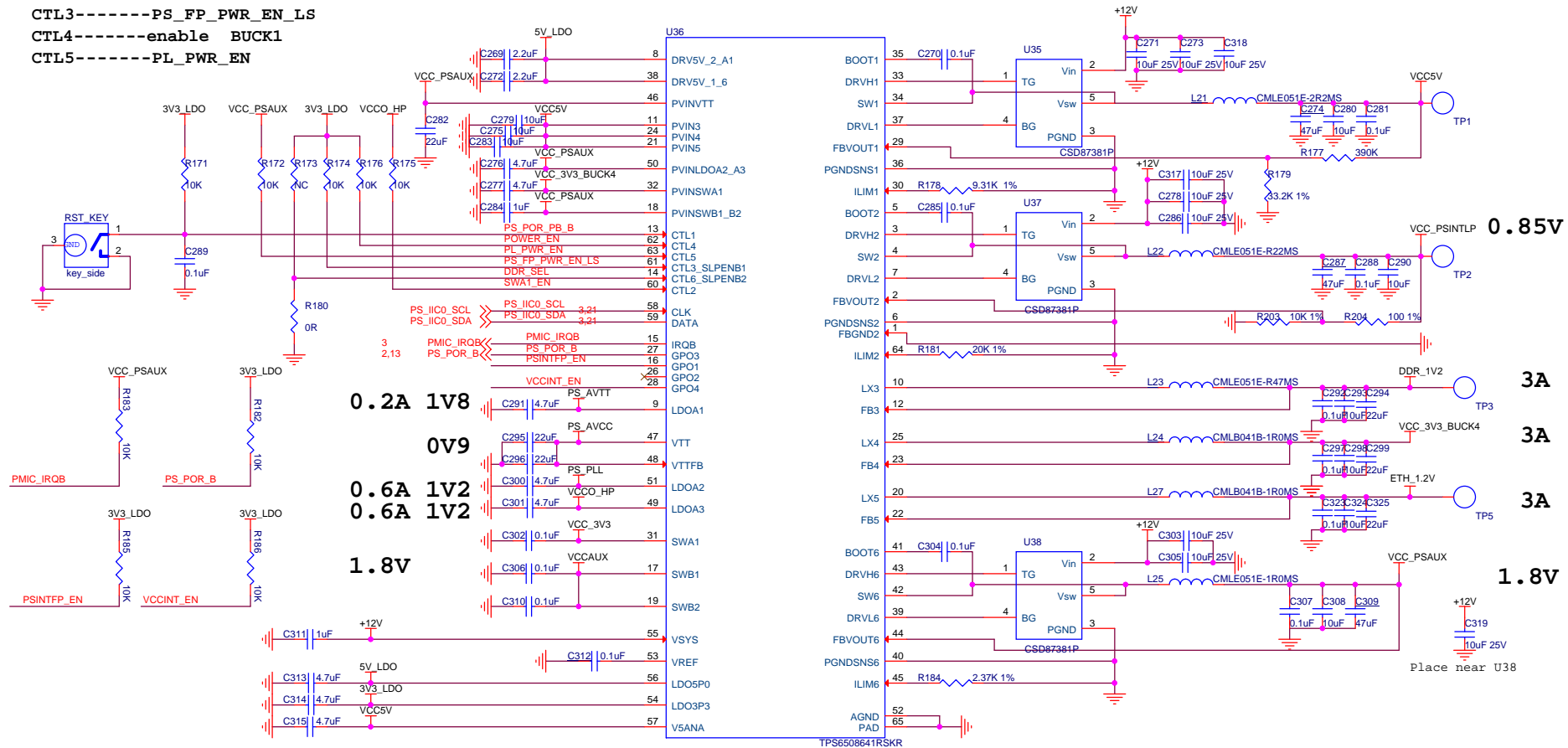
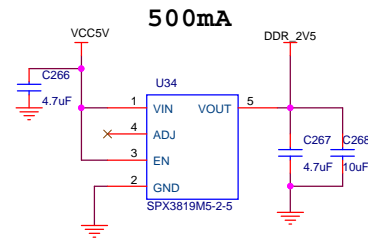
***ALINX Confidential***



PS\_IIC1: IIC ADDRESS IS 0x76



```
CTL2-----enable and disable SWA1
CTL3-----PS_FP_PWR_EN_LS
CTL4-----enable BUCK1
CTL5-----PL_PWR_EN
```



Pin connections for the TI-RTOS boot loader:

- VCC\_PSINTLP is connected to VCC\_PSINTFP and VCCINT.
- PS\_AVTT is connected to TP7.
- VCC\_3V3 is connected to TP4.
- VCCAUX is connected to TP6.
- VCCO\_HP is connected to TP8.
- PS\_PLL is connected to TP9.
- PS\_AVCC is connected to TP10.
- DDR\_2V5 is connected to TP11.



# 12V/2A AC Adapter

