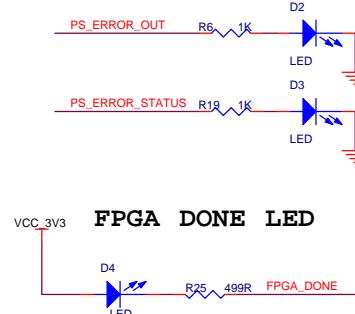
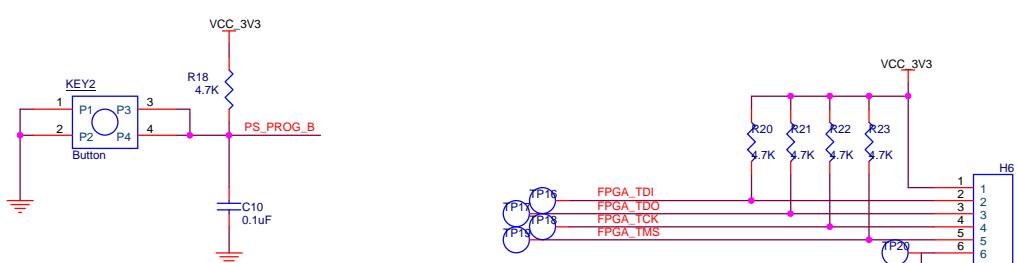
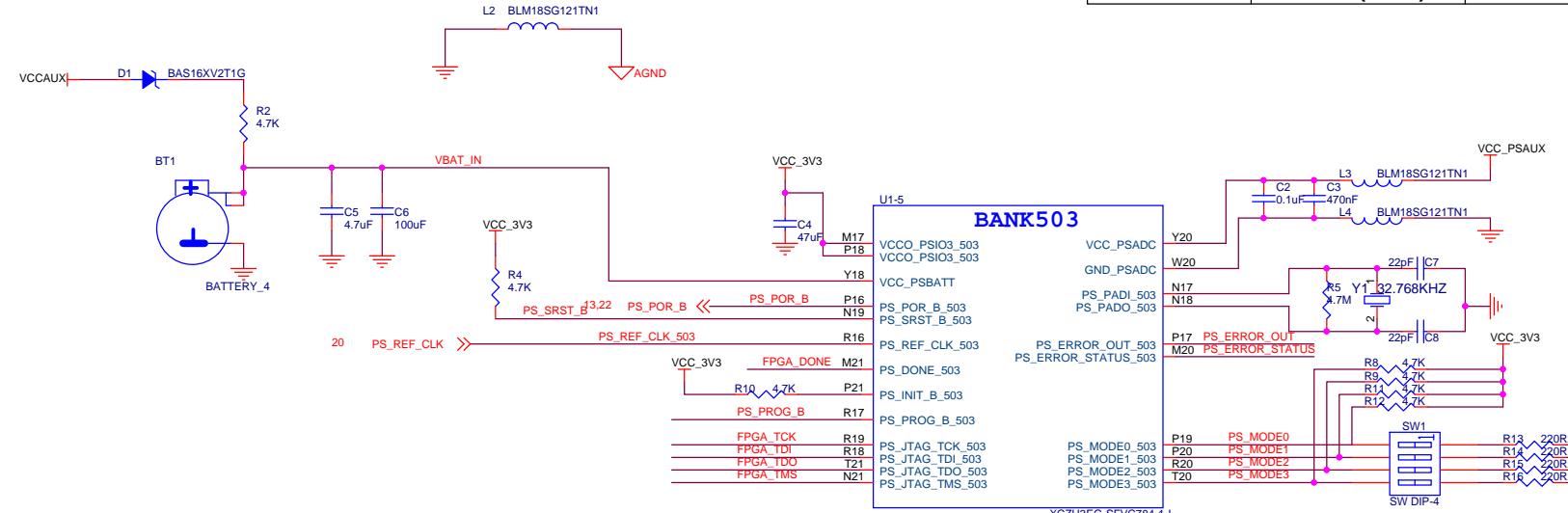
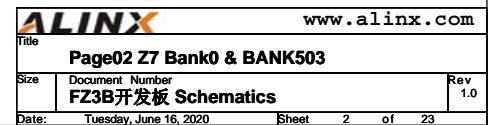


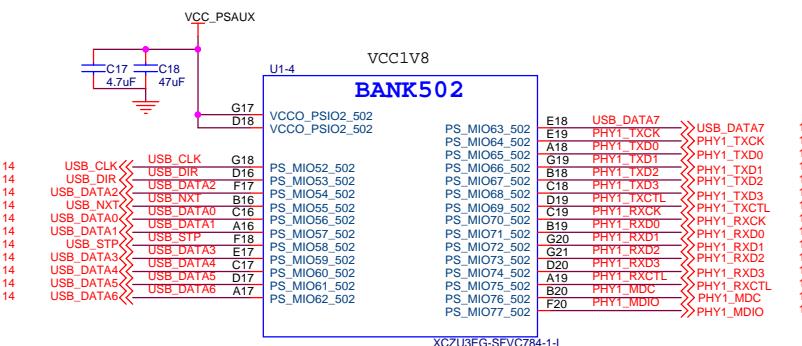
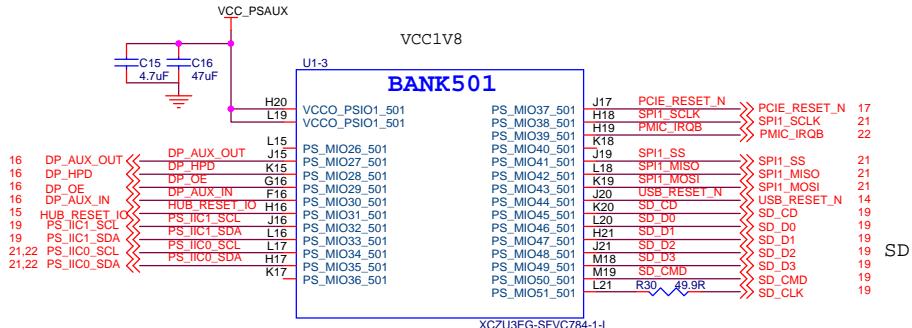
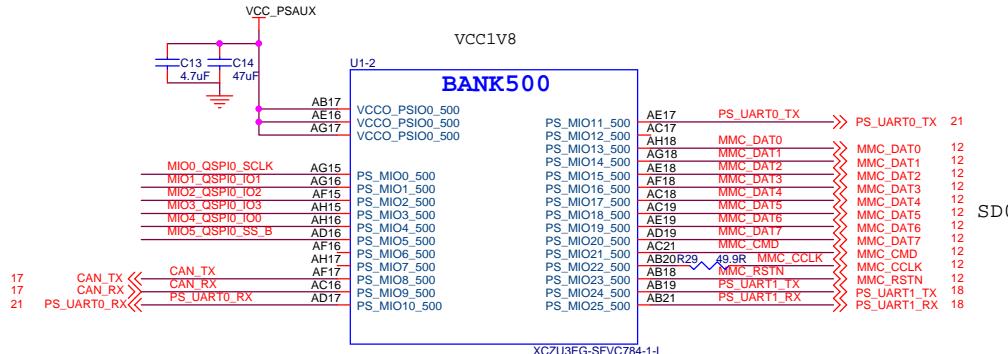
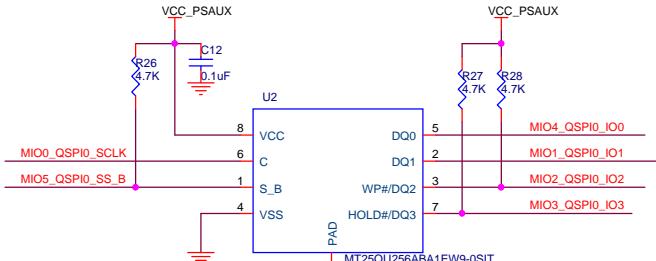
MODE[3:0]	BOOT MODE	Description
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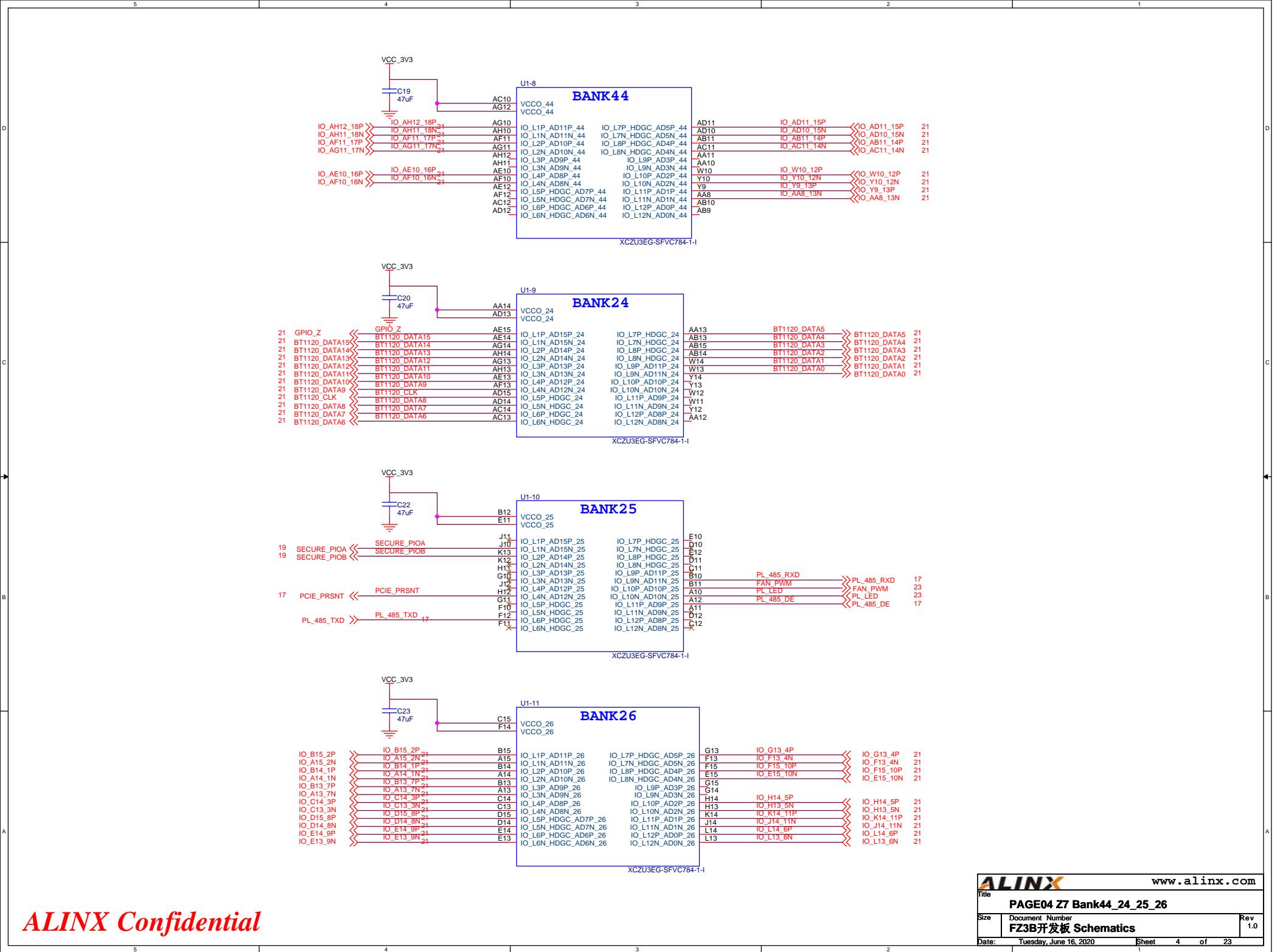
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<b>0001</b>	<b>Quad_SPI(24b)</b>	<b>24-Bit addresssing(QSPI24)</b>
<b>0010</b>	<b>Quad_SPI(32b)</b>	<b>32-Bit addresssing(QSPI32)</b>
<b>0011</b>	<b>SD0(2.0)</b>	<b>SD2.0</b>
<b>0101</b>	<b>SD1(2.0)</b>	<b>SD2.0</b>
<b>0110</b>	<b>eMMC(1.8V)</b>	<b>eMMC version 4.5 at 1.8V</b>
<b>0111</b>	<b>USB0(2.0)</b>	<b>USB 2.0 only</b>
<b>1110</b>	<b>SD1 LS(3.0)</b>	<b>SD 3.0</b>

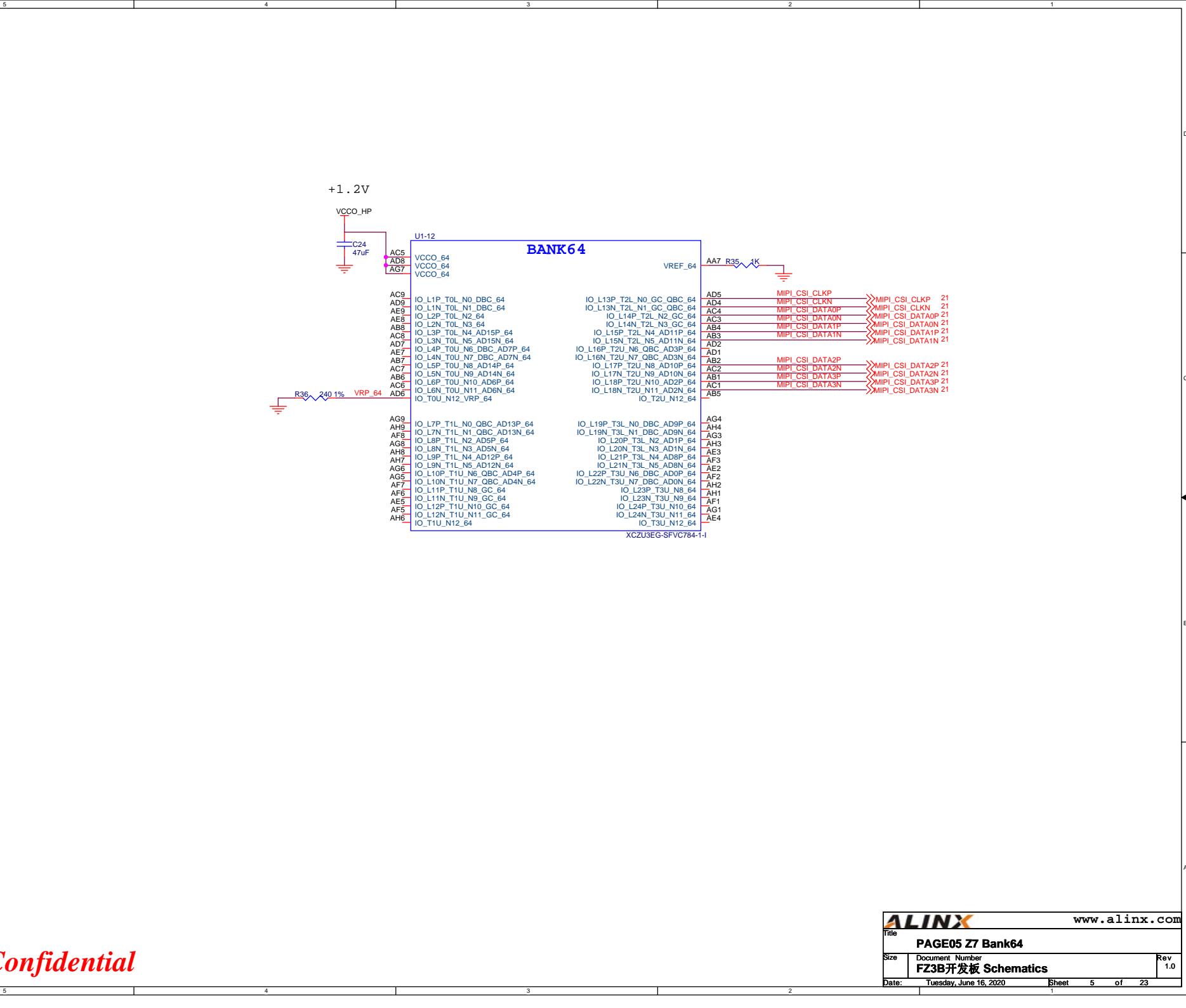


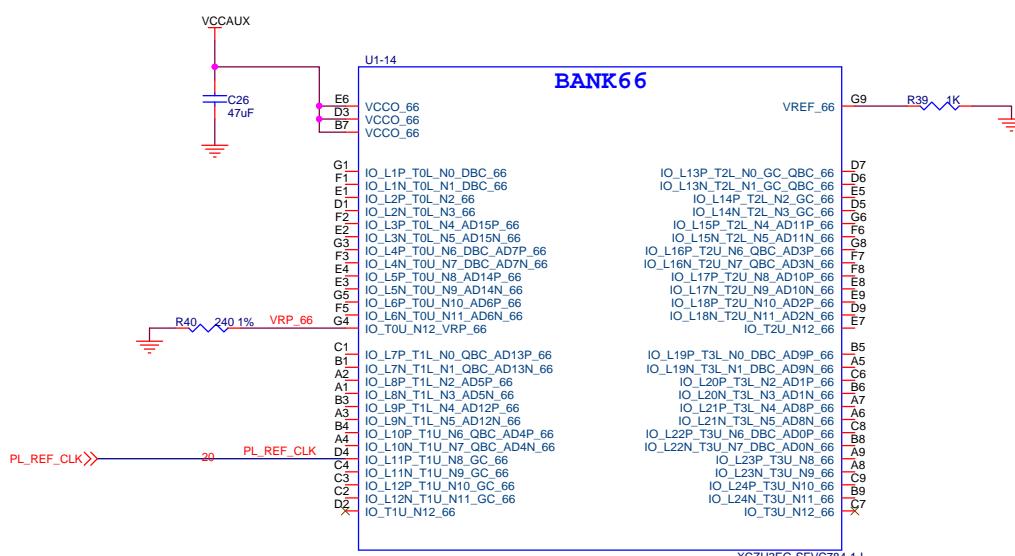
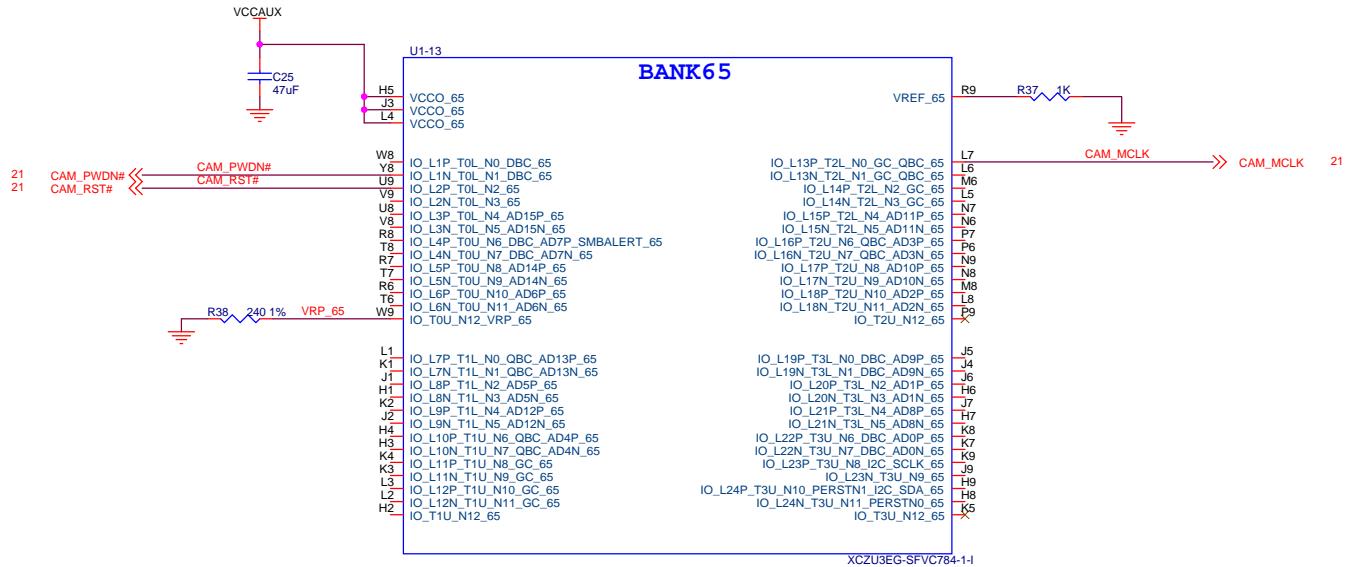
**FPGA DONE LED**

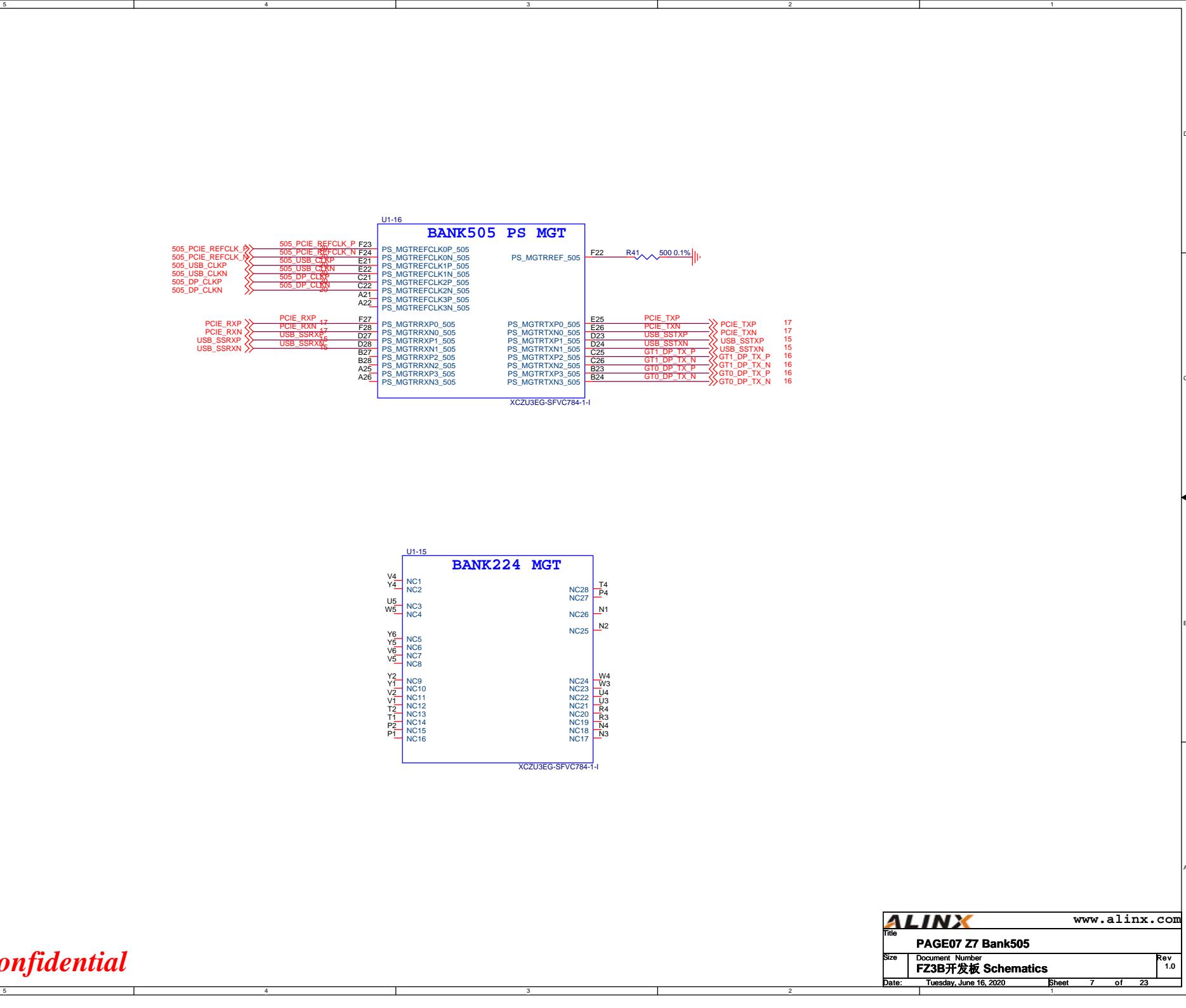


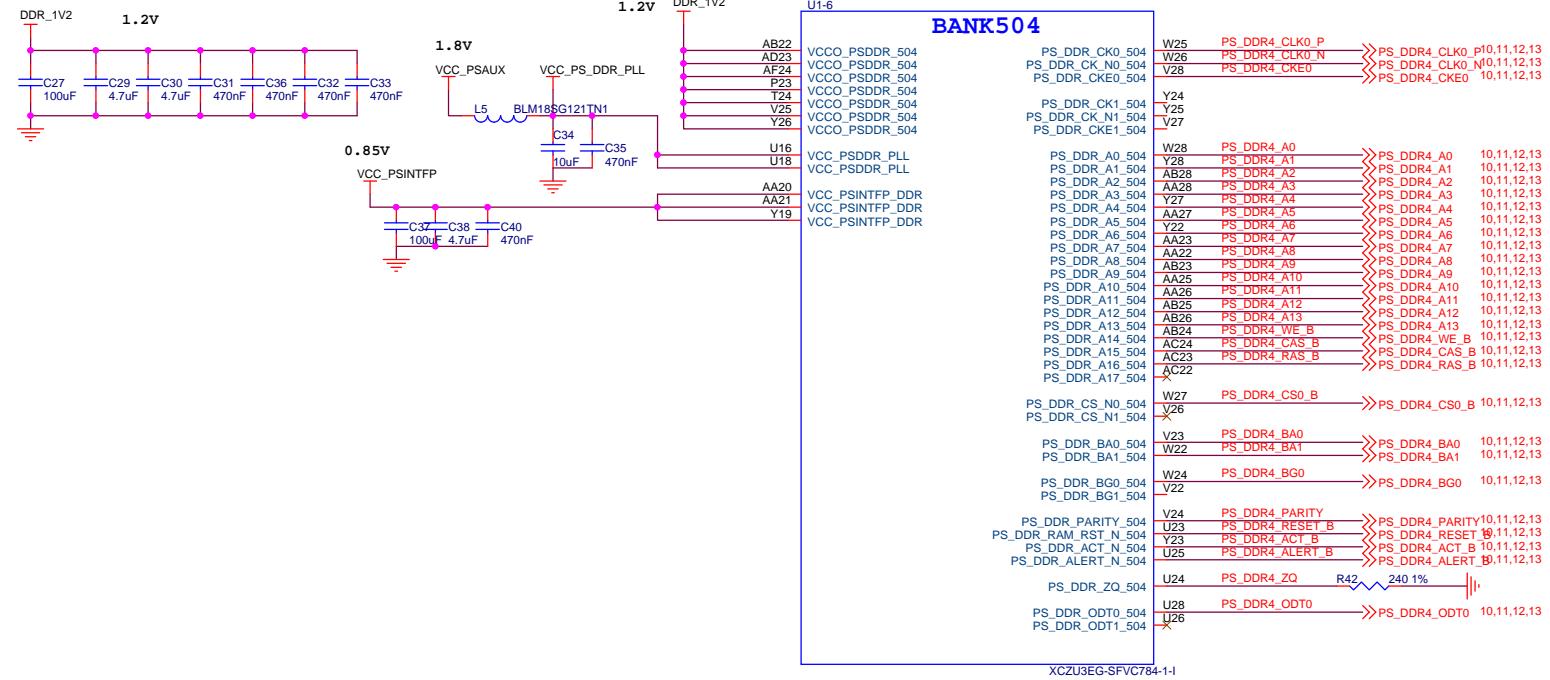




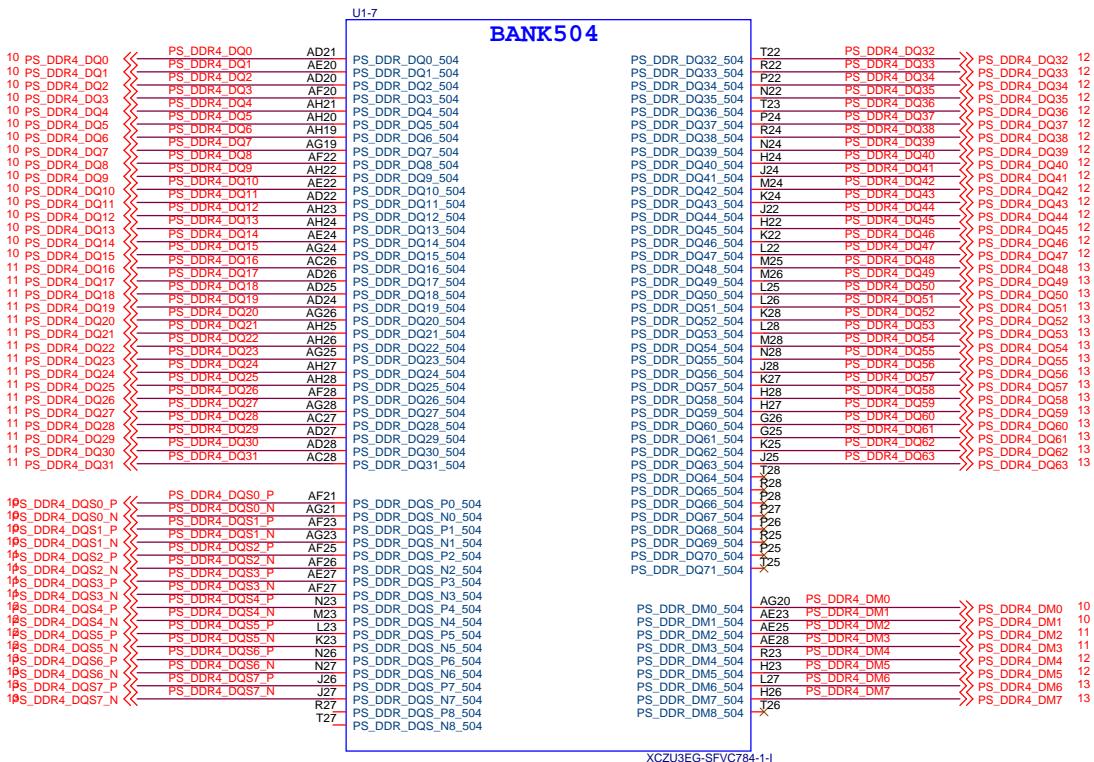




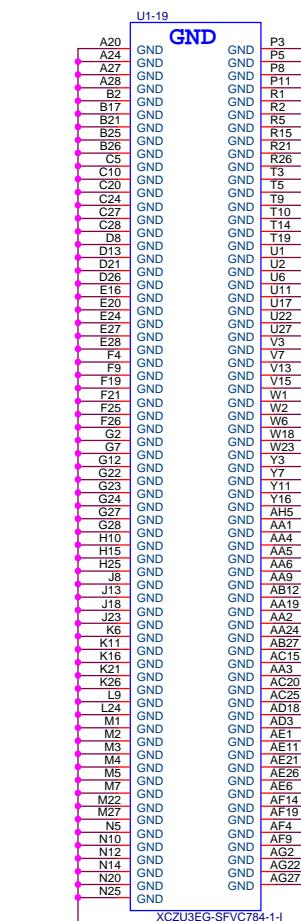
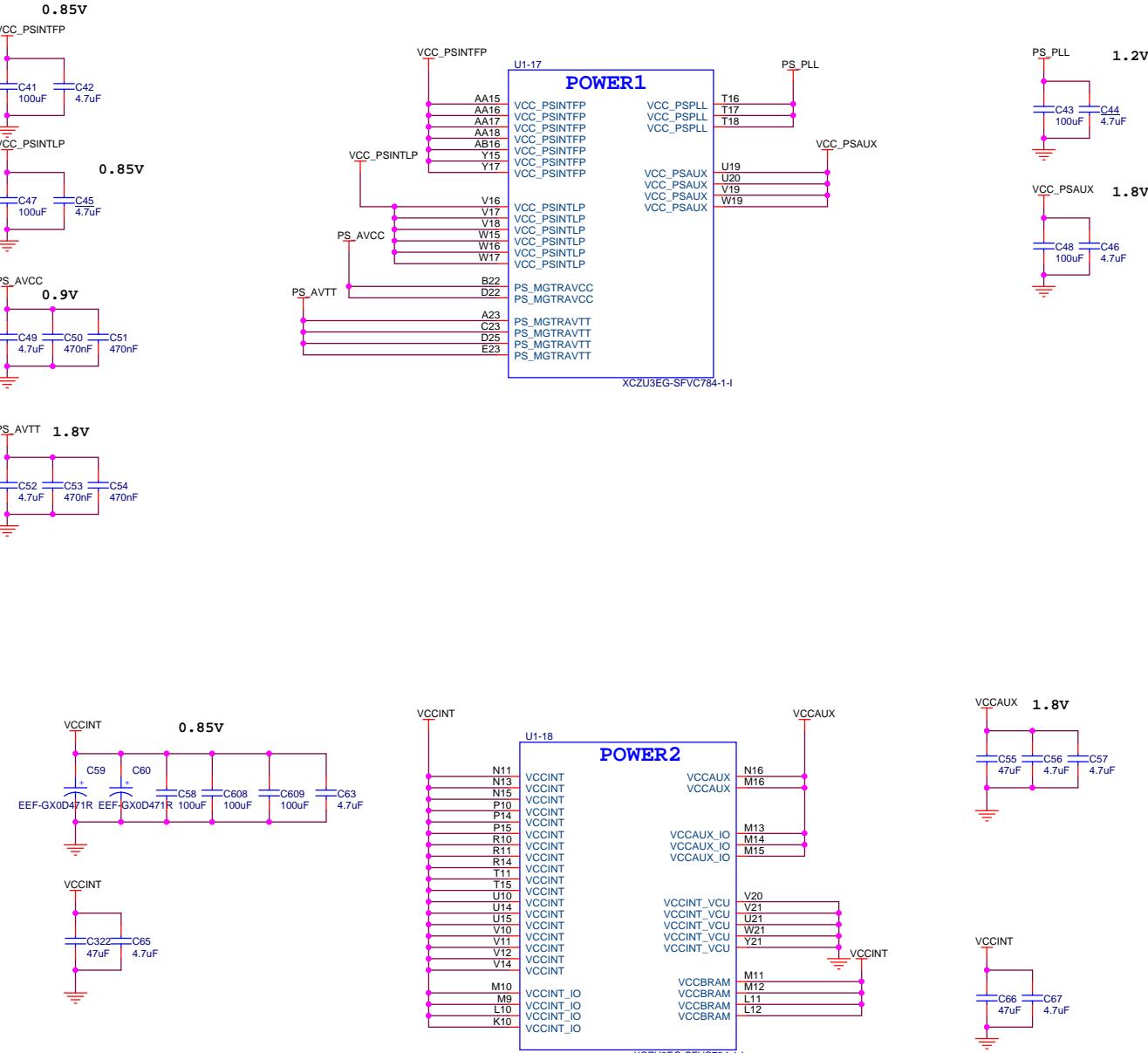


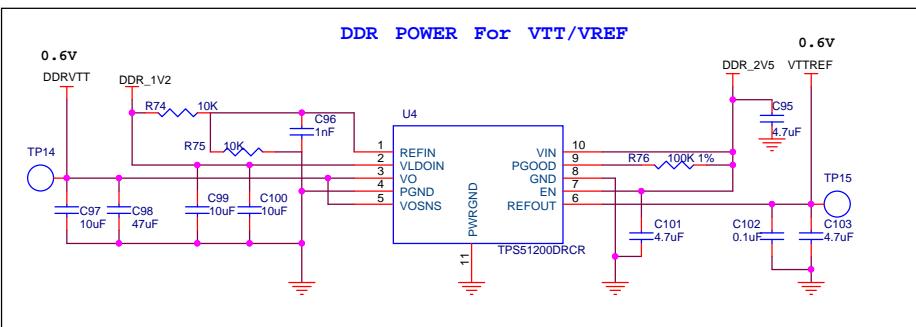
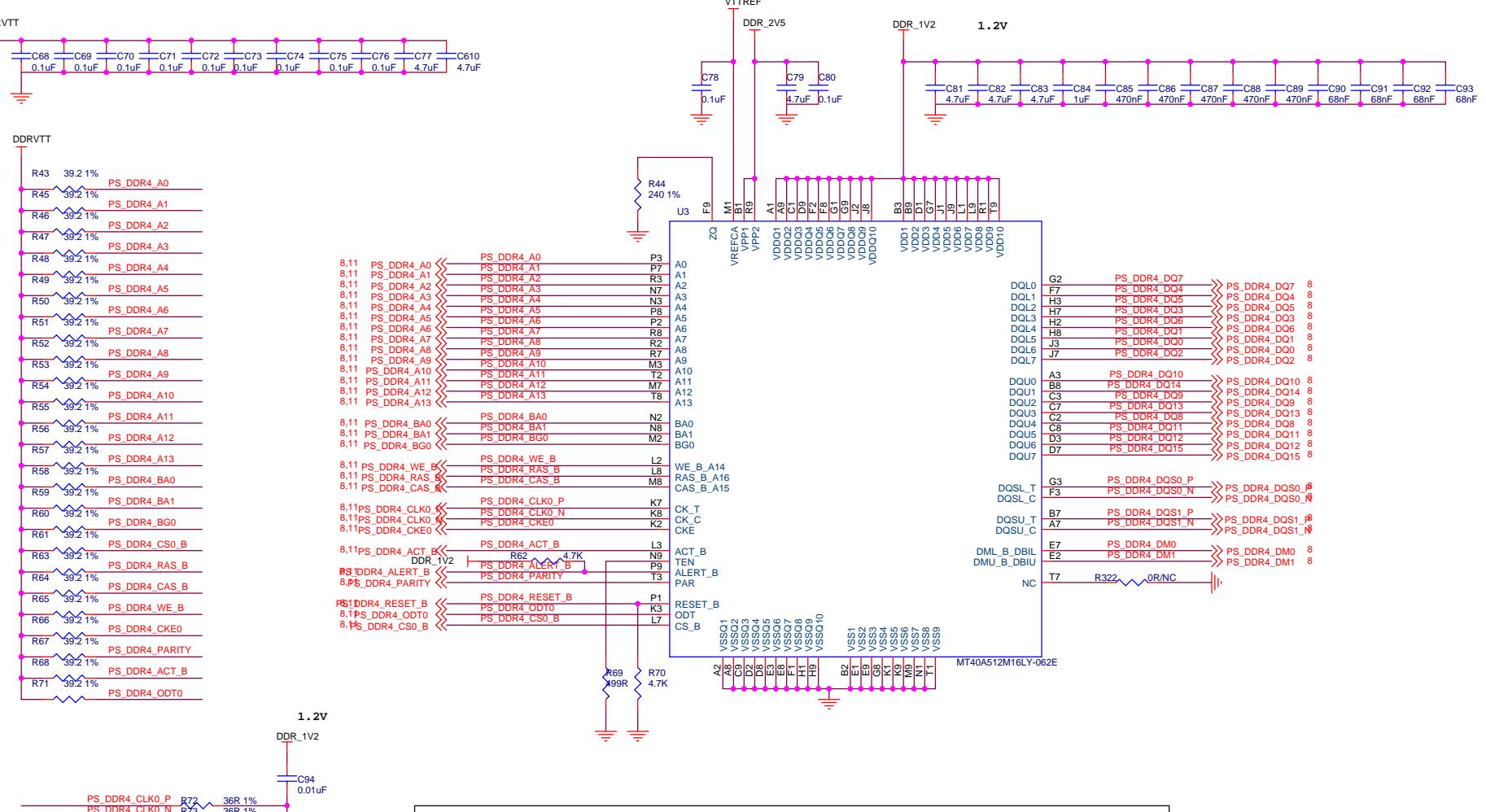


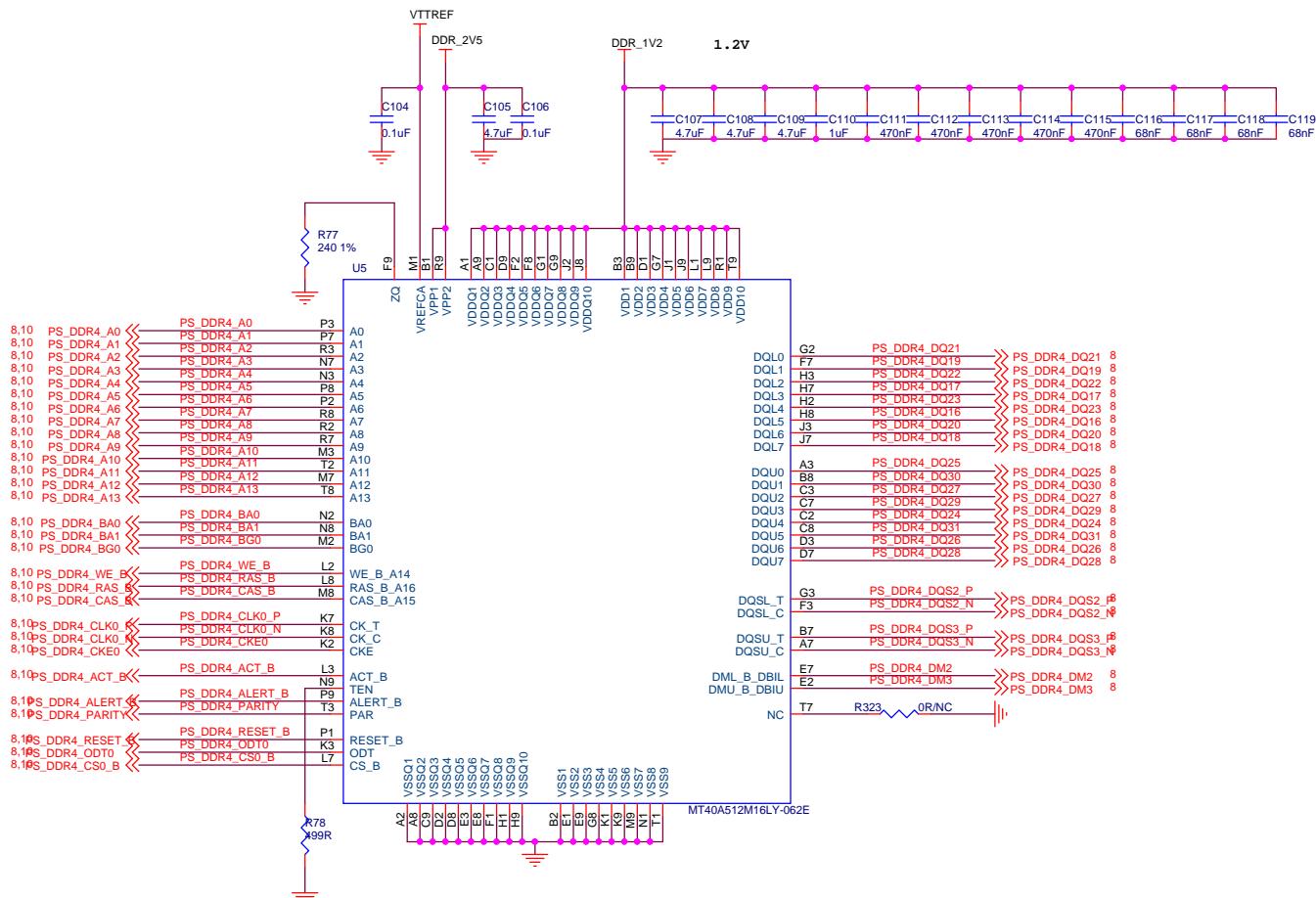
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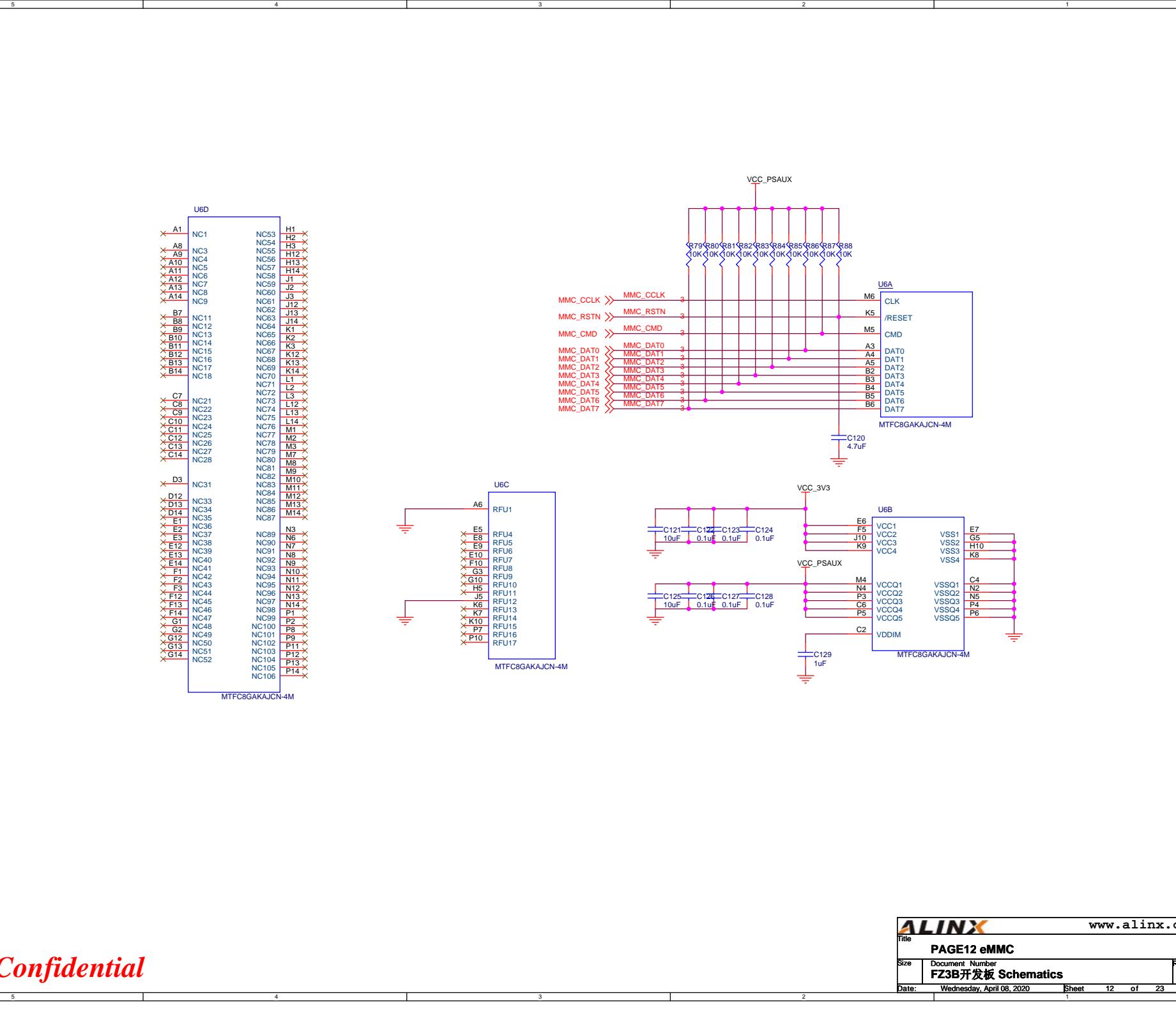


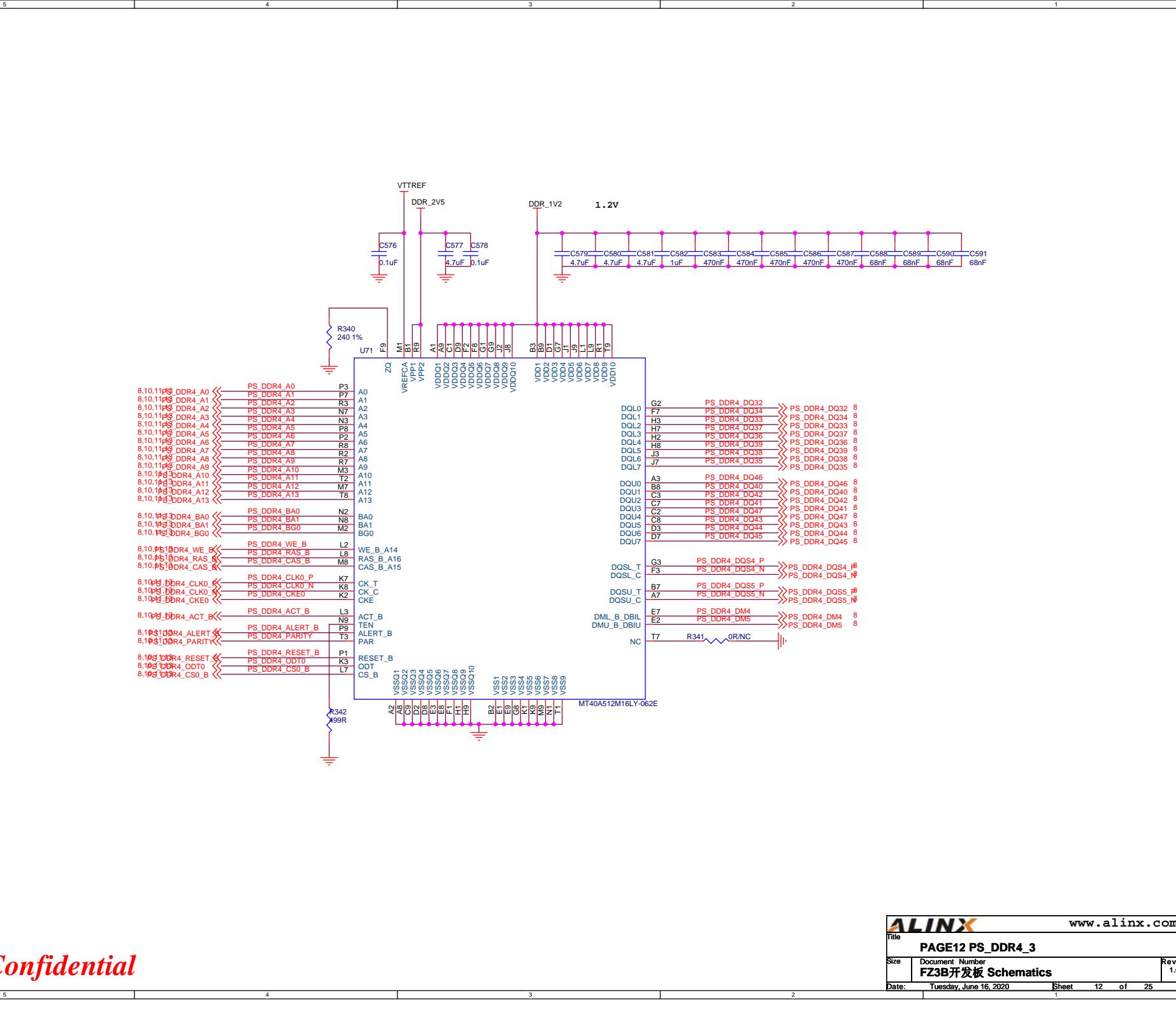
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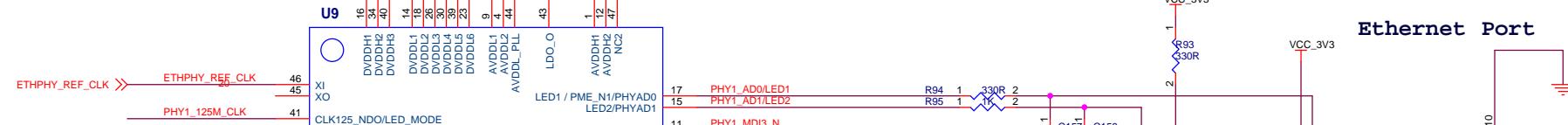
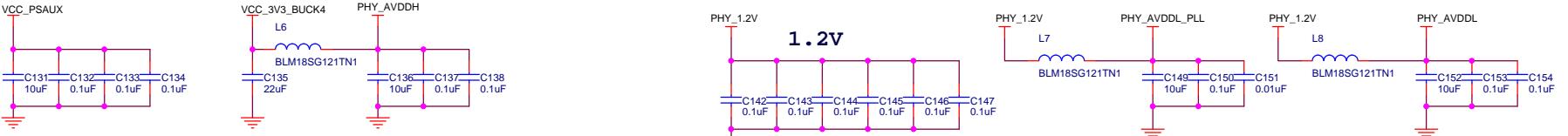






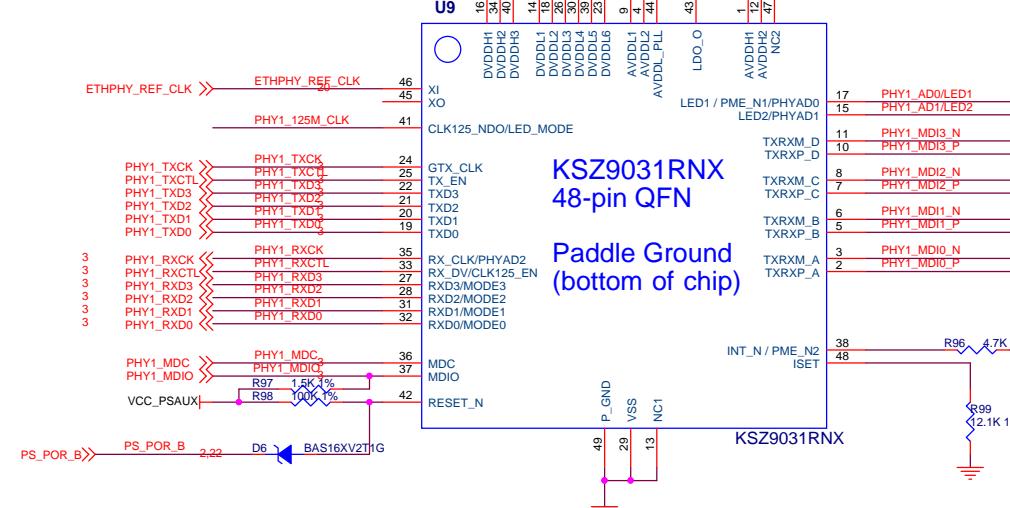


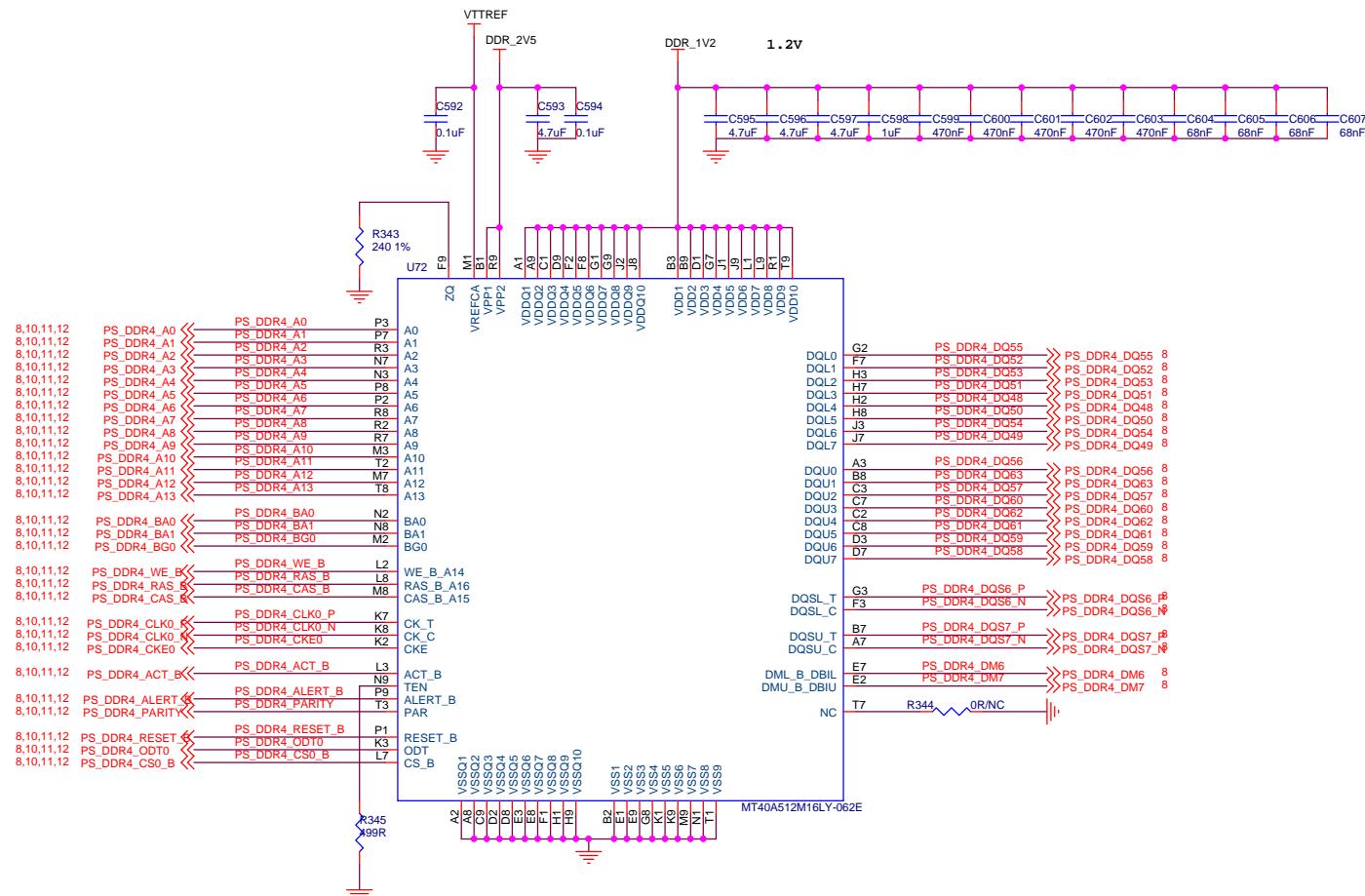
**Power Sequence: 3.3V ---> 1.2V**

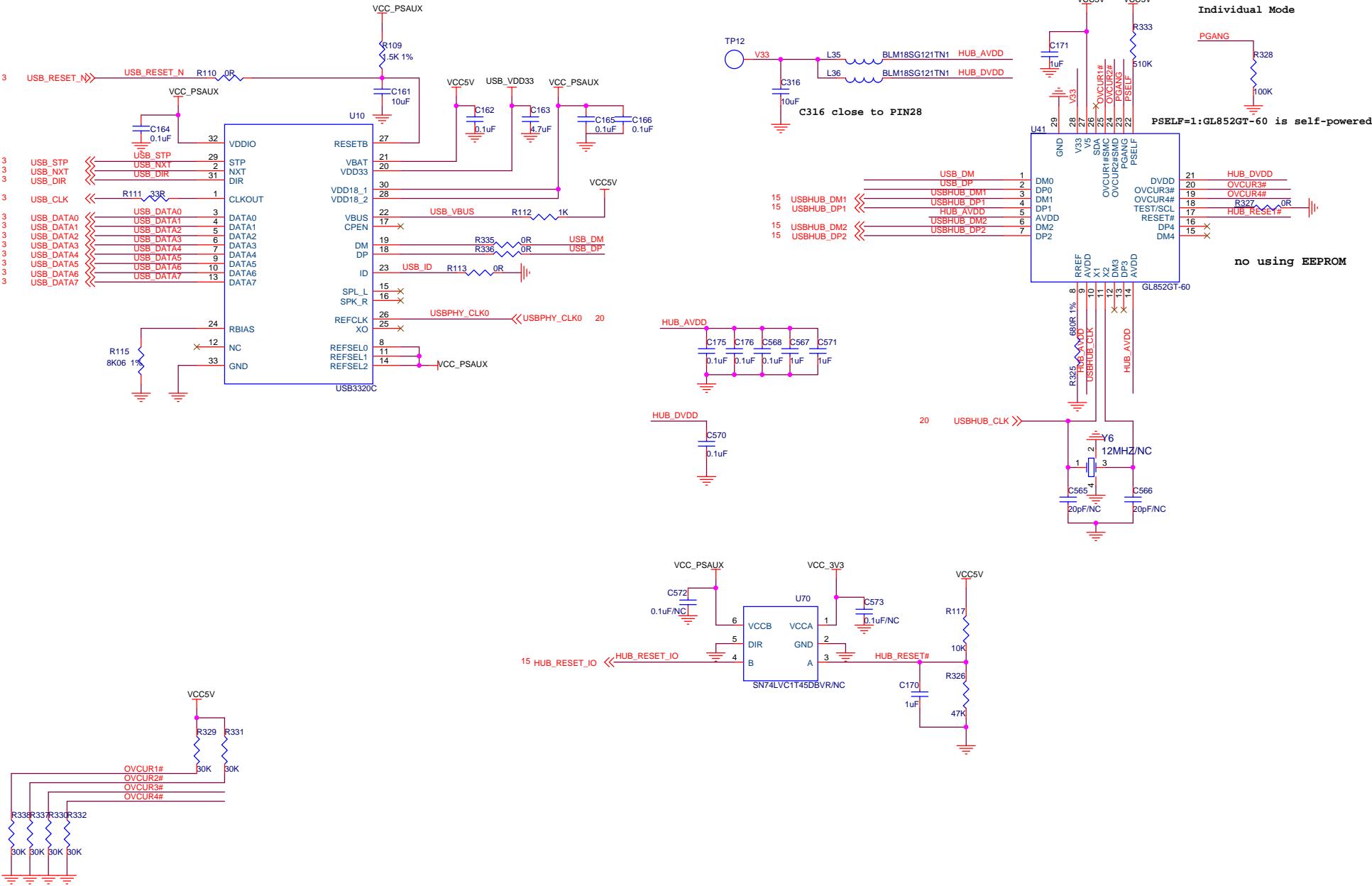


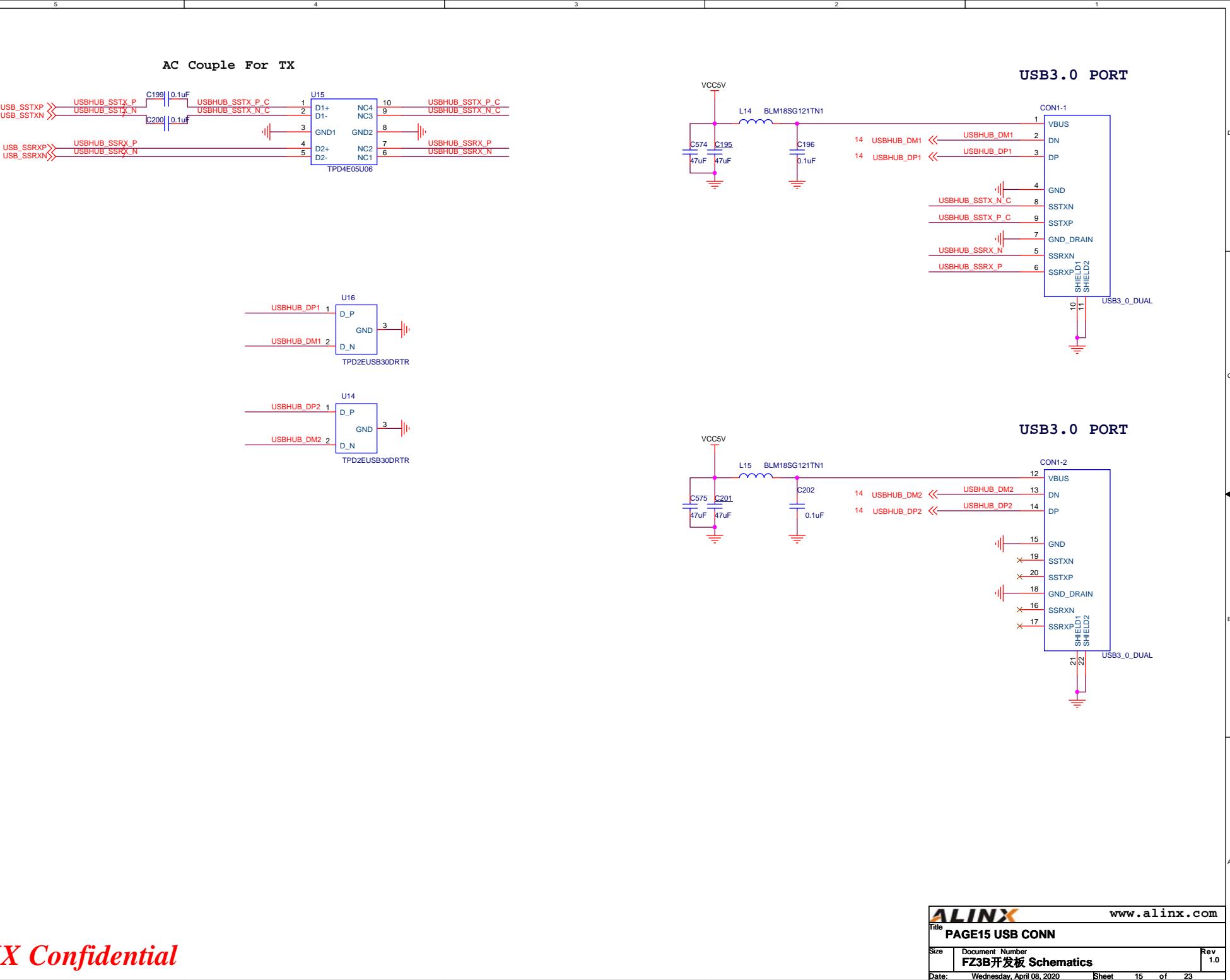
**KSZ9031RNX  
48-pin QFN**

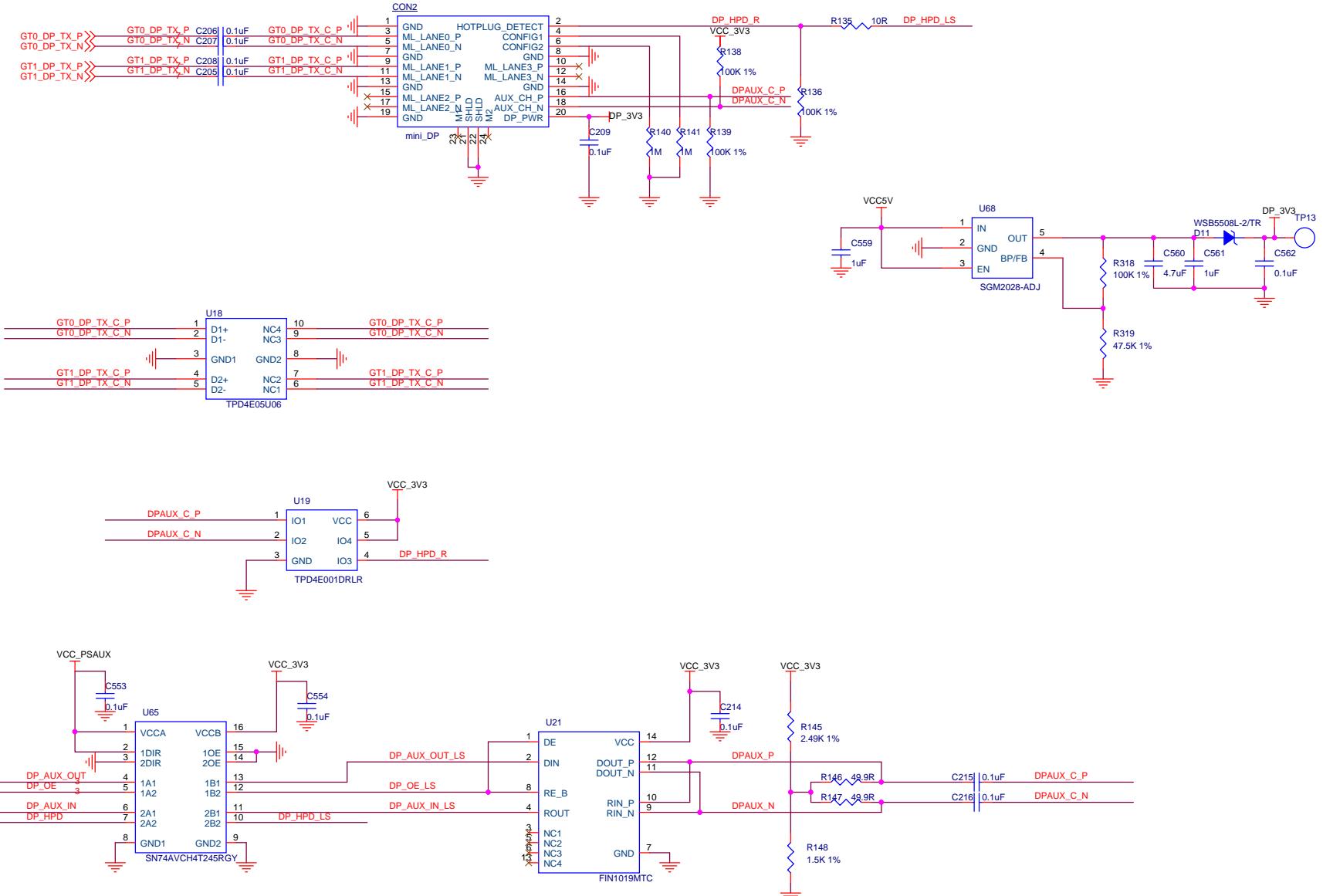
**Paddle Ground  
(bottom of chip)**

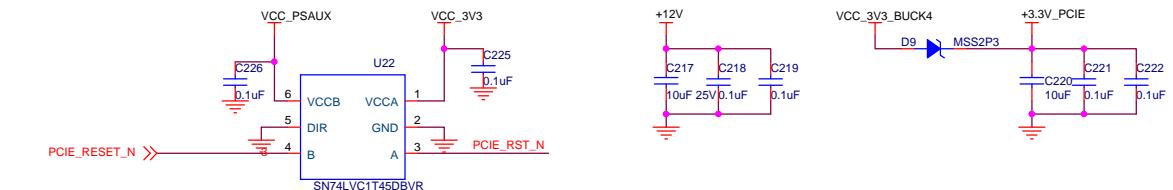




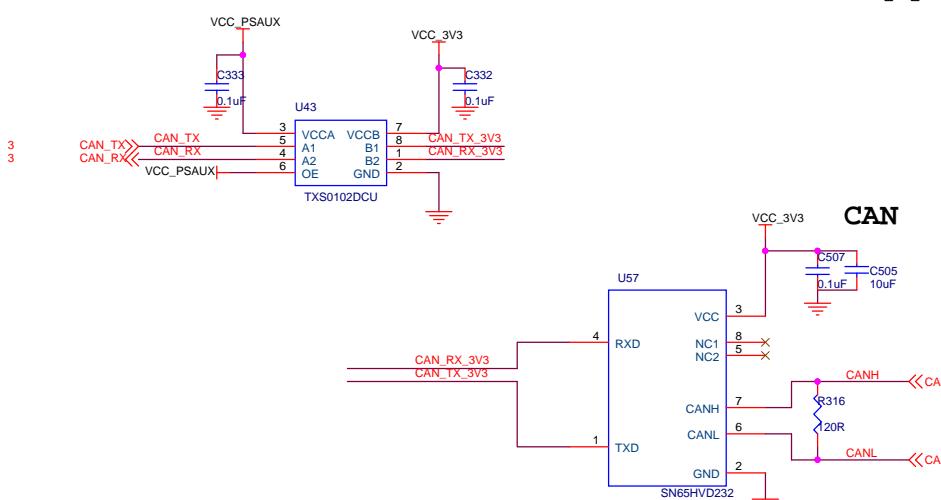
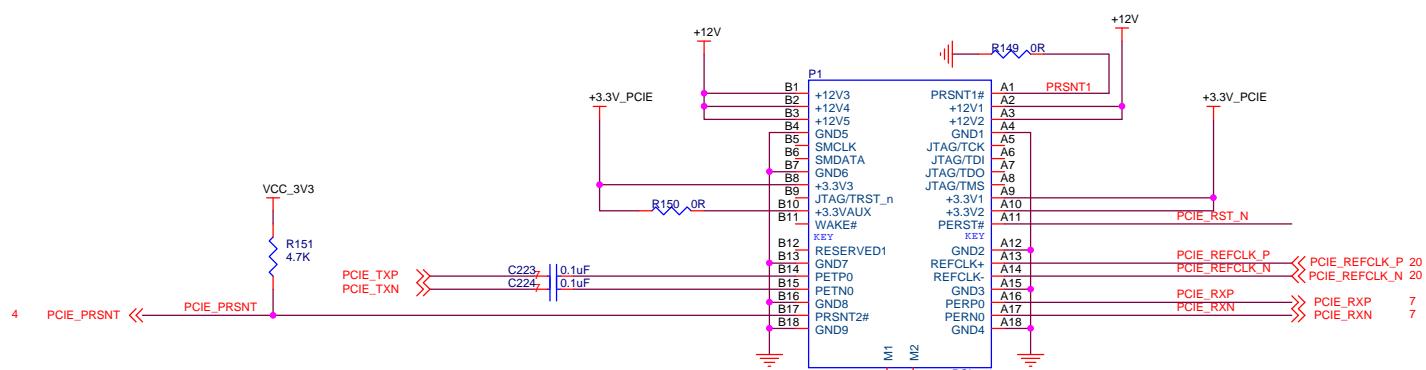




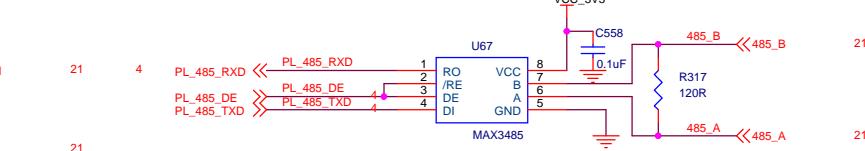




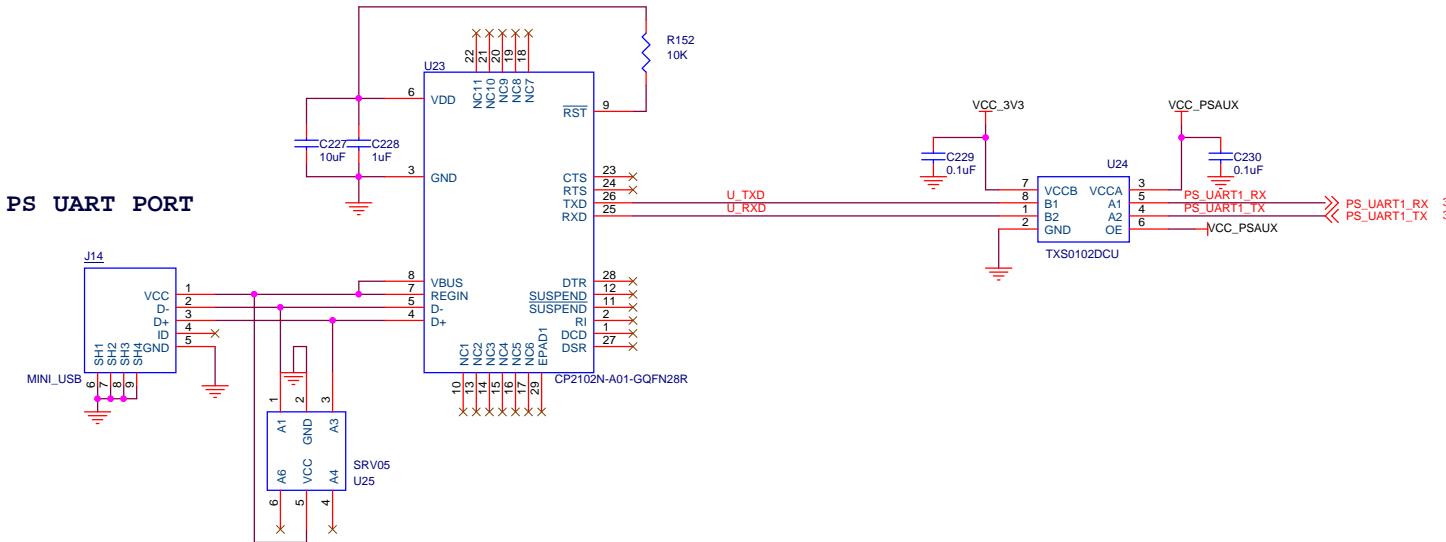
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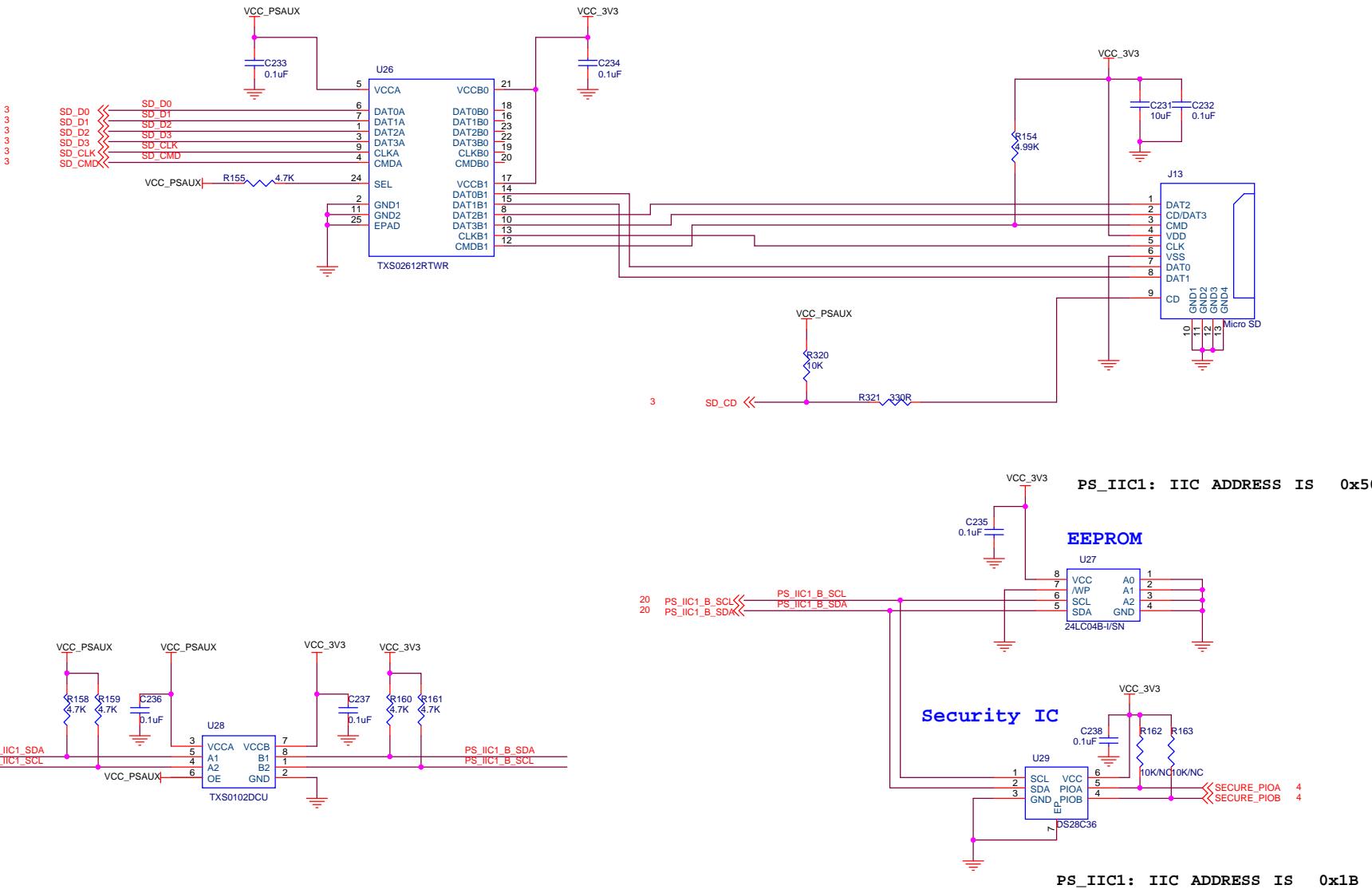
## CAN



485



## SD Card Connector



**ALINX**

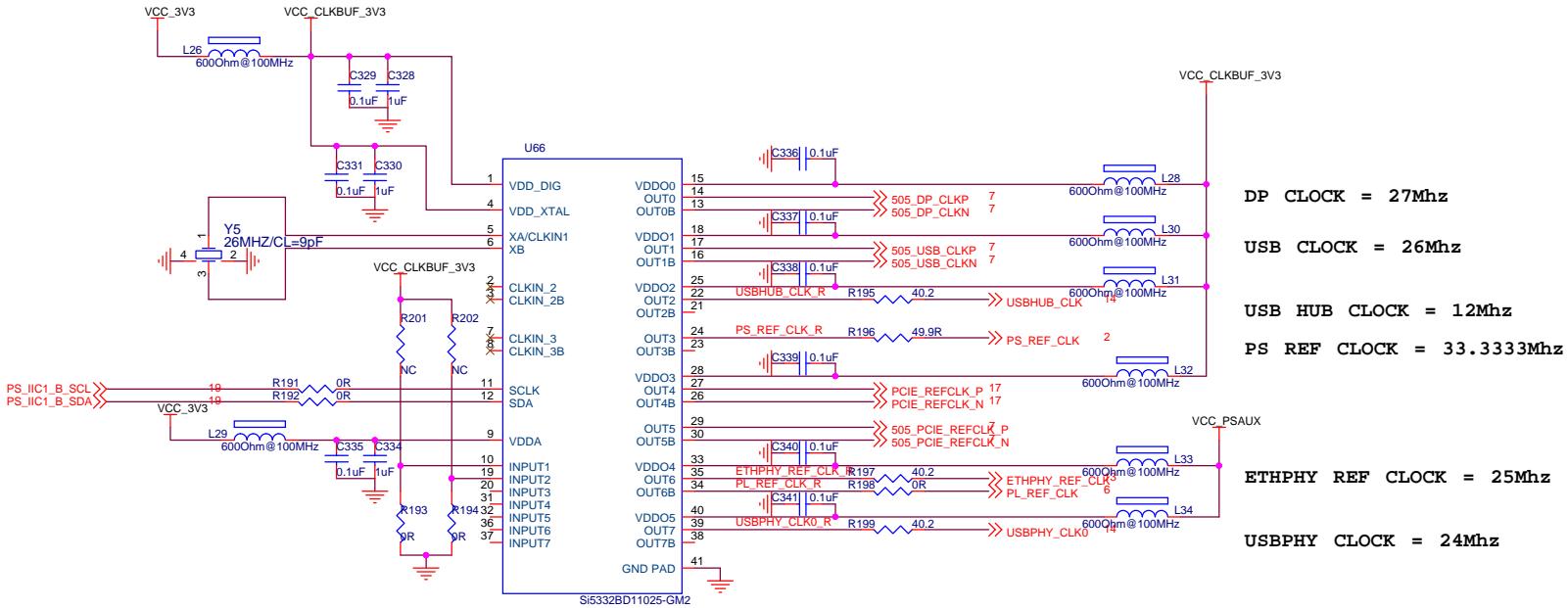
[www.alinx.com](http://www.alinx.com)

Title PAGE19 SD & EEPROM

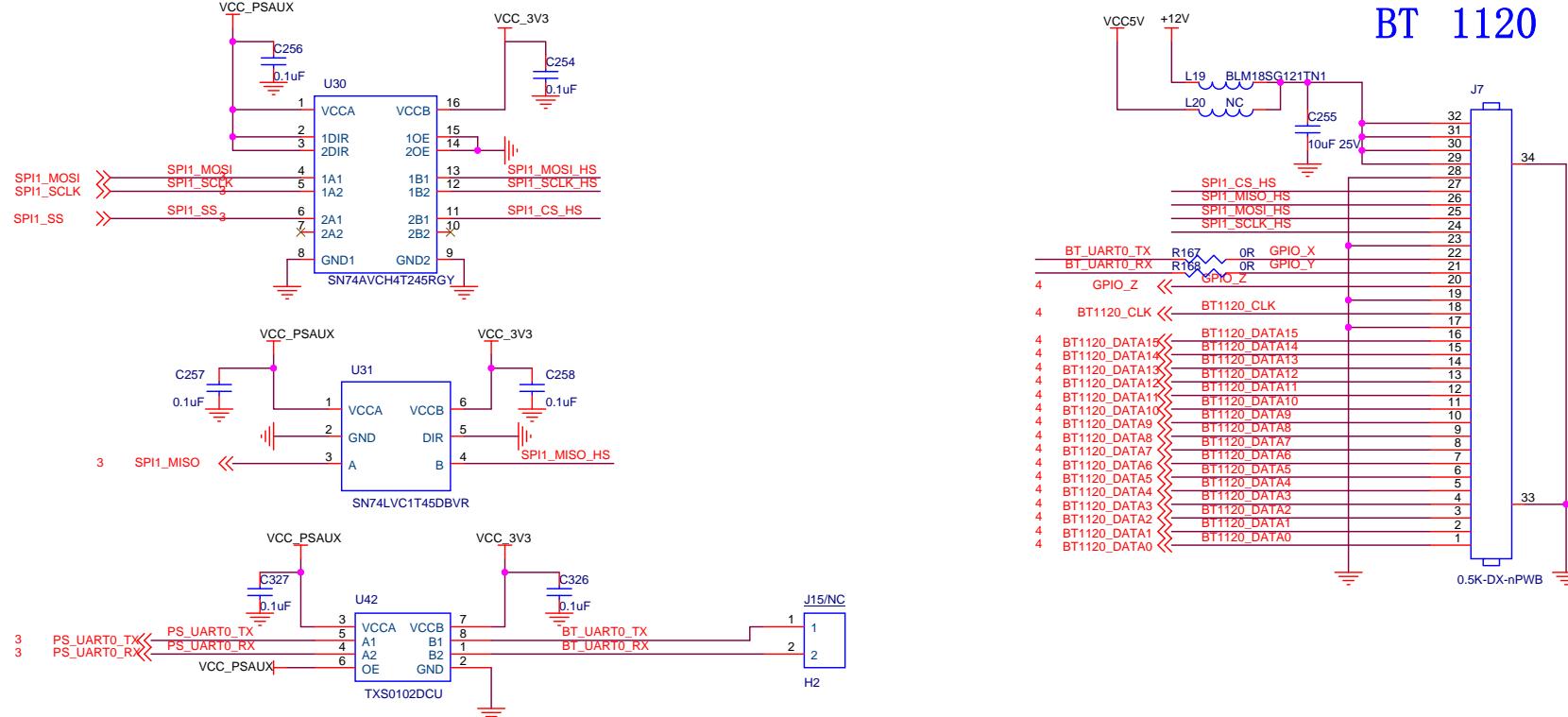
Size Document Number  
FZ3B 开发板 Schematics

Rev 1.0

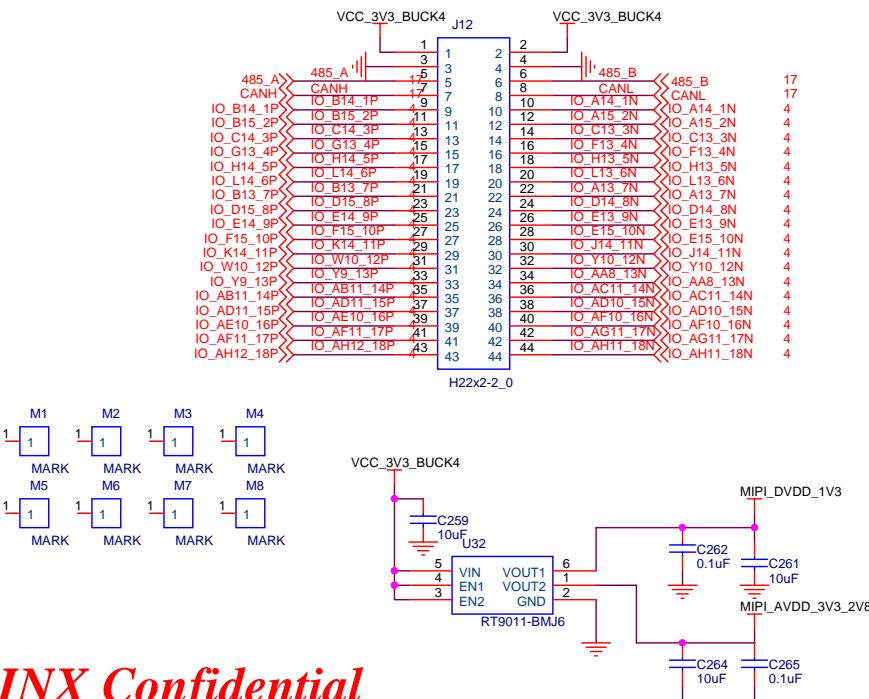
Date: Wednesday, April 08, 2020 Sheet 19 of 23



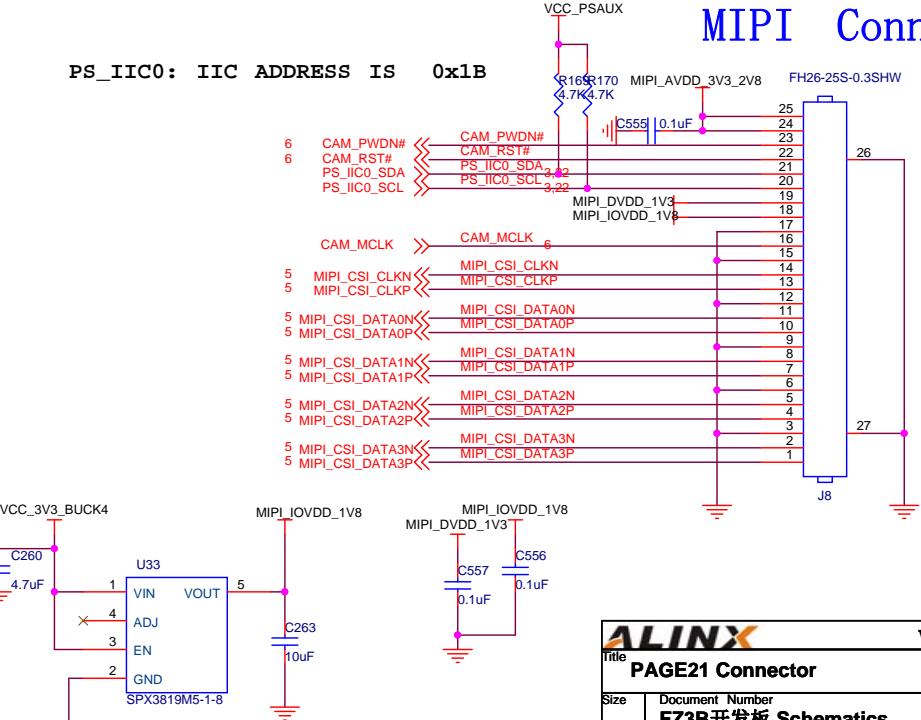
BT 1120



MIPI Connector



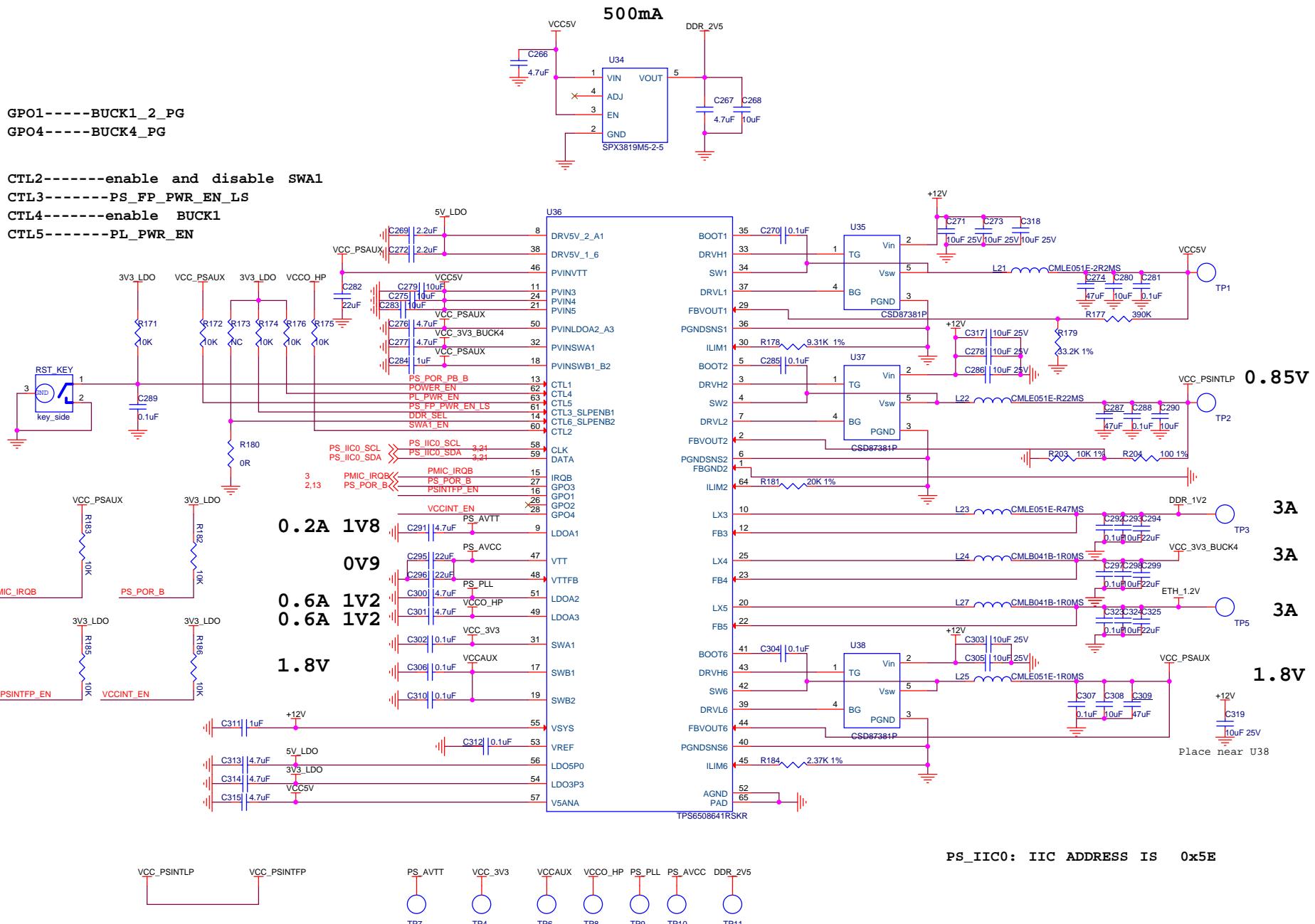
PS\_IIC0: IIC ADDRESS IS 0x1B



ALINX  
PAGE21 Connector

Size	Document Number	Rev
FZ3B	Alinx FZ3B Development Board Schematics	1.0

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