1. Description

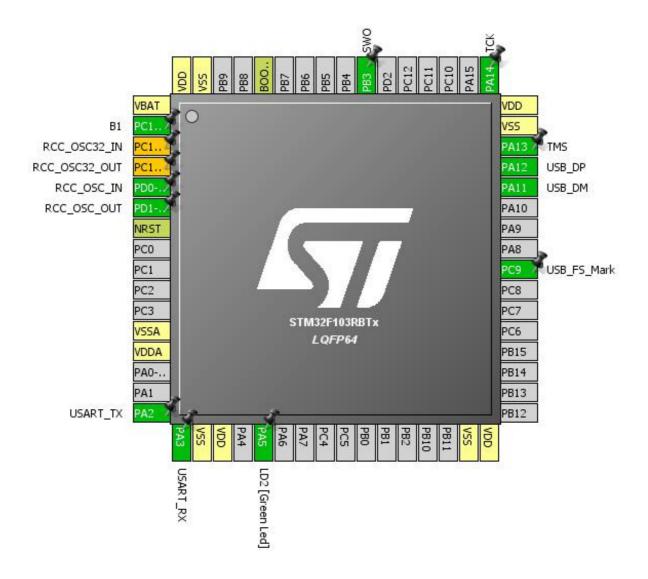
1.1. Project

Project Name	STM32F1T1
Board Name	NUCLEO-F103RB
Generated with:	STM32CubeMX 4.21.0
Date	05/25/2017

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103RBTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



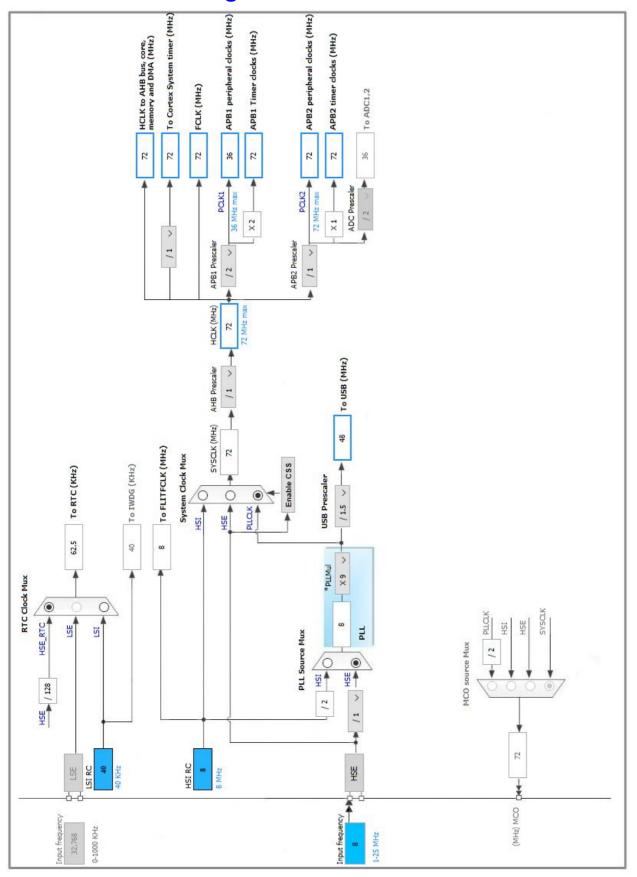
3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
2	PC13-TAMPER-RTC *	I/O	GPIO_Input	B1
3	PC14-OSC32_IN **	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT **	I/O	RCC_OSC32_OUT	
5	PD0-OSC_IN	I/O	RCC_OSC_IN	
6	PD1-OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
16	PA2	I/O	USART2_TX	USART_TX
17	PA3	I/O	USART2_RX	USART_RX
18	VSS	Power		
19	VDD	Power		
21	PA5 *	I/O	GPIO_Output	LD2 [Green Led]
31	VSS	Power		
32	VDD	Power		
40	PC9 *	I/O	GPIO_Output	USB_FS_Mark
44	PA11	I/O	USB_DM	
45	PA12	I/O	USB_DP	
46	PA13	I/O	SYS_JTMS-SWDIO	TMS
47	VSS	Power		
48	VDD	Power		
49	PA14	I/O	SYS_JTCK-SWCLK	TCK
55	PB3	I/O	SYS_JTDO-TRACESWO	SWO
60	воото	Boot		
63	VSS	Power		
64	VDD	Power		

^{*} The pin is affected with an I/O function

^{**} The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. RCC

High Speed Clock (HSE): BYPASS Clock Source

5.1.1. Parameter Settings:

System Parameters:

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 2 WS (3 CPU cycle)

RCC Parameters:

HSI Calibration Value 16
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

5.2. RTC

mode: Activate Clock Source

5.2.1. Parameter Settings:

Calendar Time:

Data Format BCD data format

General:

Auto Predivider Calculation Enabled

Asynchronous Predivider value Automatic Predivider Calculation Enabled

Output Alarm pulse signal on the TAMPER pin

5.3. SYS

Debug: Trace Asynchronous Sw

Timebase Source: TIM4

5.4. USART2

Mode: Asynchronous

5.4.1. Parameter Settings:

Basic Parameters:

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

Advanced Parameters:

Data Direction Receive and Transmit

Over Sampling 16 Samples

5.5. USB

mode: Device (FS)

5.5.1. Parameter Settings:

Basic Parameters:

Speed Full Speed 12MBit/s

Endpoint 0 Max Packet size 8 Bytes

Power Parameters:

Low PowerDisabledLink Power ManagementDisabledBattery ChargingDisabled

5.6. FREERTOS

mode: Enabled

5.6.1. Config parameters:

Versions:

FreeRTOS version 8.2.3 CMSIS-RTOS version 1.02

Kernel settings:

USE_PREEMPTION Enabled

CPU_CLOCK_HZ SystemCoreClock

1000 TICK_RATE_HZ 7 MAX_PRIORITIES 128 MINIMAL_STACK_SIZE 16 MAX_TASK_NAME_LEN Disabled USE_16_BIT_TICKS Enabled IDLE_SHOULD_YIELD Enabled USE_MUTEXES Disabled USE_RECURSIVE_MUTEXES Disabled USE_COUNTING_SEMAPHORES QUEUE_REGISTRY_SIZE Disabled USE_APPLICATION_TASK_TAG Disabled USE_ALTERNATIVE_API Enabled ENABLE_BACKWARD_COMPATIBILITY USE_PORT_OPTIMISED_TASK_SELECTION Disabled Disabled USE_TICKLESS_IDLE USE_TASK_NOTIFICATIONS Enabled

Memory management settings:

TOTAL_HEAP_SIZE 3072

Memory Management scheme heap_4

Hook function related definitions:

USE_IDLE_HOOK Disabled
USE_TICK_HOOK Disabled
USE_MALLOC_FAILED_HOOK Disabled
CHECK_FOR_STACK_OVERFLOW Disabled

Run time and task stats gathering related definitions:

GENERATE_RUN_TIME_STATS Disabled
USE_TRACE_FACILITY Disabled
USE_STATS_FORMATTING_FUNCTIONS Disabled

Co-routine related definitions:

USE_CO_ROUTINES Disabled MAX_CO_ROUTINE_PRIORITIES 2

Software timer definitions:

USE_TIMERS Disabled

Interrupt nesting behaviour configuration:

LIBRARY_LOWEST_INTERRUPT_PRIORITY 15
LIBRARY_MAX_SYSCALL_INTERRUPT_PRIORITY 5

5.6.2. Include parameters:

Include definitions:

Enabled vTaskPrioritySet Enabled uxTaskPriorityGet vTaskDelete Enabled Disabled vTaskCleanUpResources Enabled vTaskSuspend vTaskDelayUntil Disabled Enabled vTaskDelay Enabled xTaskGetSchedulerState xTaskResumeFromISR Enabled Disabled xQueueGetMutexHolder Disabled xSemaphoreGetMutexHolder pcTaskGetTaskName Disabled uxTaskGetStackHighWaterMark Disabled xTaskGetCurrentTaskHandle Disabled eTaskGetState Disabled xEventGroupSetBitFromISR Disabled xTimerPendFunctionCall Disabled

5.7. USB DEVICE

Class For FS IP: Communication Device Class (Virtual Port Com)

5.7.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces) 1

USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration) 1

USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors) 512

USBD_SUPPORT_USER_STRING (Enable user string descriptor) Disabled

USBD_SELF_POWERED (Enabled self power) Enabled

USBD_DEBUG_LEVEL (USBD Debug Level) 0: No debug message

Class Parameters:

USBD_CDC_INTERVAL (Number of micro-frames interval) 1000

5.7.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier) 1155

LANGID_STRING (Language Identifier) English(United States)

MANUFACTURER_STRING (Manufacturer Identifier) STMicroelectronics

Device Descriptor FS:

PID (Product IDentifier) 22336

PRODUCT_STRING (Product Identifier) STM32 Virtual ComPort

SERIALNUMBER_STRING (Serial number) 0000000001A

CONFIGURATION_STRING (Configuration Identifier) CDC Config

INTERFACE_STRING (Interface Identifier) CDC Interface

* User modified value

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PD0- OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PD1- OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS- SWDIO	n/a	n/a	n/a	TMS
	PA14	SYS_JTCK- SWCLK	n/a	n/a	n/a	TCK
	PB3	SYS_JTDO- TRACESWO	n/a	n/a	n/a	SWO
USART2	PA2	USART2_TX	Alternate Function Push Pull	n/a	Low	USART_TX
	PA3	USART2_RX	*	No pull-up and no pull-down	n/a	USART_RX
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
Single Mapped	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
Signals	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
GPIO	PC13- TAMPER- RTC	GPIO_Input	Input mode	No pull-up and no pull-down	n/a	B1
	PA5	GPIO_Output	Output Push Pull	n/a	Low	LD2 [Green Led]
	PC9	GPIO_Output	Output Push Pull	n/a	Low	USB_FS_Mark

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	15	0
System tick timer	true	15	0
USB low priority or CAN RX0 interrupts	true	5	0
TIM4 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
RTC global interrupt	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
USB high priority or CAN TX interrupts	unused		
USART2 global interrupt	unused		

^{*} User modified value

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32F1
Line	STM32F103
мси	STM32F103RBTx
Datasheet	13587_Rev17

7.2. Parameter Selection

Temperature	25
Vdd	3.3

8. Software Project

8.1. Project Settings

Name	Value
Project Name	STM32F1T1
Project Folder	E:\STM32\workspace_f4\STM32F1T1
Toolchain / IDE	SW4STM32
Firmware Package Name and Version	STM32Cube FW_F1 V1.4.0

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	