# POSTER: Finding vulnerabilities in P4 programs with assertion-based verification

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# **ABSTRACT**

Current trends in SDN extend the network programmability to the data plane through the usage of programming languages like P4. In this context, the chance of introducing errors and consequently software vulnerabilities in the network increases significantly, since packets can be processed in a general fashion. Existing data plane verification mechanisms are unable to model P4 programs or present relevant restrictions in the set of modeled properties. To overcome these limitations and make programmable data planes more secure, we present a P4 program verification technique based on assertion checking and symbolic execution. First, P4 programs are annotated with assertions expressing general correctness and security properties. Then, the annotated programs are transformed into C code and all their possible paths are symbolically executed. Results show that it is possible to prove properties in just a few seconds using the proposed technique. Moreover, we were able to uncover two potential vulnerabilities in a large scale P4 production application.

# **KEYWORDS**

ACM proceedings, LATEX, text tagging

# 1 INTRODUCTION

The data plane programmability allows operators to deploy new communication protocols and develop network services with agility. Through the usage of programming languages like P4, it is possible to specify in a few instructions which and how packet headers are manipulated by the different forwarding devices pertaining to the infrastructure. Despite the flexibility provided by this paradigm, a series of challenges need to be addressed before its ample adoption.

One of the challenges faced refers to security. If on the one hand P4 adds a new programming axis (the network data plane), on the other it also raises the chance of bug occurrences caused by incorrect protocol implementations. Such bugs can be easily transformed into vulnerabilities if exploited towards the violation of network security policies. The traditional way for trying to overcome this problem is checking if the network satisfies the intended properties using formal verification techniques (e.g., model checking or symbolic execution). Several mechanisms have been developed in this direction [3, 5, 6], but none of them is capable of efficiently verifying security properties of (P4) data plane programs.

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With the goal of enabling the verification of general correctness and security properties in P4 programs (e.g., isolation, header integrity and loop freedom), we propose a mechanism based on assertions and symbolic execution. First, a P4 program is annotated with assertions expressing the intended properties. The annotated program is then automatically translated into a C-based model, which is symbolically executed by an engine that traverses all its possible paths. Our mechanism can prove a property is satisfied by verifying if any execution path in the model violates the assertions specified.

We prototyped the proposed mechanism using the KLEE symbolic engine and the reference P4 compiler provided by the P4 Language Consortium<sup>1</sup>. Results show that the proposed mechanism is capable of verifying security properties (e.g., header integrity and absence of traffic amplification) in the order of seconds for programs expressing packet processing pipelines with up to 15 tables.

#### 2 VERIFYING P4 PROGRAMS

To better understand the type of problem we are facing, Figure 1 shows an example of what would be a vulnerability in a P4 program. This code snippet specifies a packet processing pipeline containing two tables (*tcp\_table* and *acl\_table*), invoked inside an *ingress* control block. While one can expect *acl\_table* to be applied to both TCP and UDP traffic, UDP packets bypass the filtering.

```
1 control ingress() {
2 apply {
3    ...
4    if (headers.ip.nextHeader == TCP) {
5        tcp_table.apply();
6        // ACL applied over TCP traffic only
7        acl_table.apply();
8    }
9    ...
10 }
11}
```

Figure 1: An example of vulnerability in a P4 program

Assuming that the network security policy disallows this kind of practice, the program in question could be used as a starting point of many attacks. Since current verification approaches do not enable expressing and automatically verifying security properties of P4 programs, we propose a novel mechanism to identify this type of vulnerability.

**Overview**. The key idea consists of verifying models of the original programs annotated with simple assertions. In this sense, Figure 2 shows the verification workflow. First, the P4 program

<sup>&</sup>lt;sup>1</sup>p4.org

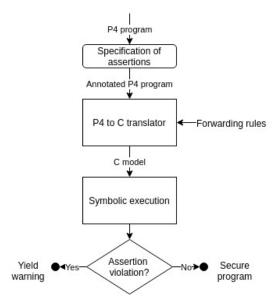


Figure 2: Control flow of the verification process.

developer or network operator annotates the code with assertions to guarantee general properties of interest. These properties can reflect a network security policy or simply represent the expected program behavior. Once annotated, a translation process takes place to automatically generate an equivalent program model using the C language. Optionally, forwarding rules can also be used as inputs to the translator. The generated model is then verified by a symbolic engine, which tests all the program execution paths to find potential failures or vulnerabilities. If no inserted assertion is violated during the symbolic execution process, the P4 program is considered secure regarding the analyzed properties. Otherwise, the respective violation is reported, allowing the developer or operator to correct the program.

**Specifying assertions**. To include assertions in a P4 program, we define a specification language that allows the expression of properties of interest. Our language uses the code annotation mechanism available in P4 through an assert annotation. Each assertion a is composed of an expression e or method  $m \in \{forward, traverse\_path, constant, if, extract\_header, emit\_header\}$ . The set of methods was designed to facilitate the specification of security properties commonly required (e.g., network isolation, header integrity, information flow). Semantically, each assertion represents a boolean that should evaluate to a true or false logical value. In this sense, expressions have the same semantics as their equivalent in the P4 language, while methods have their own semantics. For example,  $traverse\_path$  indicates if a given program section was executed, while constant(f) is true if the header field f is not changed during program execution.

**Constructing C models.** Once a P4 program is annotated, our mechanism generates an equivalent C model through a translating process. The process as a whole involves two steps: i) transforming the P4 program into a directed acyclic graph (DAG); and ii) transforming the generated DAG into C code using the nodes of interest.

We use C structs to model P4 headers and translate the remaining P4 structures (i.e., tables, actions, control blocks, and parsers) to multiple C functions. Each assertion type is modeled in C using a particular approach. In general, we use global boolean values initially set to false. They are assigned to true in different locations of the model depending on the semantics of the assertion used. P4 externs (i.e., device specific functionalities) can be integrated into the translator through libraries.

Symbolically executing program models. The generated C model is verified by a symbolic engine, which will traverse all possible paths in the program while treating the incoming packet headers as symbolic values. The number of execution paths is essentially given by the number of tables and actions used. Whenever a table can only be accessed under some condition (e.g., depending on some specific protocol), a new execution path is created. The same happens whenever multiple actions can be invoked by a given table.

#### 3 PRELIMINARY EVALUATION

The main goals of our experiments are: i) answering if it is possible in fact to find vulnerabilities in P4 programs using the proposed mechanism; and ii) assess how much time it takes to verify properties of interest according to different program characteristics. We have prototyped the proposed mechanism using Klee (version 1.3.0) as the symbolic engine. To build C models, we first convert a P4 program to its JSON representation using the reference compiler provided by the *P4 Language Consortium*, then we translate the JSON model (represented as a DAG) to C code using a translator implemented specifically for this purpose. The translator source code, as well as the scripts and workloads used are available online <sup>2</sup>. All the experiments were performed using a linux virtual machine (kernel version 4.8.0) with one 3 GHz core and 16 GB of RAM.

# 3.1 Security analysis

To demonstrate the potential of the proposed verification mechanism as a defense tool against network attacks originated from vulnerabilities in P4 programs, we have selected the DC.p4 program as a case study. DC.p4 was proposed by [4], and captures the behavior of a data center switch. We have chosen this program due to its complexity and representativeness of real scenarios. We have verified it with the goal of finding faults that can be explored by potential attacks.

**Code circumvention.** The first set of performed experiments are related to the verification of failures in security functionalities implemented by the program (e.g., VLAN and ACL to provide network isolation). We verify if it is possible to circumvent any of these functionalities in such a manner as to avoid that its corresponding security policies is in fact applied over a packet. We used assertions using the format  $if(bypass == 0, traverse\_path)$  to express this type of property, where bypass == 0 indicates that the security functionality is active (i.e., it was configured by the administrator), and  $traverse\_path$  verifies if the functionality is being executed for every possible input.

2

<sup>&</sup>lt;sup>2</sup>https://github.com/ufrgs-networks-group/assert-p4

Given the program complexity, the full verification process takes in the order of days to finish (see Figure 3(a)). However, our verifier was capable of finding violations of the tested properties in less than 10 seconds. We have found that even though the program supports multiple options of access control lists (e.g., level 2 and 3 lists), IPv4 packets do not go through a level 2 list, which can be transformed in a vulnerability depending on the configuration adopted by the network administrator (e.g., if the administrator decides to only configure a level 2 ACL).

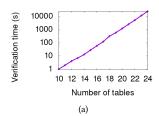
**Traffic amplification.** Some functionalities of the DC.p4 program (e.g., packet mirroring) involve replication of traffic among the device ports or its CPU. Both cases are usually part of a network monitoring task. If wrongly executed, such procedures can serve as a source of attacks based on packet replication or traffic amplification [2]. In this regard, we verify if the replication procedures in the DC.p4 program are correctly executed. We have used assertions in the format! (outport == original\_port && constant(outport)) inside actions that replicates the traffic to express these properties. The first and second members of these assertions test if the replicated packet outgoing port is the same as the original packet and if such port is changed during the replicated packet processing, respectively.

As in the previous case, our mechanism was able to find properties violations quickly (less than 13 minutes). Overall, the program is general enough to allow the control plane to configure the outgoing port of both the original packet and its replica, while not applying any modification to the replica headers. Thus, it is the control plane responsibility to ensure that the device table configuration does not cause a single packet to be forwarded multiple times to the same destination, which could be a complicated task.

#### 3.2 Performance

In this section, we assess how our proposed verification mechanism scales according to P4 program characteristics. To this end, we used the Whippersnapper [1] programmable data planes benchmark to synthetically generate P4 programs with different pipeline sizes (i.e., number of tables). Additionally, we tested the impact of varying the quantity of actions associated with each table. We consider for these cases a program with only two tables (with the goal of minimizing this factor in the verification time), using scripts to insert new actions and assertions.

As expected, the verification time grows exponentially to variations applied to the number of tables. Pragmatically, however, it takes around a minute to check programs containing pipelines with up to 15 tables (Figure 3(a)), which includes various current P4 programs. We can also observe that the execution time grows polynomially with relation to varying the number of actions (Figure 3(b)). In this case, the degree of the polynomial depends on the quantity of tables of the P4 program (e.g., two for the tested program). In practice, given a program containing p sequential tables, where each table can invoke one among q different actions, a total of  $q^p$  distinct paths should be symbolically executed. Pragmatically, P4 programs do not usually have more than a dozen actions in each table, which makes the proposed mechanism feasible for the majority of existing instances.



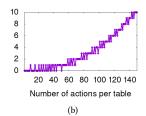


Figure 3: Performance analysis of the proposed mechanism.

# 4 CONCLUSION

We present an assertion language that can be used by P4 programmers to express correctness and security properties of a specific implementation. Our solution is more expressive than other data plane verification approaches, being the first work to allow proving properties specific to P4 source code and optionally the forwarding rules used by its tables. We evaluated our approach by proving properties of traffic replication and security functionalities circumvention in the DC.p4 program. Its performance analysis revealed that despite the efficiency in verifying small programs, the execution time grows rapidly with relation to the number of tables and actions. Therefore, we are working to extend SEFL [6], a language designed to be symbolically executed, to enable the full modeling of P4 programs. We envision that SEFL and its symbolic engine would enable scalable verification of assertions in P4 programs.

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3