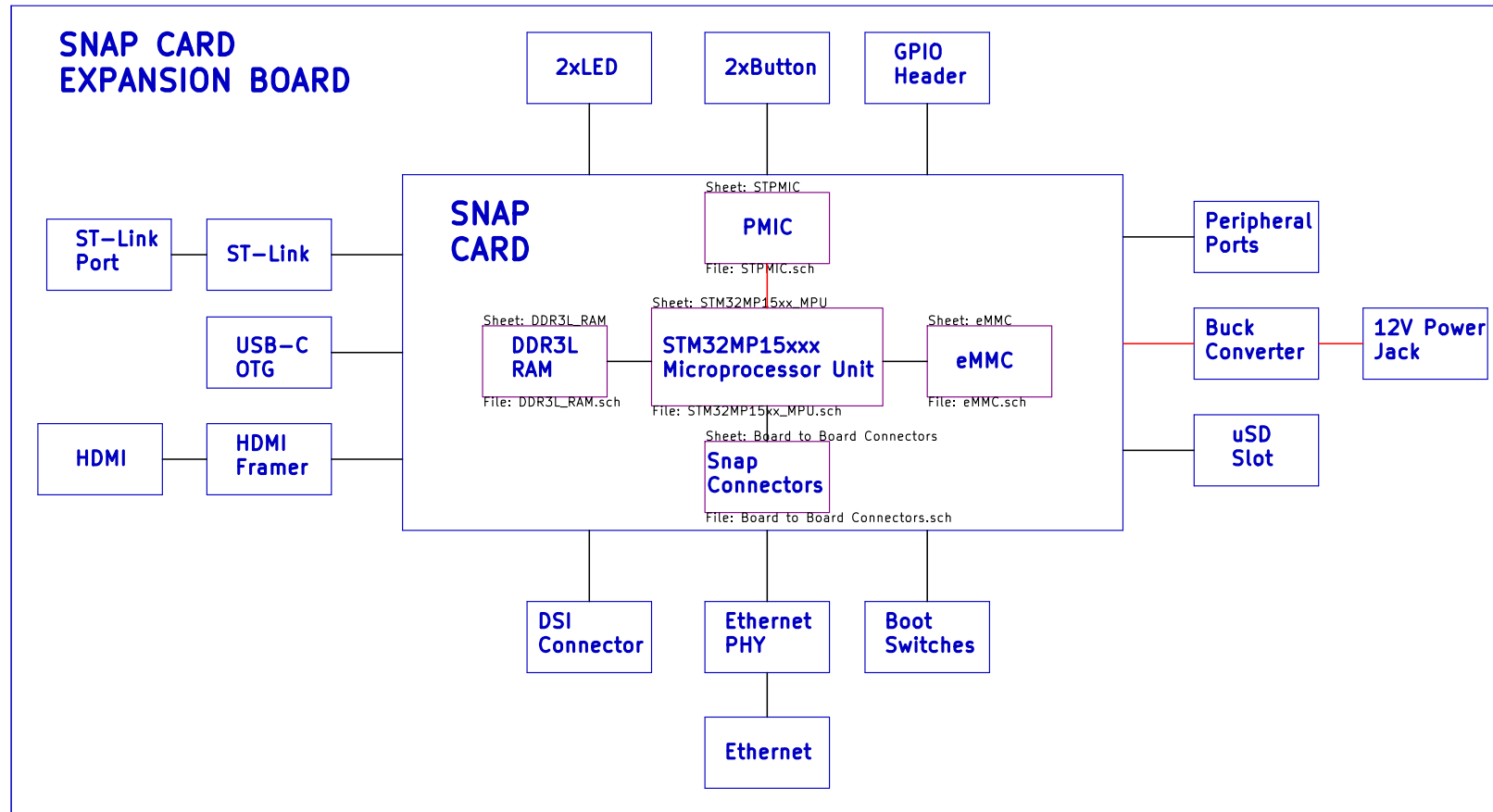


SNAP PROJECT OVERVIEW



All Features

- STM32MP157 Dual Core A7 + M4 MPU
- ST PMIC
- 4-Gbit DDR3L, 16 bits, 533MHz
- 1-Gbps Ethernet(RGMII) compliant with IEEE-802.3ab
- USB OTG HS
- 4 User LEDs
- 2 User Push Buttons
- 12 V DC input Power Jack
- 1xEthernet RJ454 Connector
- 2x USB-A Connectors
- 1xMIPI-DSI Connector
- RPI Compatible GPIO Header
- On-board ST-Link/V2-1 debugger/Programmer
- WiFi 802.11b/g/n
- Bluetooth Low Energy 5.0

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Title: SNAP PROJECT OVERVIEW

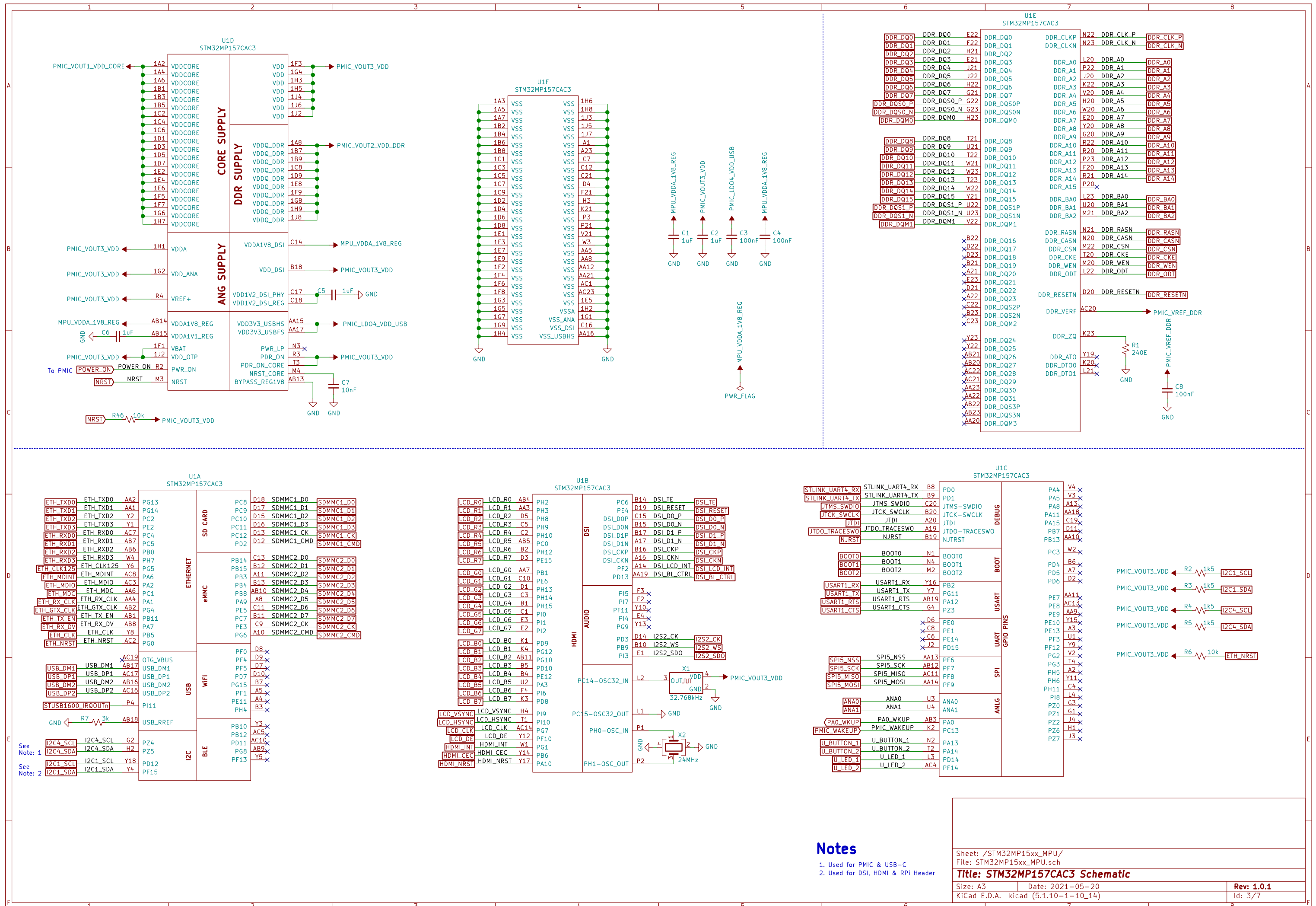
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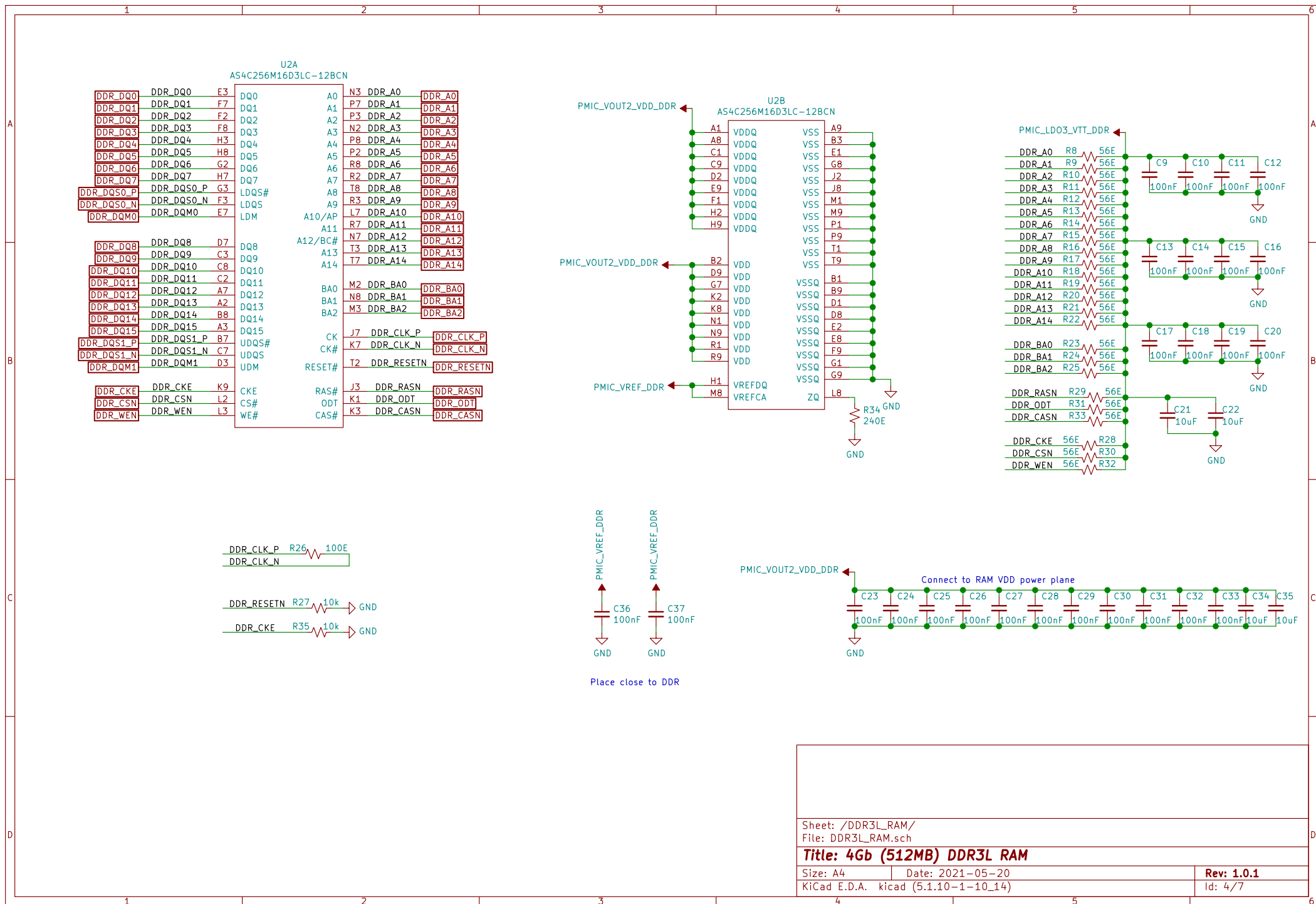
Rev: 1.0.1
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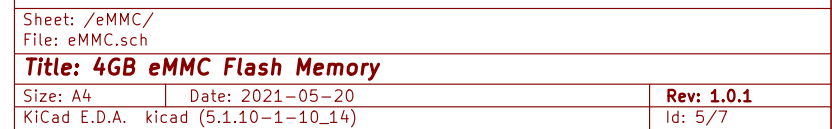
Table of Contents

- Sheet 1: Project Overview
- Sheet 2: Snap Card block diagram / Table of contents (this page)
- Sheet 3: STM32MPU Power

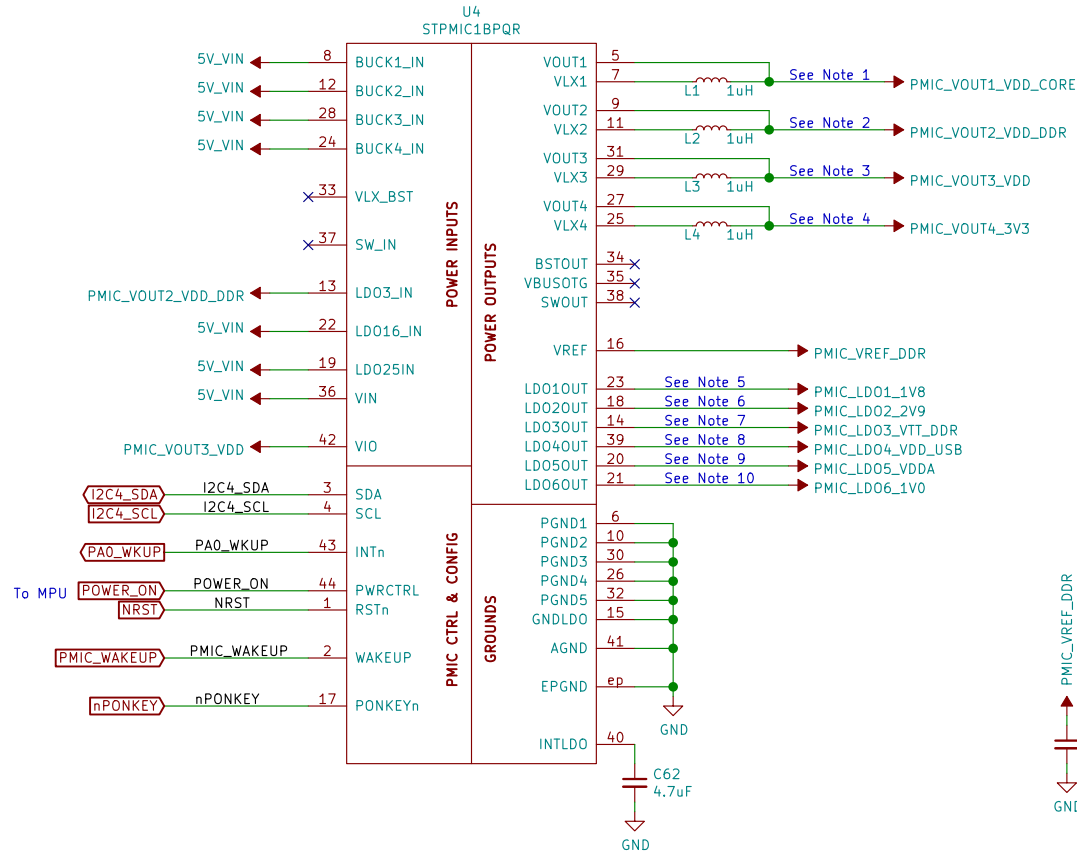
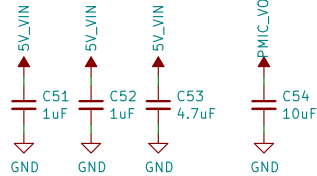
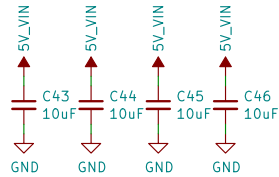
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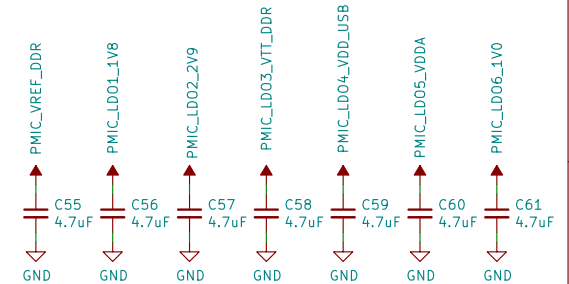
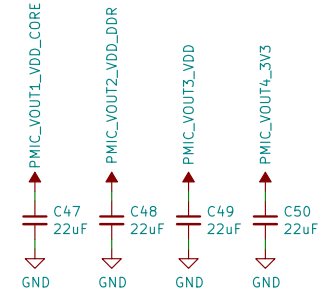




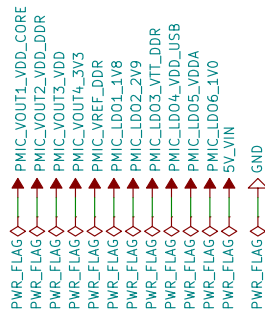
Input Power Caps



Output Power Caps



ERC Power Flags



Notes

- Default = 1.2V; I_{max} = 2000mA
- Default = 1.1V; I_{max} = 1600mA; NOT ON BY DEFAULT
- Default = 1.8V; I_{max} = 1000mA
- Default = 3.3V; I_{max} = 3000mA
- Default = 1.8V; I_{max} = 800mA; NOT ON BY DEFAULT.
- Default = 2.9V; I_{max} = 800mA
- Default = 1.8V; I_{max} = 150mA; NOT ON BY DEFAULT
- Default = 3.3V; I_{max} = 200mA
- Default = 2.9V; I_{max} = 800mA
- Default = 1.0V; I_{max} = 350mA; NOT ON BY DEFAULT

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File: STPMIC.sch

Title: ST Power Management IC

Size: A4 Date: 2021-05-20
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Rev: 1.0.1
Id: 6/7

