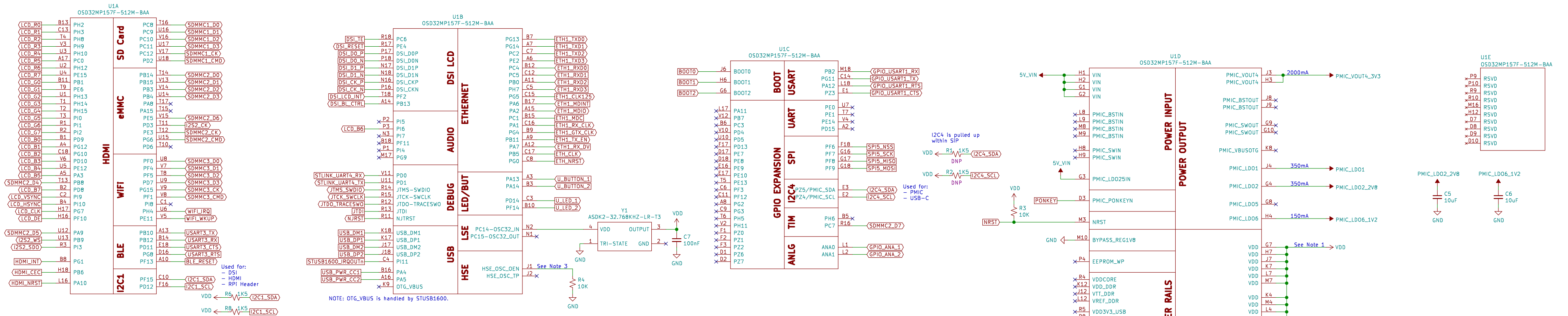
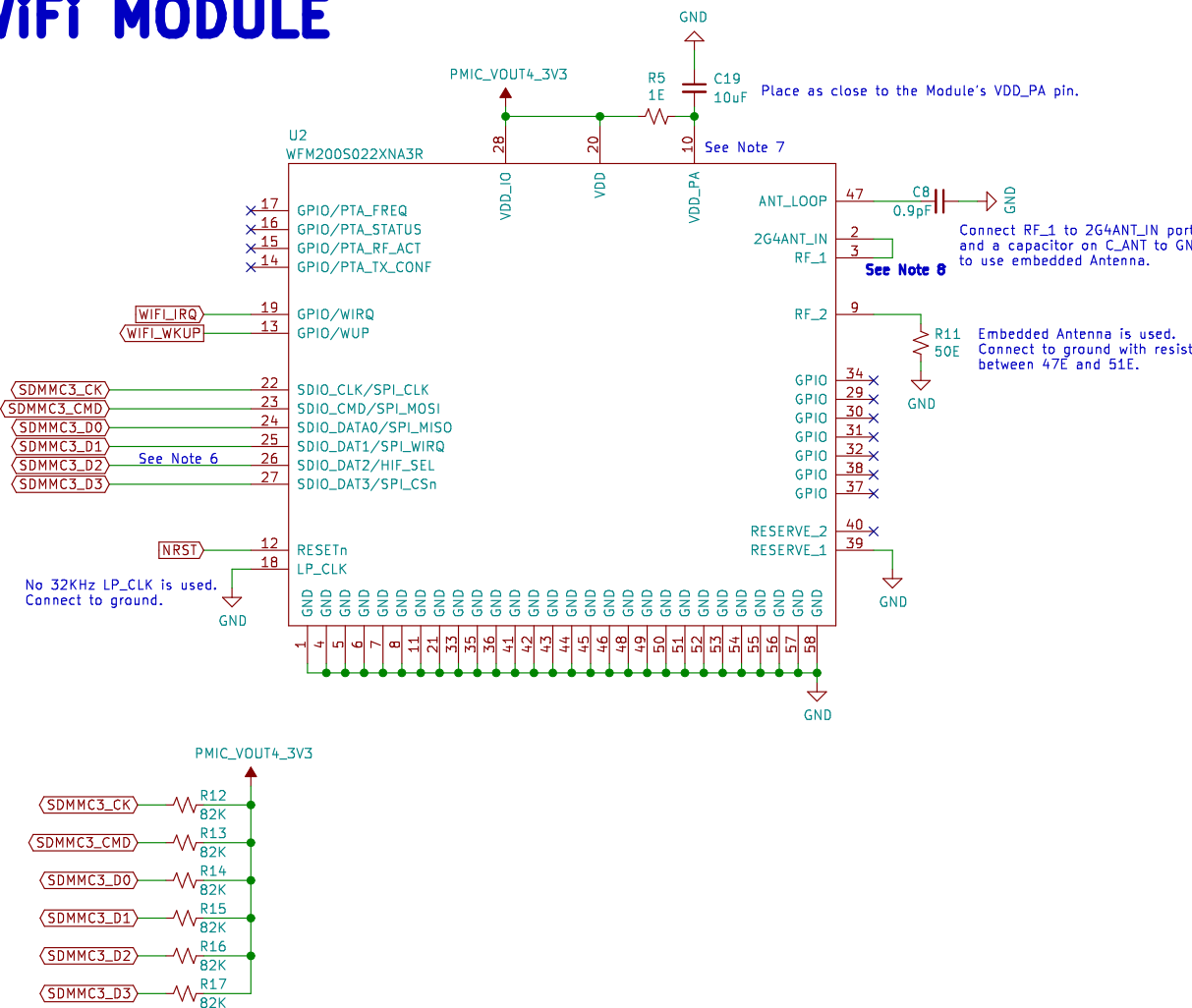


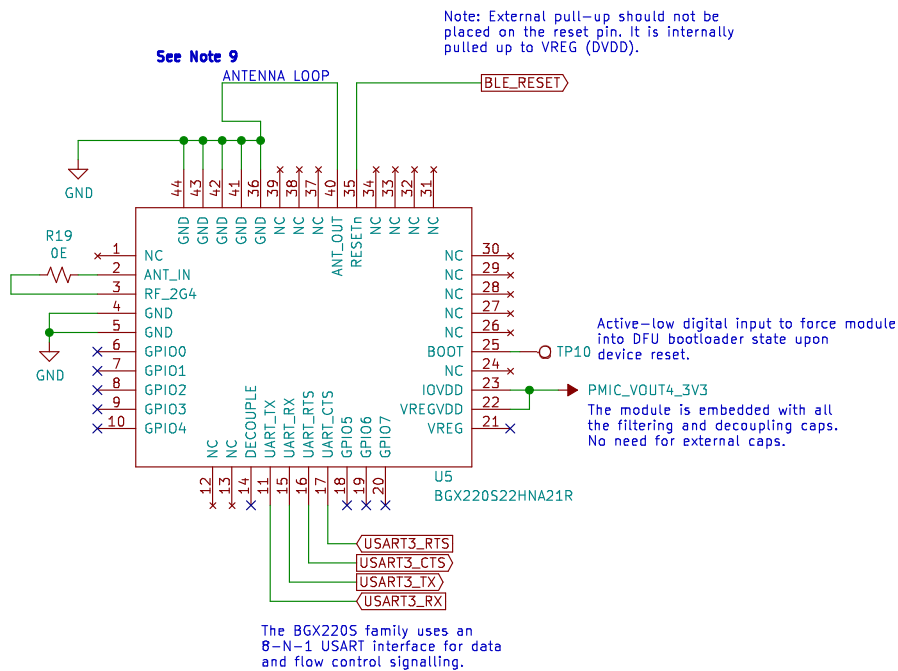
OSD32MP157F SIP BASED ON STM32MP157FAC1 – TFBGA361



WiFi MODULE



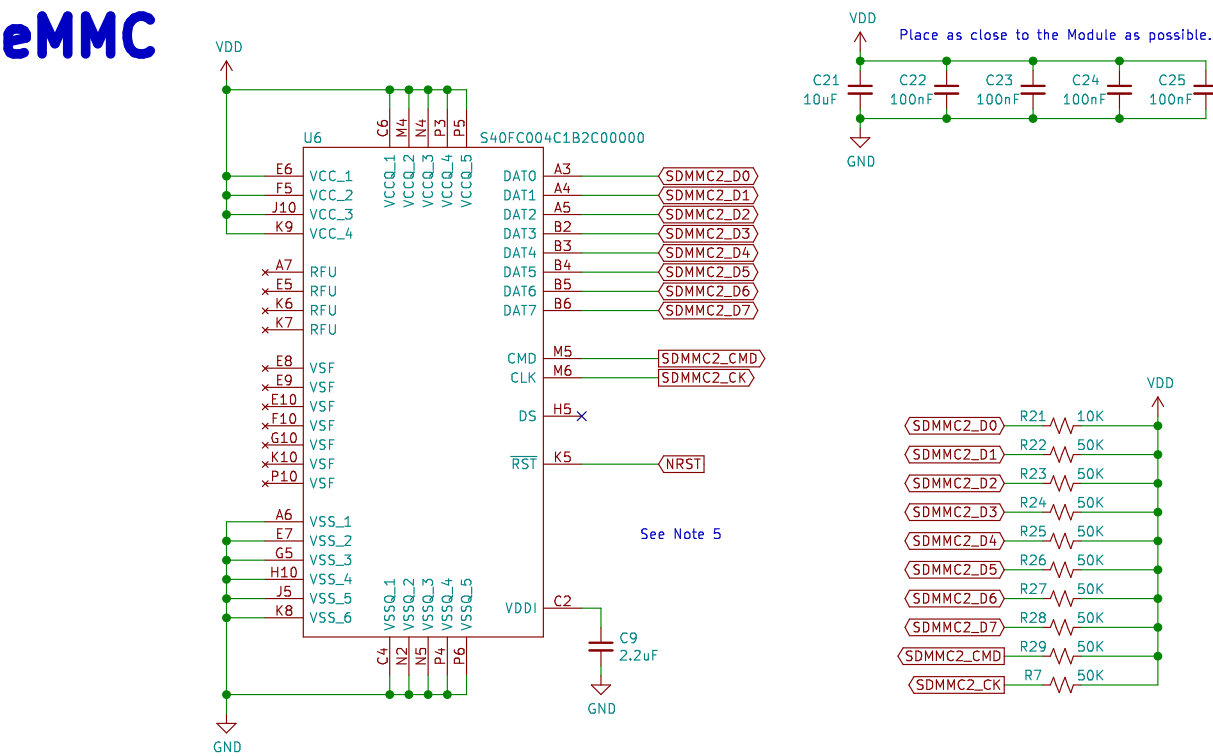
BLUETOOTH MODULE



NOTES

- Note 1:** Connection between two sets of VDD pins is not required. It is for easy layout purpose.
- Note 2:** By default VBAT is connected to VDD. The VBAT power domain contains RTC, Backup Registers, Retention RAM and Backup SRAM. This domain is automatically supplied by VDD to optimize battery life when PDR_ON is enabled.
- When VDD is not available (i.e. when VDD drops below PDR threshold level), VBAT power domain is sourced from VBAT pin which can receive power from a battery or externally.
- When PDR_ON is connected to GND, VBAT functionality is no longer available and VBAT must be connected to VDD externally.
- Note 3:** Internal pull down on HSE_OSC_DEN pin. Recommended to include external resistor.
- Note 4:** VDDA is a power output (PMIC_LD05). VREF+ can be connected to another voltage reference. If desired to change ADC voltage range. If unused, leave VDDA floating. Connect VREF+ to VREF- to VSSA.
- Note 5:** Do not route through RFU/VSF pins. All other pins are NC and can be routed through.
- Note 6:** Logic state of SDIO_DATA2/HIF_SEL pin during the rising edge of the RESETn signal should be HIGH to enable SDIO interface.
- Note 7:** VDD_PA is power amplifier supply. It should be capable of supplying 200mA momentarily. It is recommended to connect to regulator capable of supplying 300mA.
- Note 8:** Follow layout recommendations from UG955: WFM200 Hardware Design User's Guide.
- Note 9:** Follow layout recommendations from BGX2205 Wireless Datasheet design guidelines.

eMMC



SNAP CONNECTORS

