

[illegible]

```

ldi r21, 0b00000101
out TCCR0B, r21

ldi r21, (1<<TOIE0)
sts TIMSK0, r21                ; Store Direct To SRAM
ldi r21, 240
out TCNT0, r21

sei                            ; Enable Global Interrupts
ser r17                        ; Set Register 17
out DDRB, r17

ldi r18, 0

;-----;
LOOP:
    nop                        ; No Operation
    rjmp LOOP

;-----;
TIMER:
    com r17                    ; Ones Compliment
    out PORTB, r17
    reti

```