

[illegible]

```
cp r16, r17          ;Compare the r17 SWITCH input to the r16 Register.  
  
;Branch Equals  
breq SW5isPressed  
rjmp switch_loop     ;Jump out of loop.  
  
;If SW5 is Pressed then activate Register 18 and turn LED 0 on.  
SW5isPressed: out portB, r18
```