

[illegible]

```

        ldi r17, (1<<TXEN1)|(1<<RXEN1)
        sts UCSR1B, r17

;-----;
GET_CHAR:
        lds r17, UCSR1A                ; Load Direct from SRAM
        sbrs r17, RXC1                ; Skip if Bit in Register is Set

        rjmp GET_CHAR

        lds r16, UDR1

;-----;
PORT_OUTPUT:
        com r16                        ; Ones Compliment
        out PORTB, r16
        com r16

;-----;
PUT_CHAR:
        lds r17, UCSR1A
        sbrs r17, UDRE1

        rjmp PUT_CHAR

        sts UDR1, r16

        rjmp GET_CHAR

;SPEED : 4800

```