

[illegible]

```

    out DDRB, r17

    ldi r17, 0x55
    out PORTB, r17

    ldi r17, 12
    sts UBRR1L, r17                ; Store Direct To SRAM

    ldi r17, 0b10011000
    sts UCSR1B, r17

    sei                            ; Enable Global Interrupts

MAIN:
    nop                            ; No Operation
    rjmp MAIN

GET_CHAR:
    lds r20, UCSR1A                ; Load Direct from SRAM
    lds r16, UDR1

PORT_OUTPUT:
    com r16                        ; Ones Complement
    out portb, r16
    com r16

PUT_CHAR:
    lds r20, UCSR1A
    sts UDR1, r16

    reti                            ; Interrupt Return

```