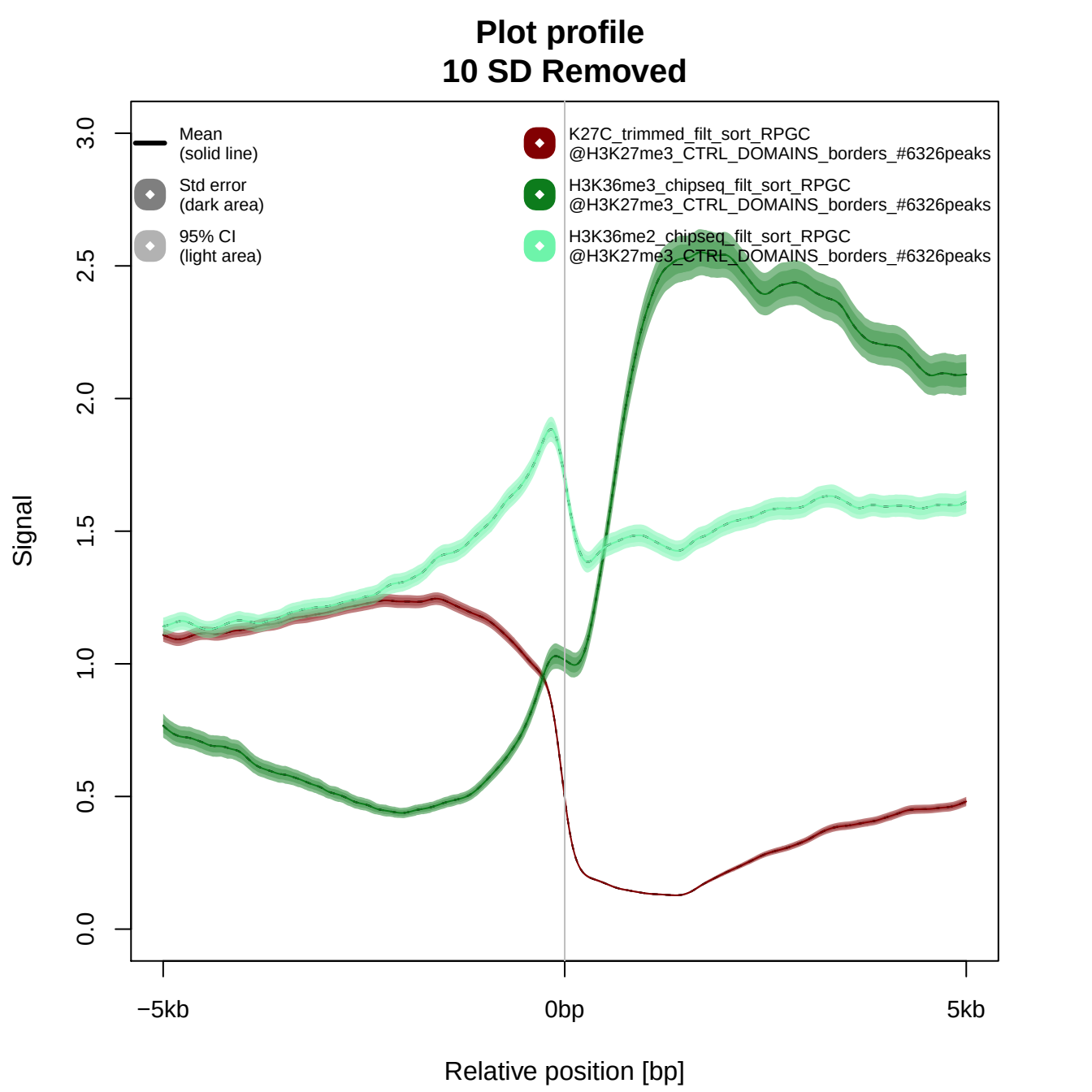
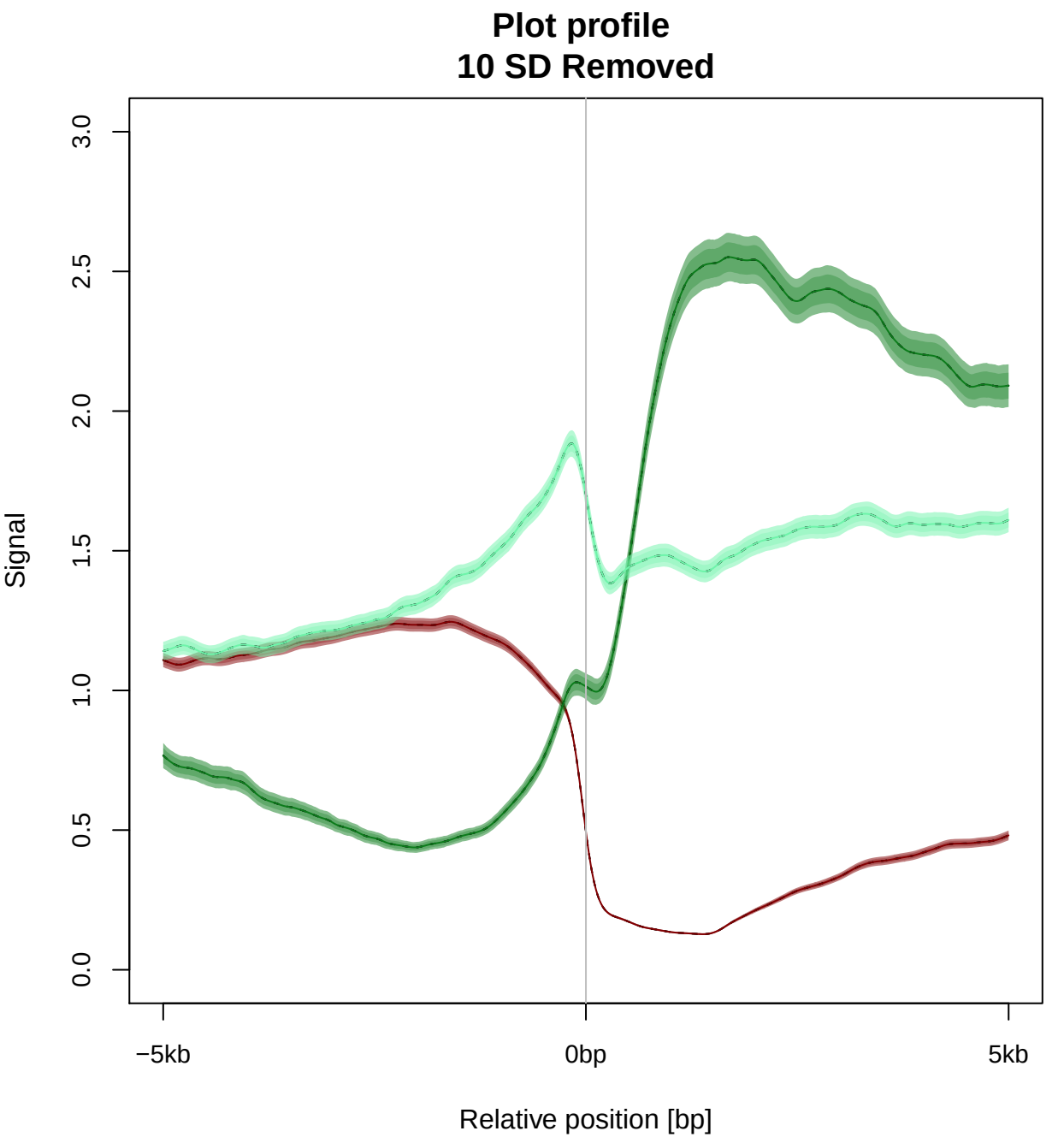

Plot profile
10 SD Removed
Mean
(solid line)
Std error
(dark area)
95% CI
(light area)
K27C_trimmed_filt_sort_RPGC
@H3K27me3_CTRL_DOMAINS_borders_#6326peaks
H3K36me3_chipseq_filt_sort_RPGC
@H3K27me3_CTRL_DOMAINS_borders_#6326peaks
H3K36me2_chipseq_filt_sort_RPGC
@H3K27me3_CTRL_DOMAINS_borders_#6326peaks
Signal
0.0
0.5
1.0
1.5
2.0
2.5
3.0
−5kb
0bp
5kb
Relative position [bp]


Plot profile
10 SD Removed
Signal
0.0
0.5
1.0
1.5
2.0
2.5
3.0
−5kb
0bp
5kb
Relative position [bp]