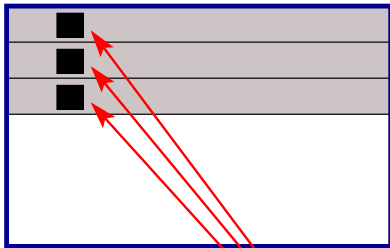


Cache



n ways to map
a memory address

RAM

