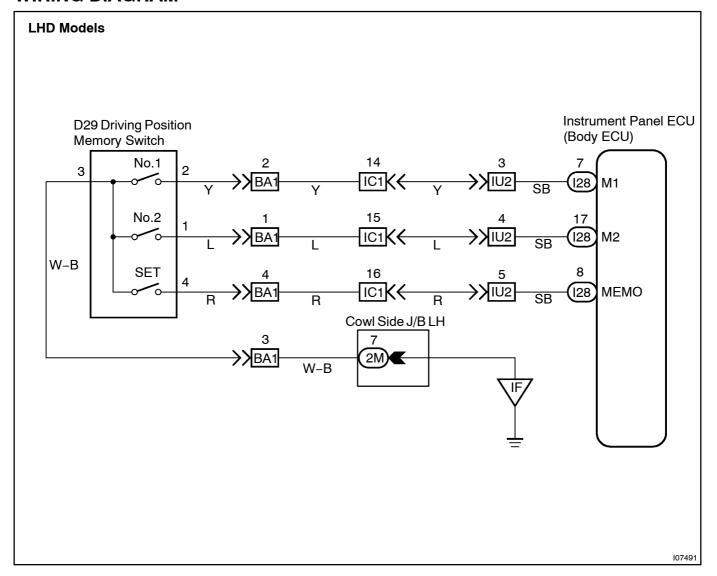
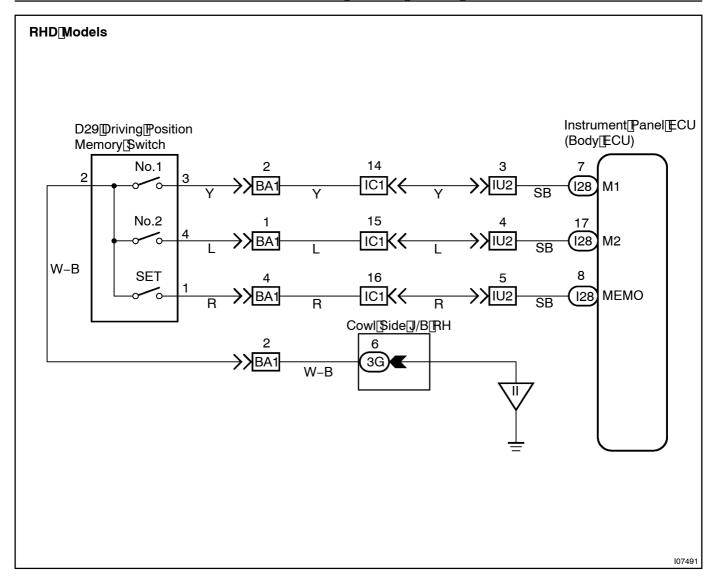
DI3N1-02

Memory switch circuit (W/ memory)

WIRING DIAGRAM





INSPECTION PROCEDURE

1 Check memory switch. (See page BE-130)

NG

Replace the memory switch.

OK

2□

 $\label{lem:connector_between_memory} \textbf{$\widehat{\textbf{g}}$ nd $\widehat{\textbf{g}}$ connector $\widehat{\textbf{b}}$ etween $\widehat{\textbf{g}}$ memory $\widehat{\textbf{s}}$ witch $\widehat{\textbf{g}}$ nd $\widehat{\textbf{g}}$ ody $\widehat{\textbf{E}}$ CU.$

NG□

Repair or replace wireharness or connector.

OK

Proceed@o@next@ircuit@nspection@shown@n problem@symptoms@able. (See_page_DI-773)