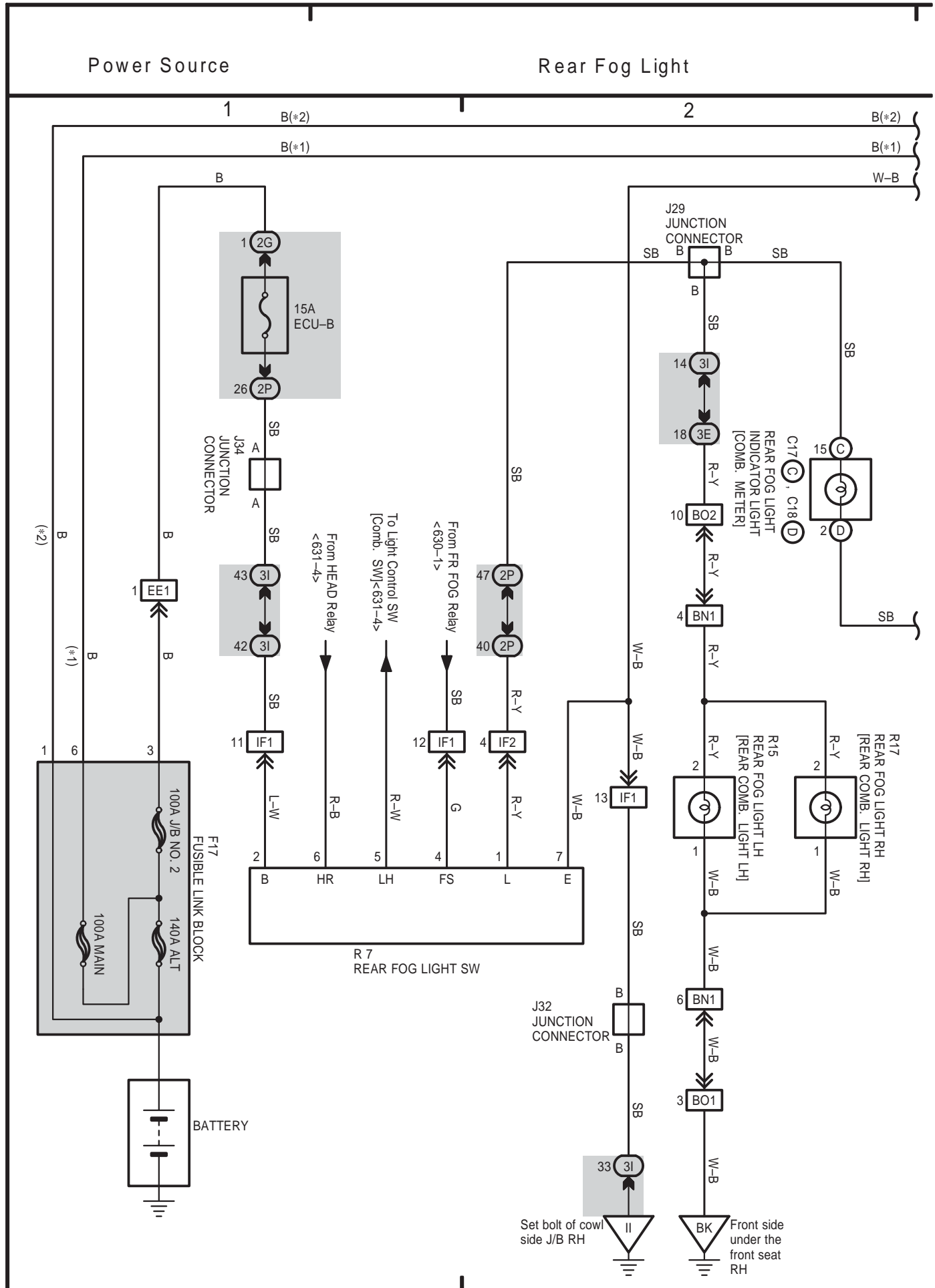


F ELECTRICAL WIRING DIAGRAM (System Circuits)



- * 1 : 1HD-FTE
- * 2 : Except *1

