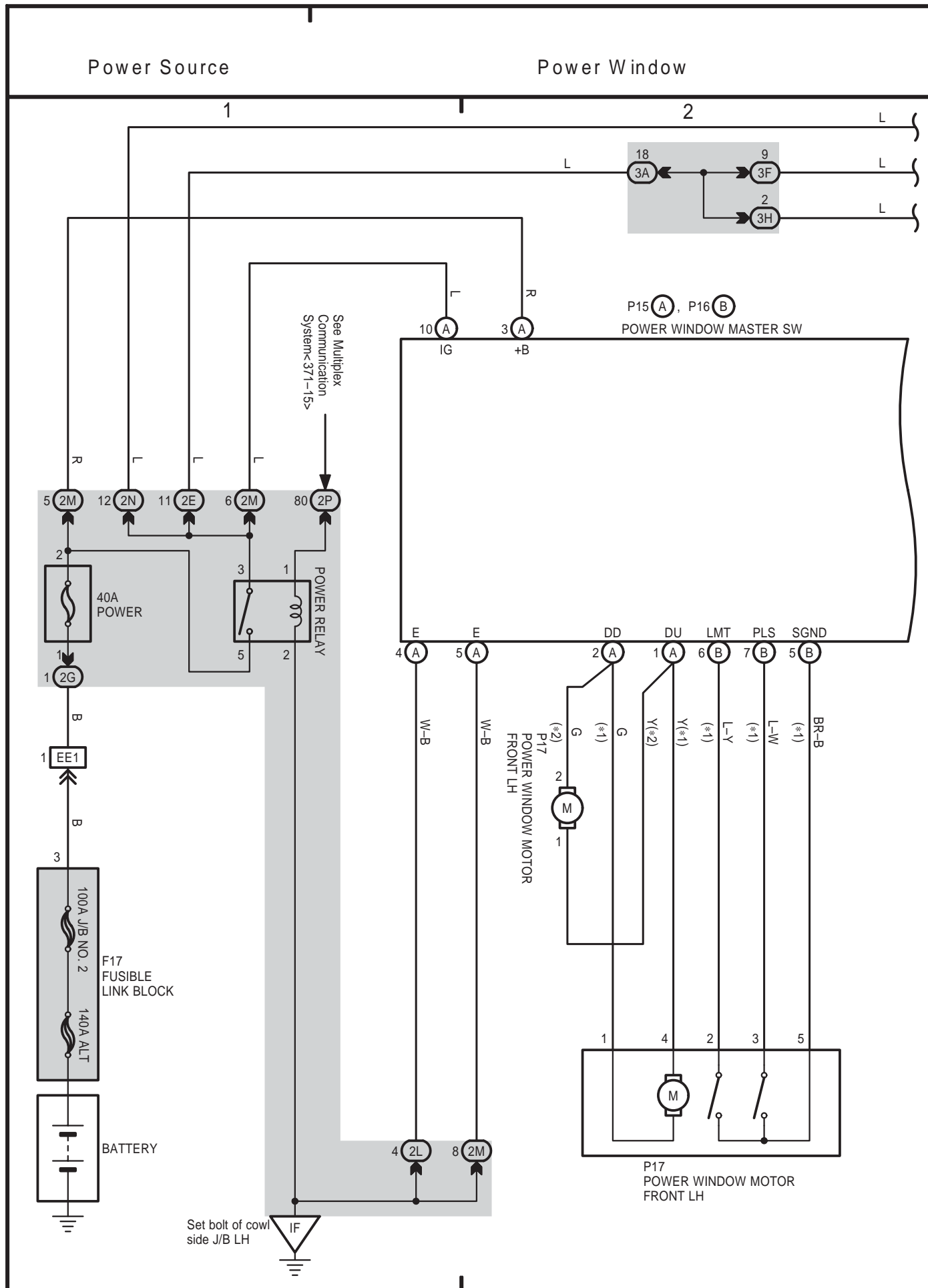


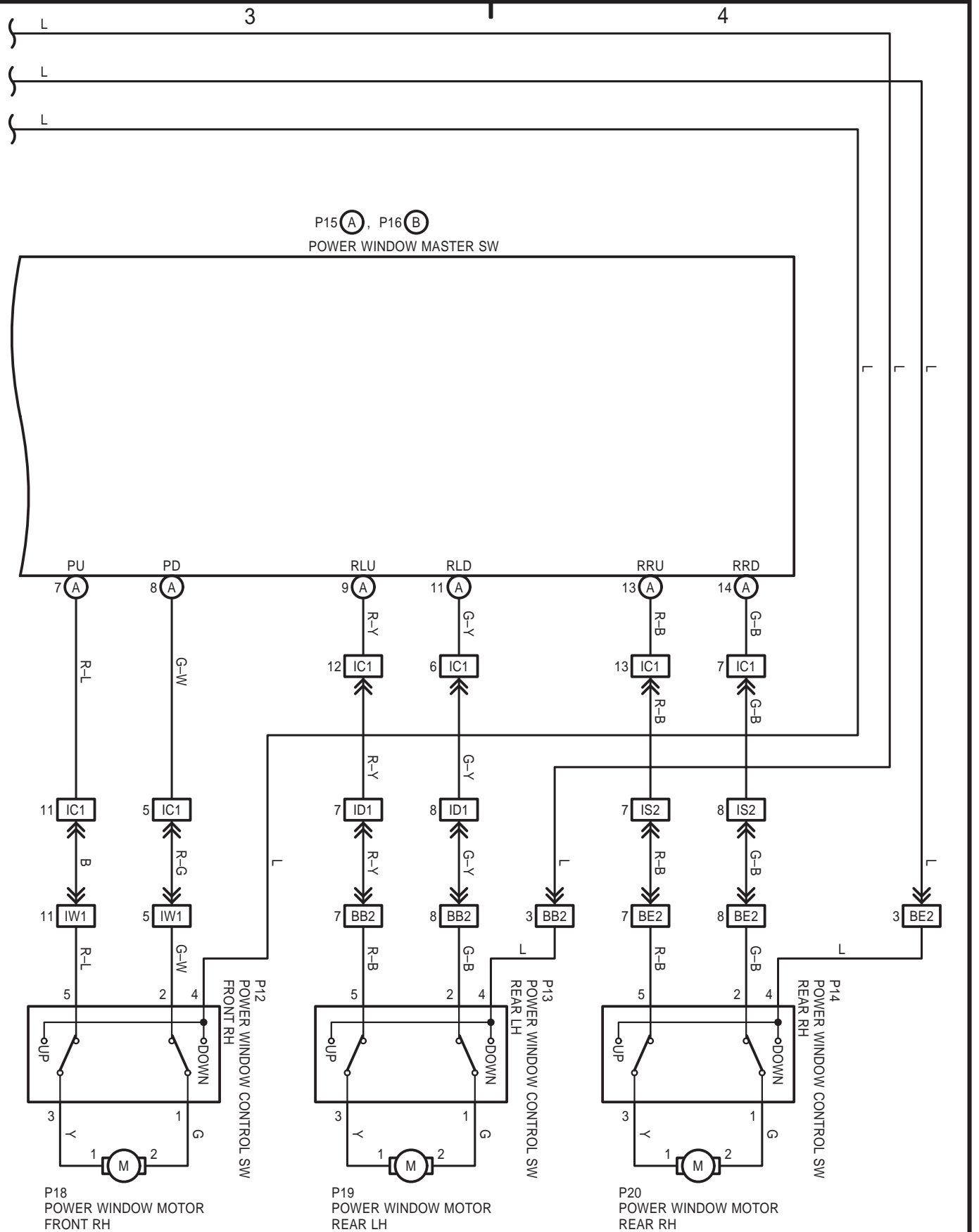
F ELECTRICAL WIRING DIAGRAM (System Circuits)



LHD

Power Window

* 1 : w/ Jam Protection
* 2 : w/o Jam Protection



LHD