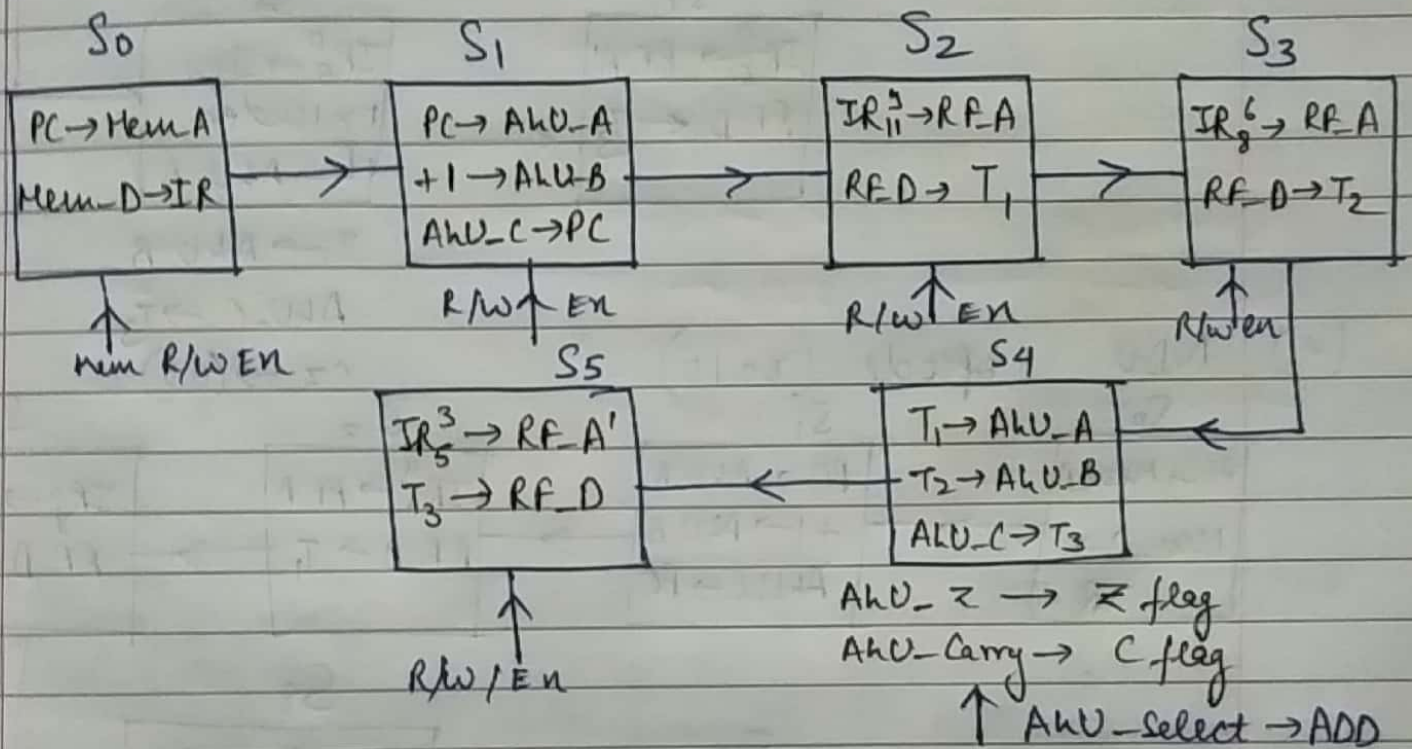


Total registers $\rightarrow 8$ (GPID) $R_0 - R_7$

Temporary registers $\rightarrow 3$ T_1, T_2, T_3

(i) ADD opcode (0000) $CZ \rightarrow 00$



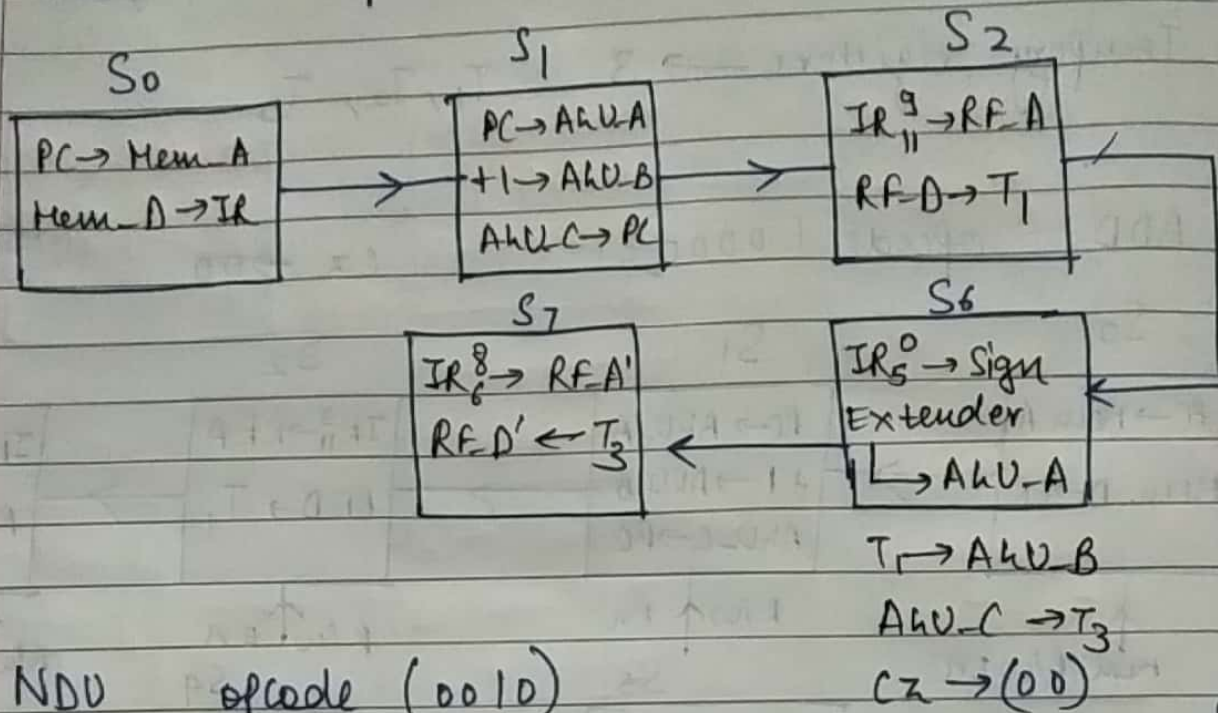
(ii) ADC opcode (0000) $CZ \rightarrow 10$

Same as above except that it is executed only if carry flag is set, otherwise only PC is updated

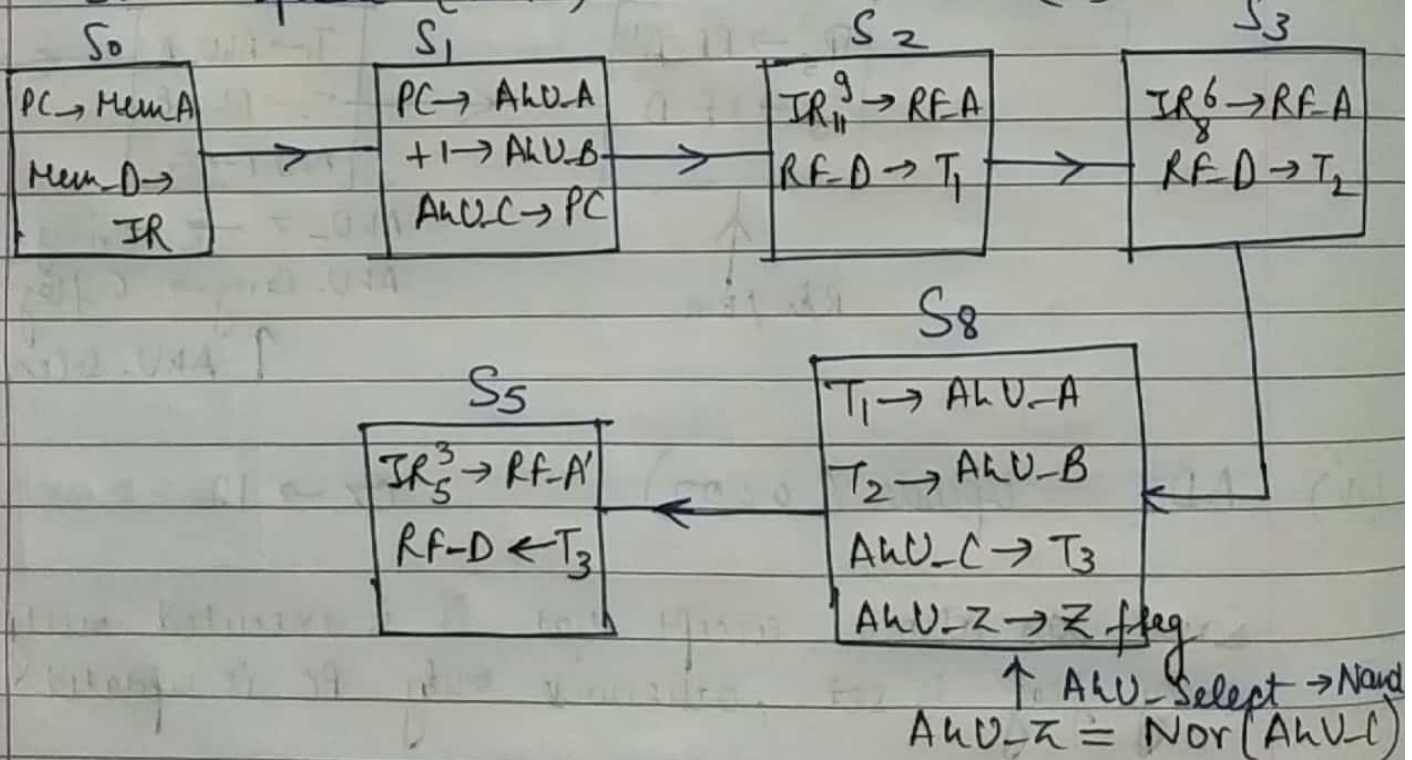
(iii) ADZ opcode (0000) $CZ \rightarrow 01$

Same as above except that it is executed only if zero flag is set, otherwise only PC is updated

(iv) ADI opcode (0001)



(v) NDU opcode (0010)



(vi) NDC opcode (0010)

CZ → 10

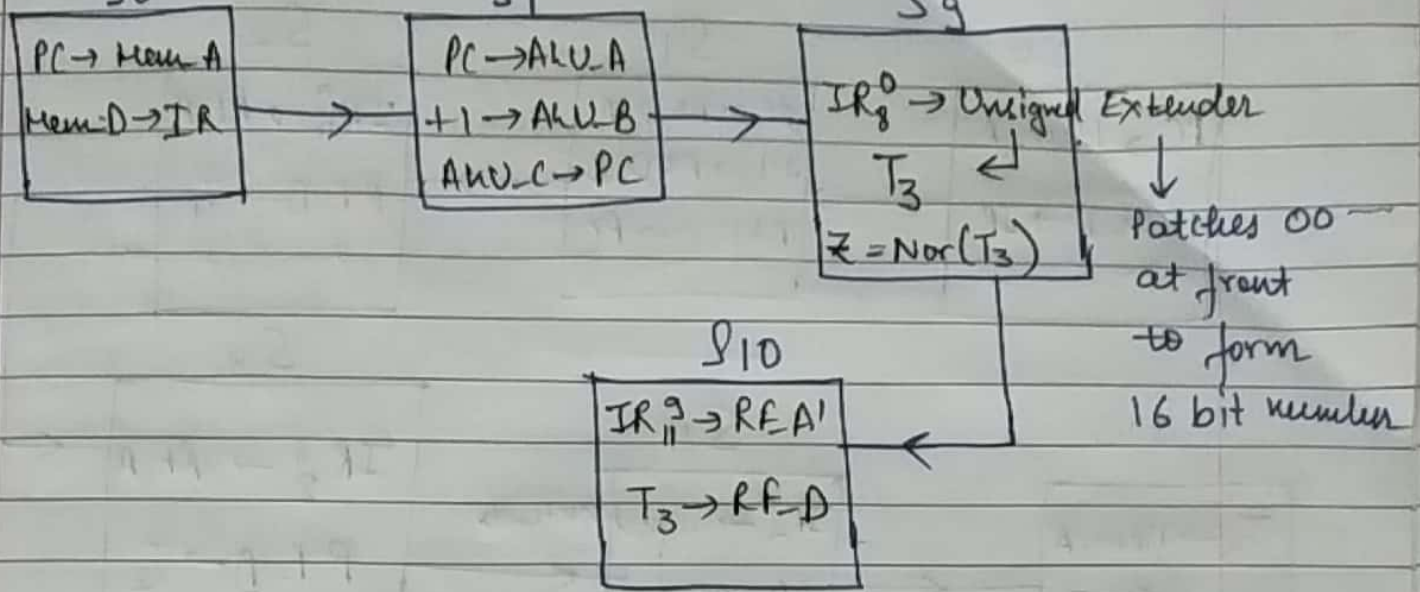
Same as above except that it is executed if carry flag is set else only PC is updated

(vii) NDZ opcode (0010)

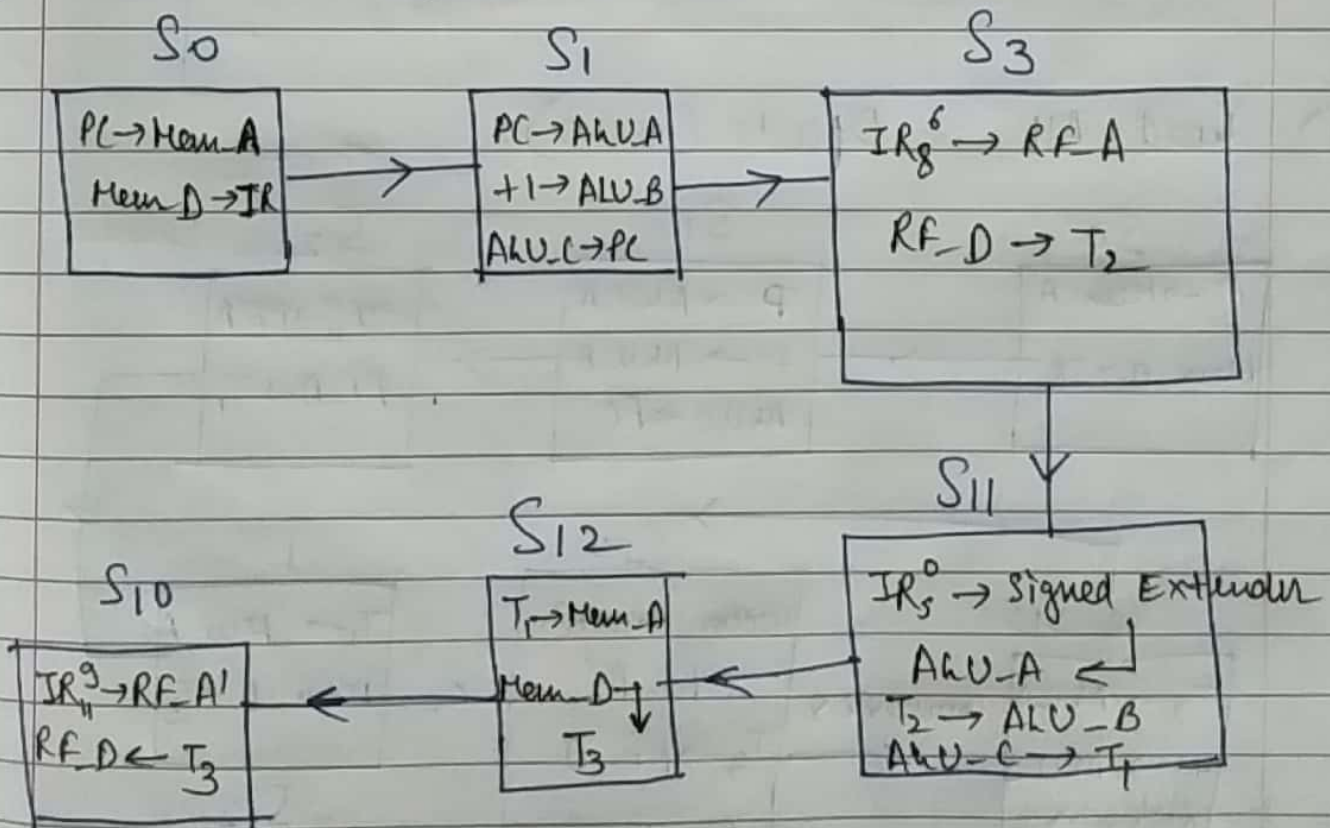
CZ → 01

executed only when zero flag is set.

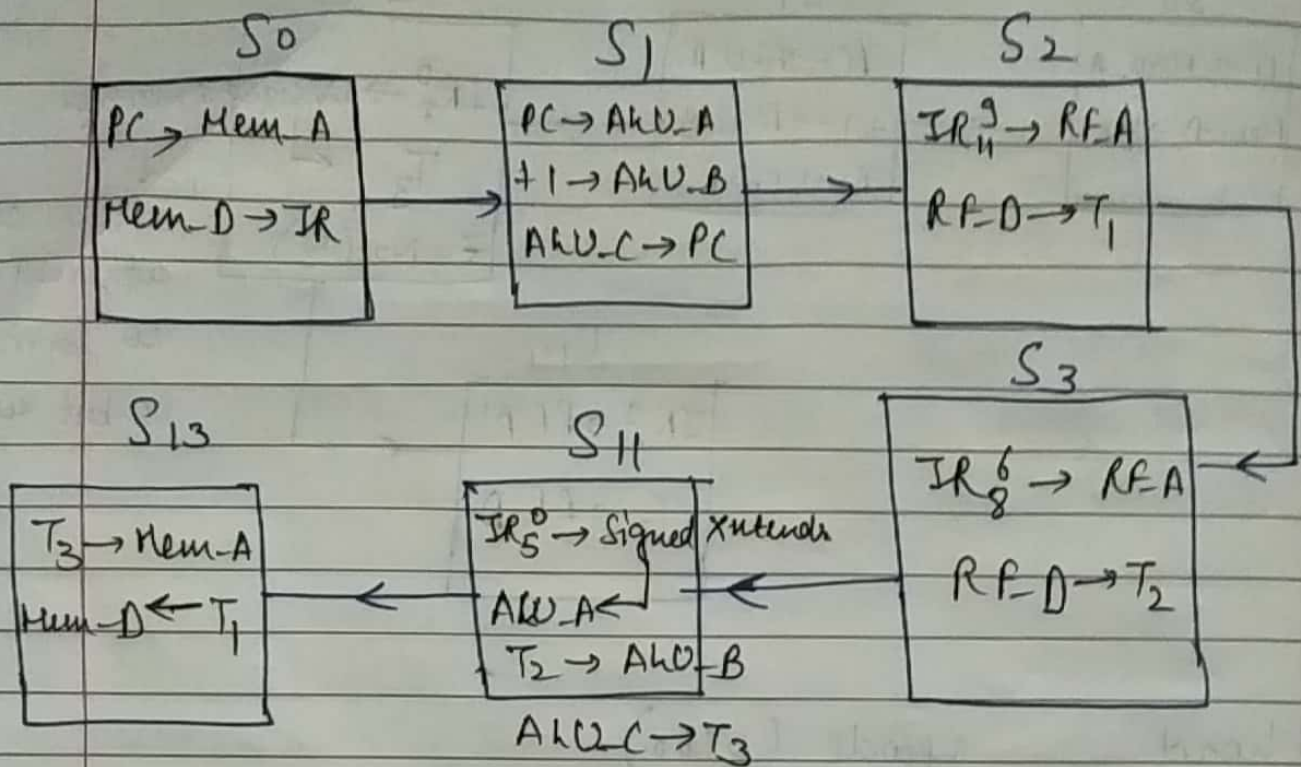
(viii) LHI opcode (0011)



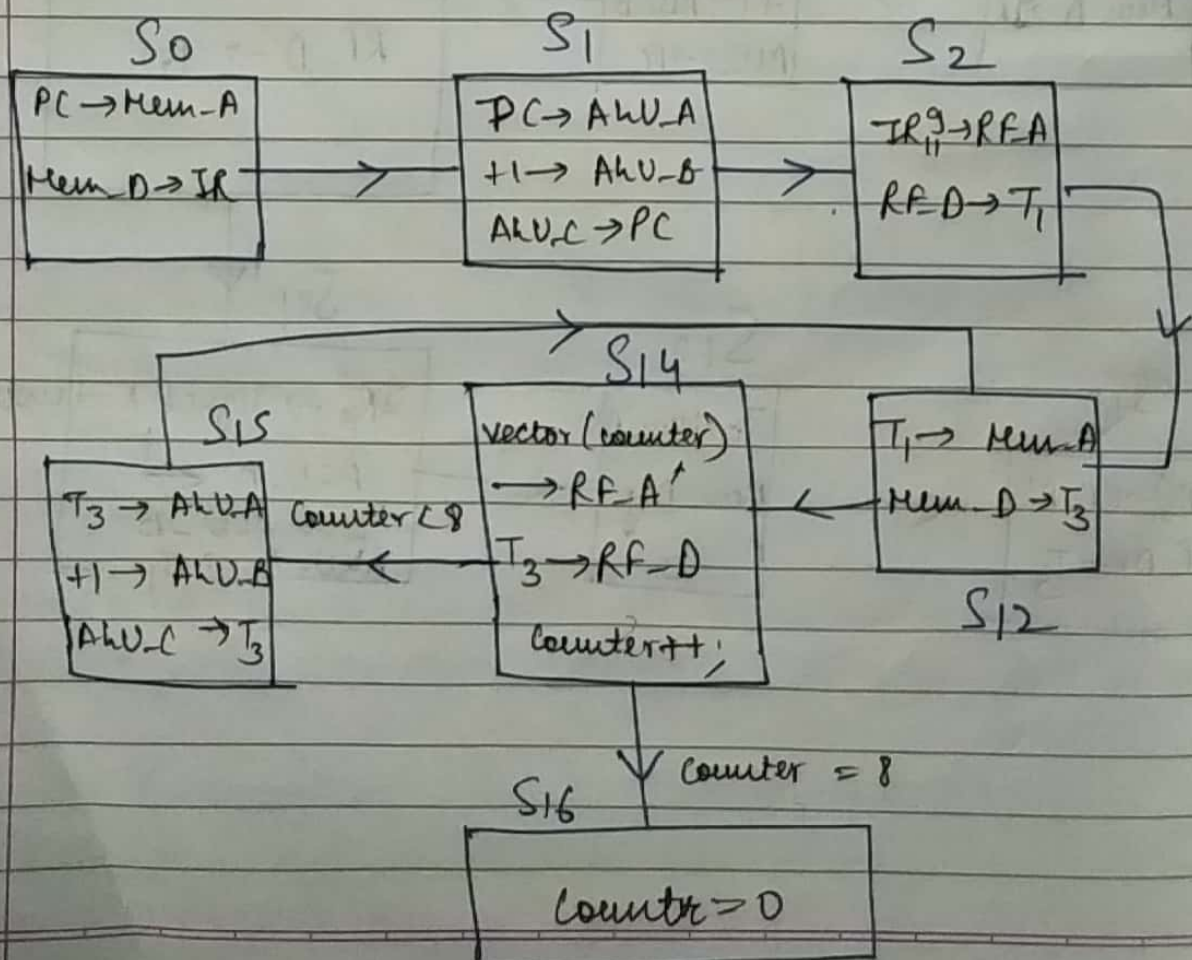
(ix) load opcode (0100)



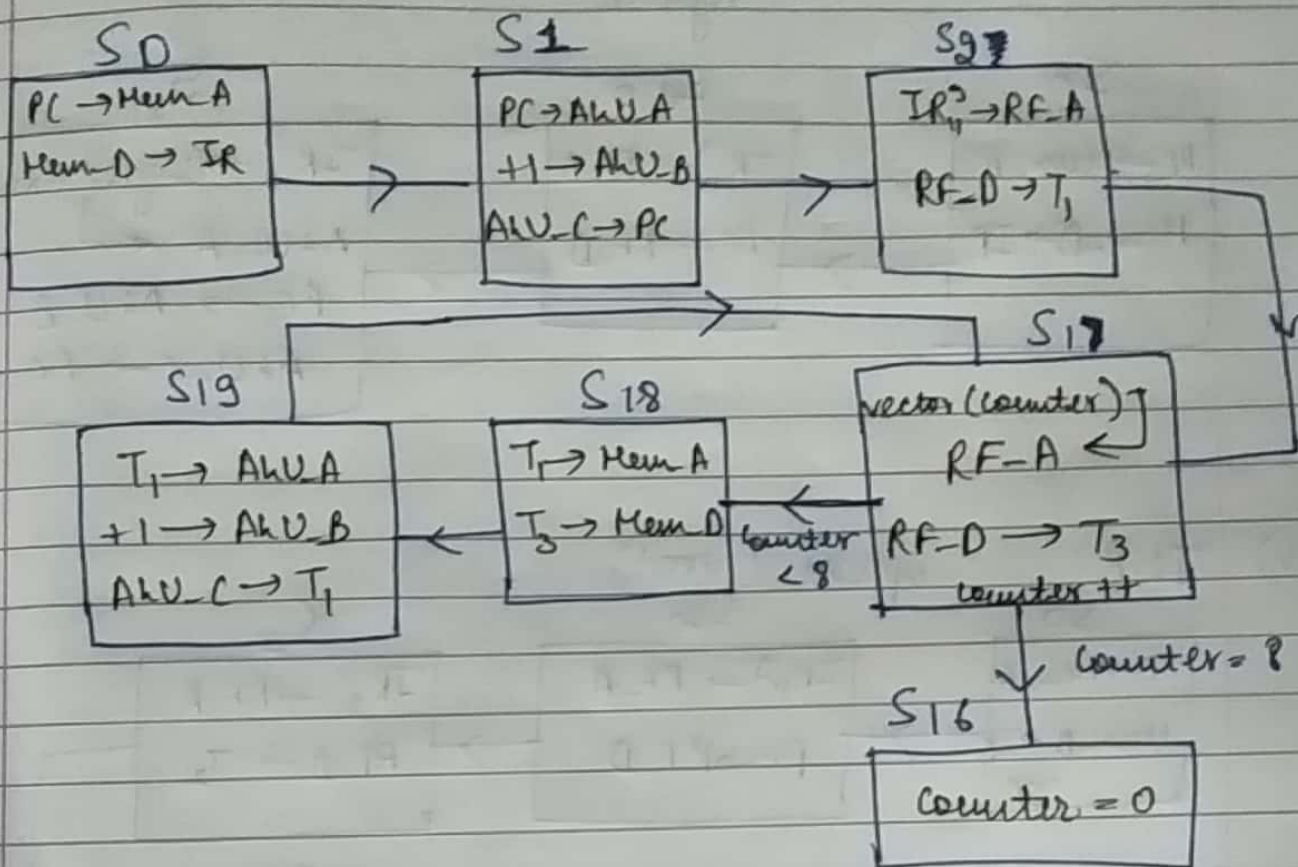
(x) Store opcode (0101)



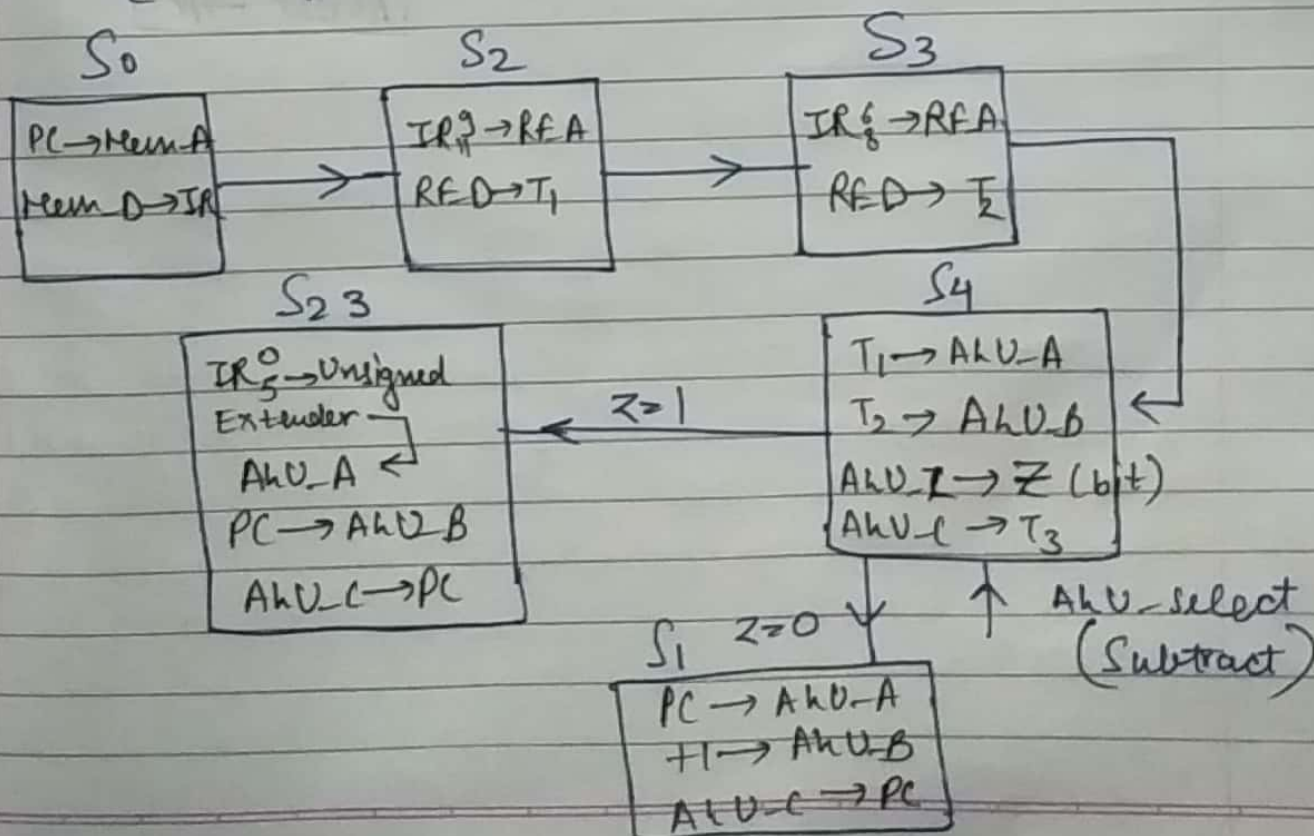
(xi) Load All Opcode (0110)



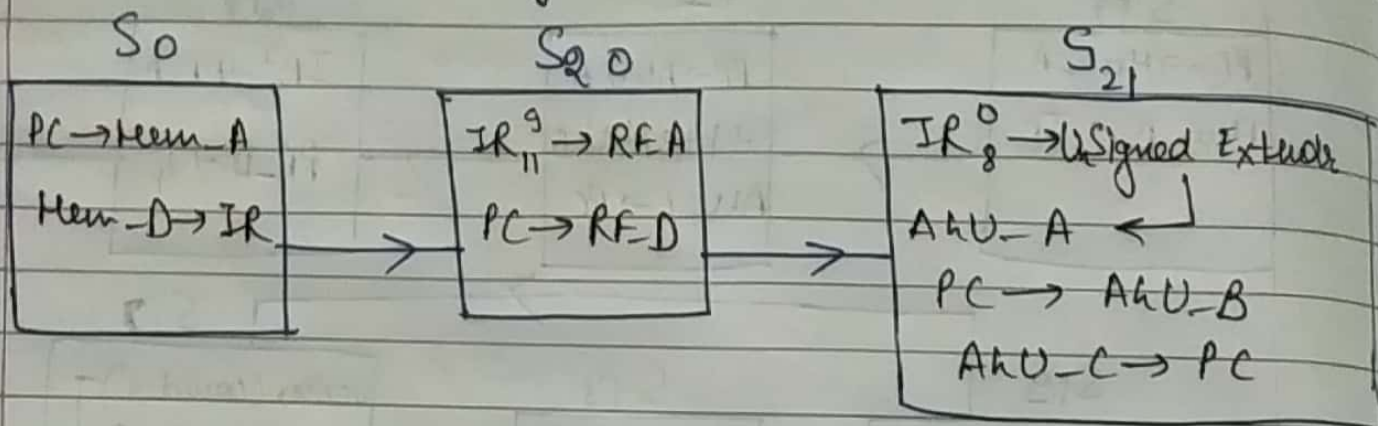
(Xii) Store All opcode (0111)



(Xiii) BEQ opcode (0111)



(XIV) JALR Jump and link register opcode (1000)



X(V) JLR opcode (1001)

