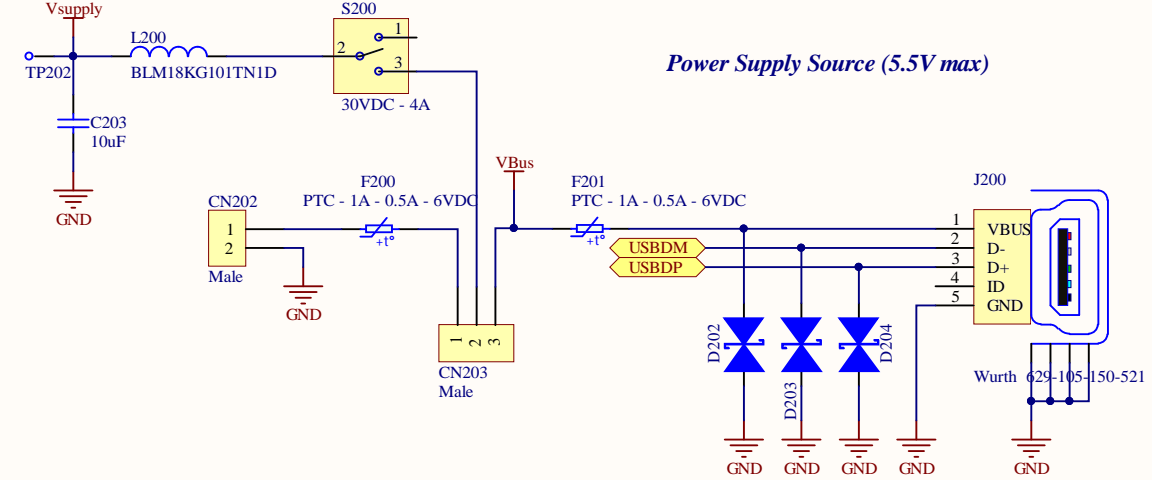
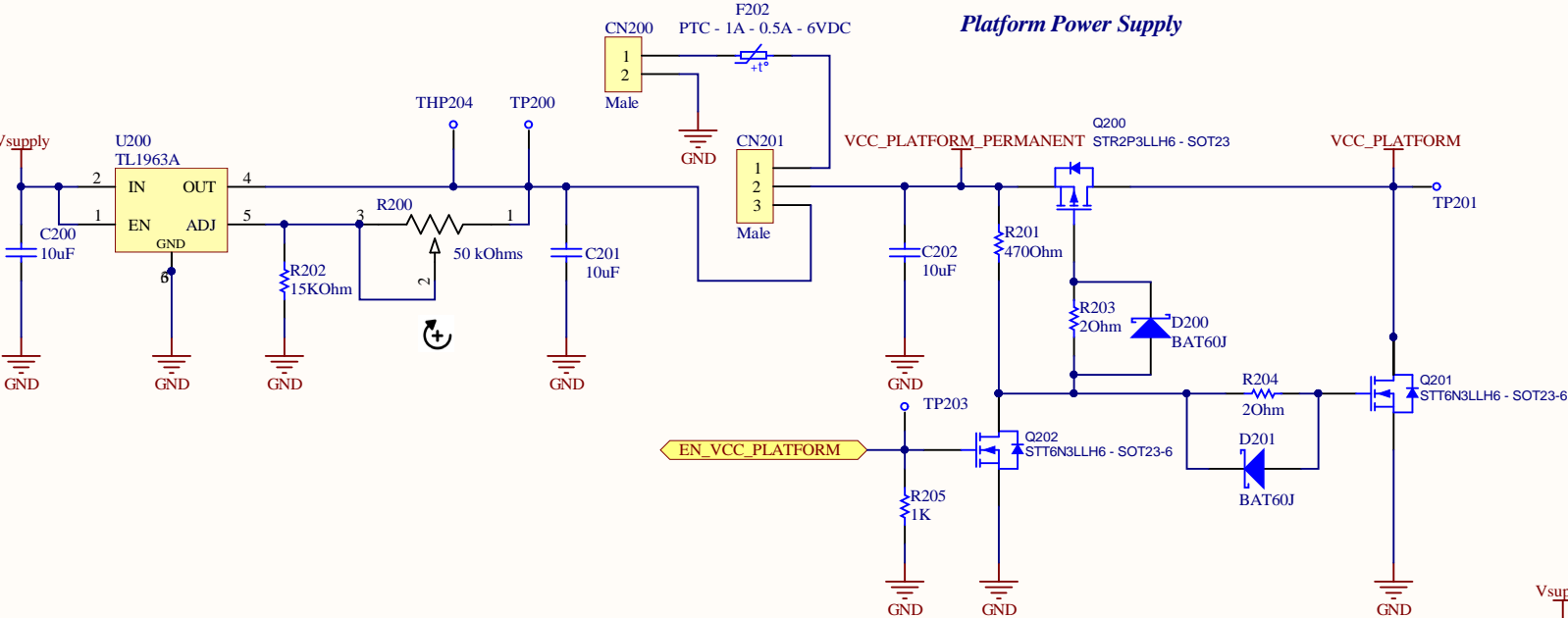
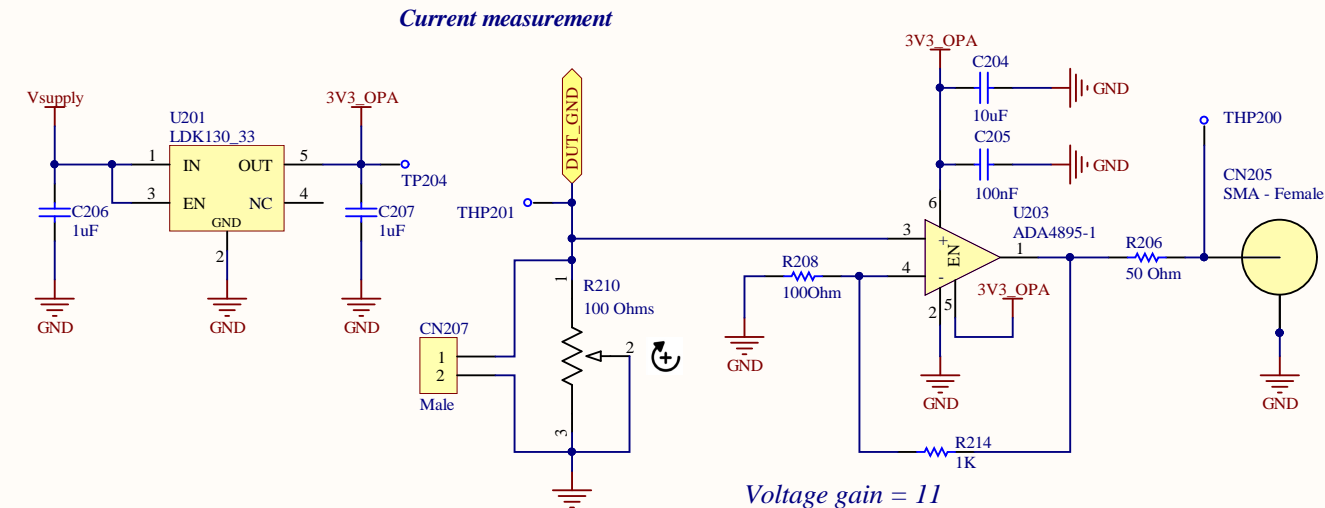
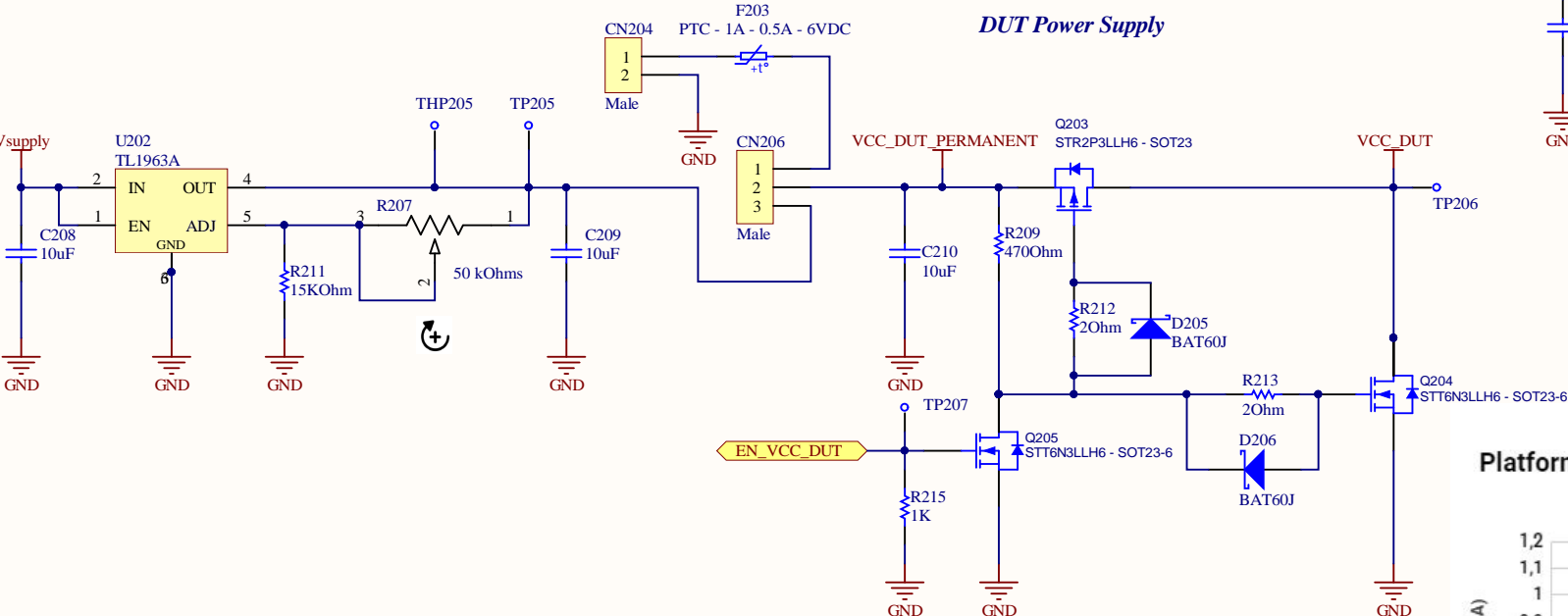


Platform supply voltage can be adjusted from 1.21V to Vsupply - Vdropout

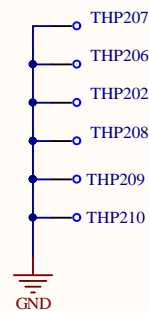


DUT supply voltage can be adjusted from 1.21V to Vsupply - Vdropout

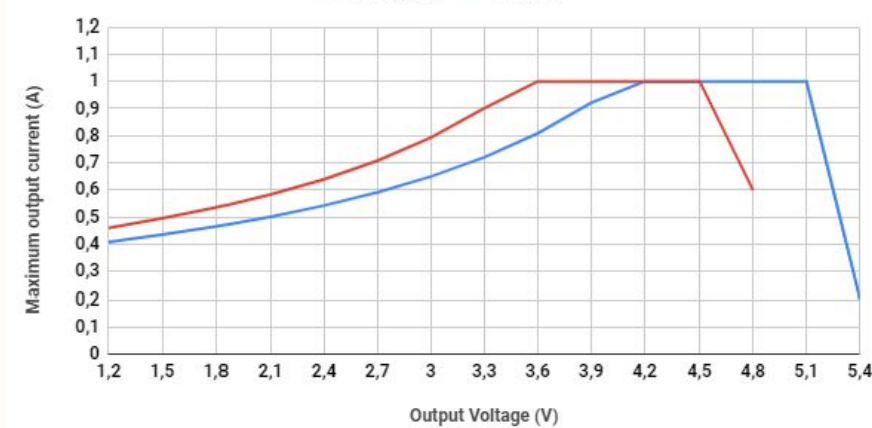


Voltage gain = 11

Ground scope probes

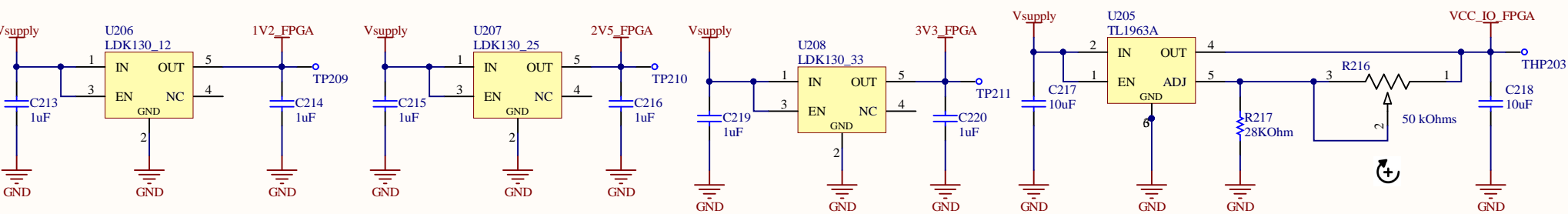


Maximum output current vs Output voltage of Platform and DUT linear regulators (U200 and U202) (1A fuse input)

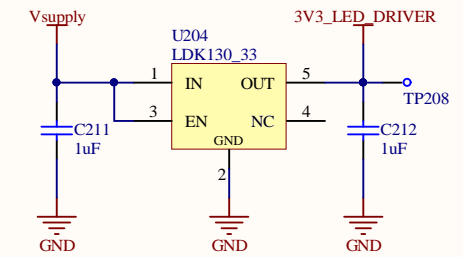


FPGA Power Supplies

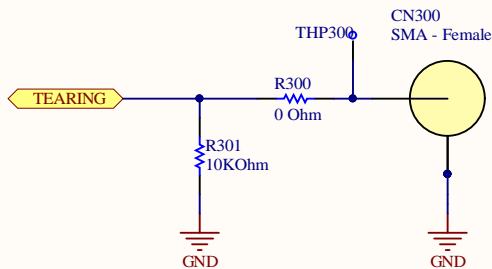
FPGA I/Os voltage of banks 4 to 7 can be adjusted from 1.21V to 3.37V



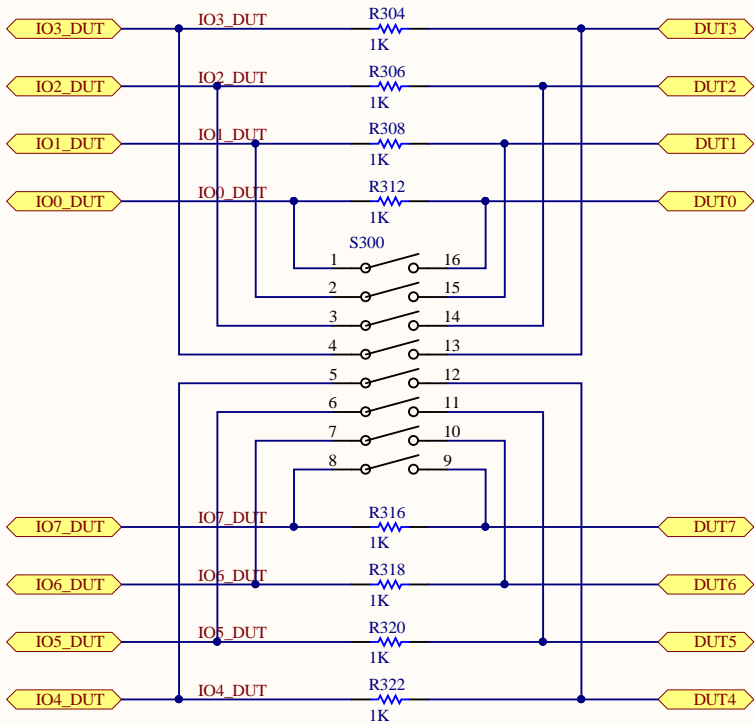
LED Driver Power Supply



Tearing SMA connector

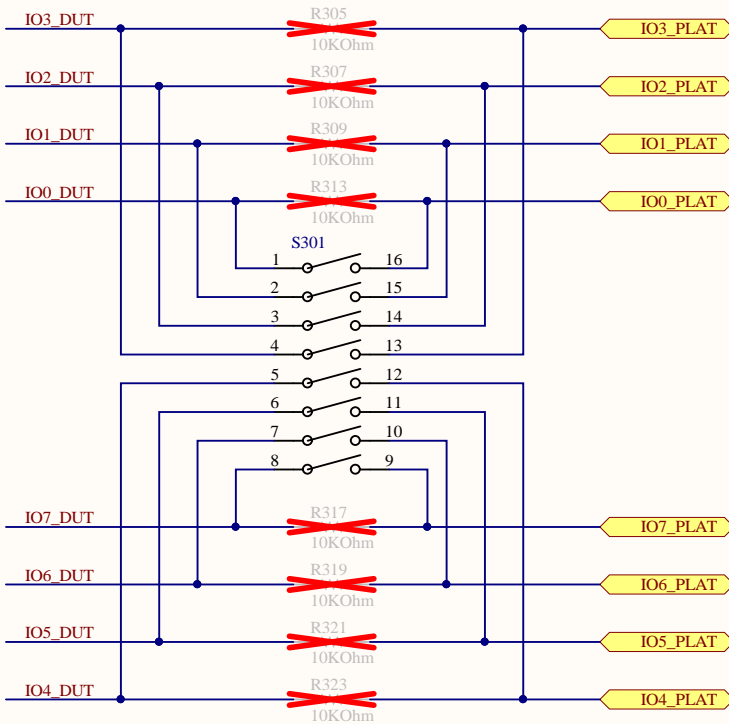


Glitch protection



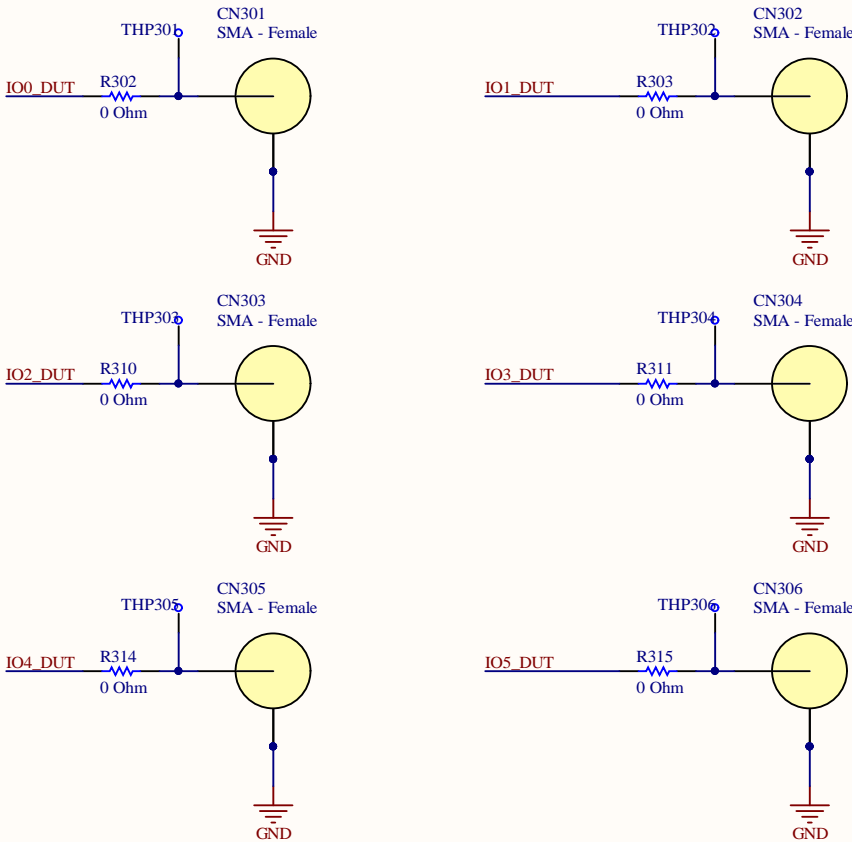
Misc - DIP switch - 8 rows - TE connectivity - 1825057-7 -ADE0804

DUT to platform DIP switch

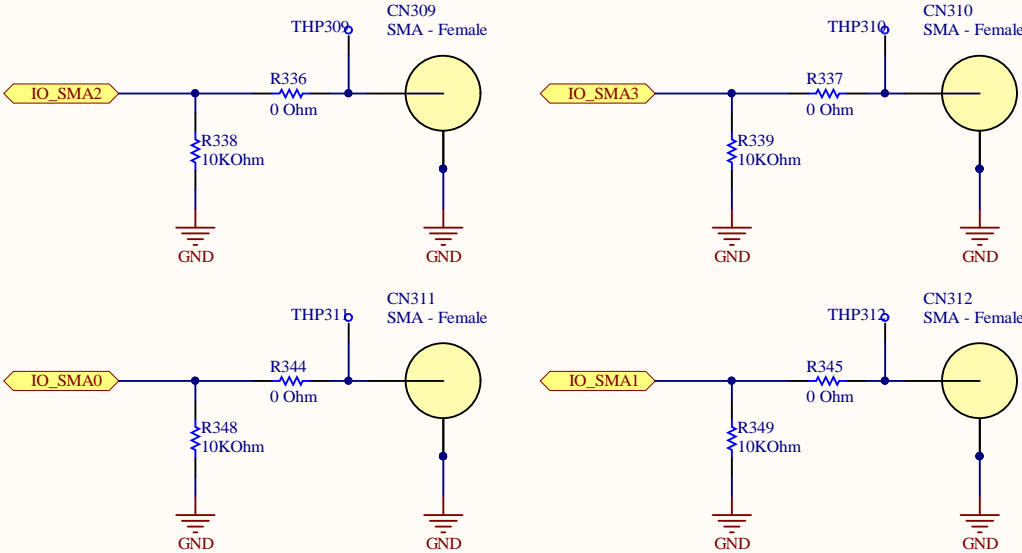


Misc - DIP switch - 8 rows - TE connectivity - 1825057-7 -ADE0804

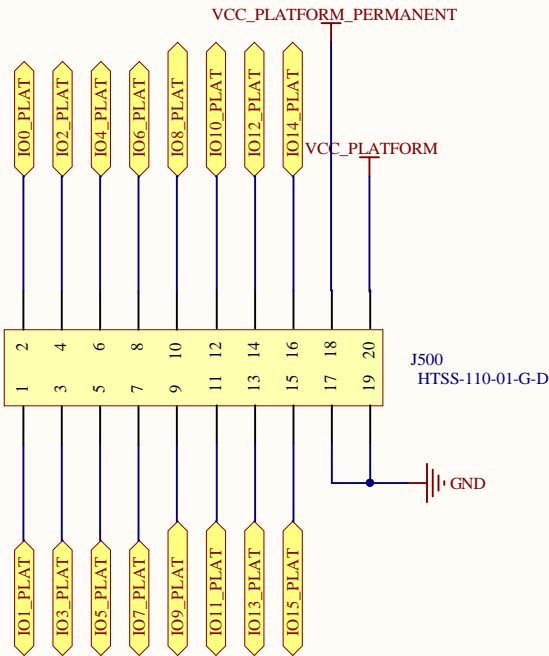
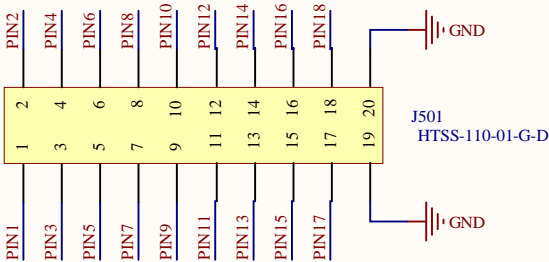
DUT IOs SMA connectors



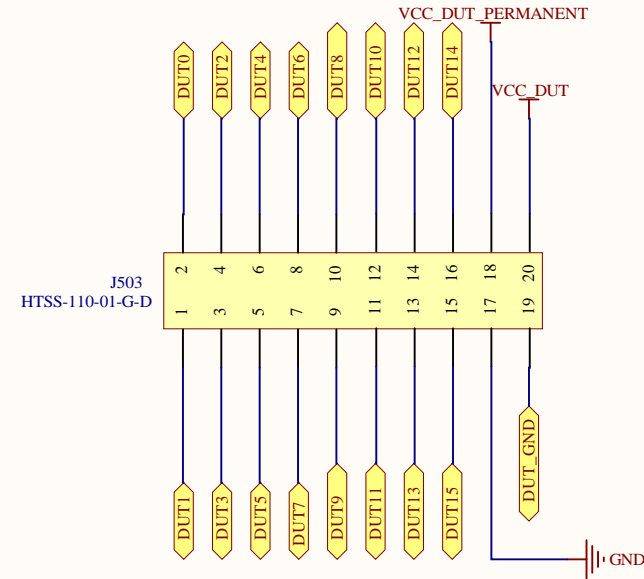
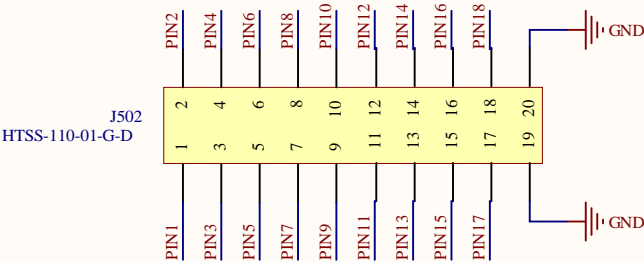
GPIO SMA connectors



Platform Connectors



DUT Connectors



Title: **Scaffold**

Size: A4 Revision: 1.1

Date: 26/06/2019 Sheet 6 of 7

Drawn by: G.Levant

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