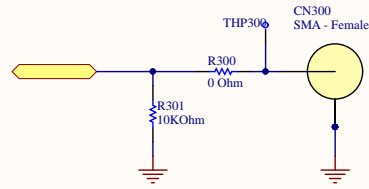
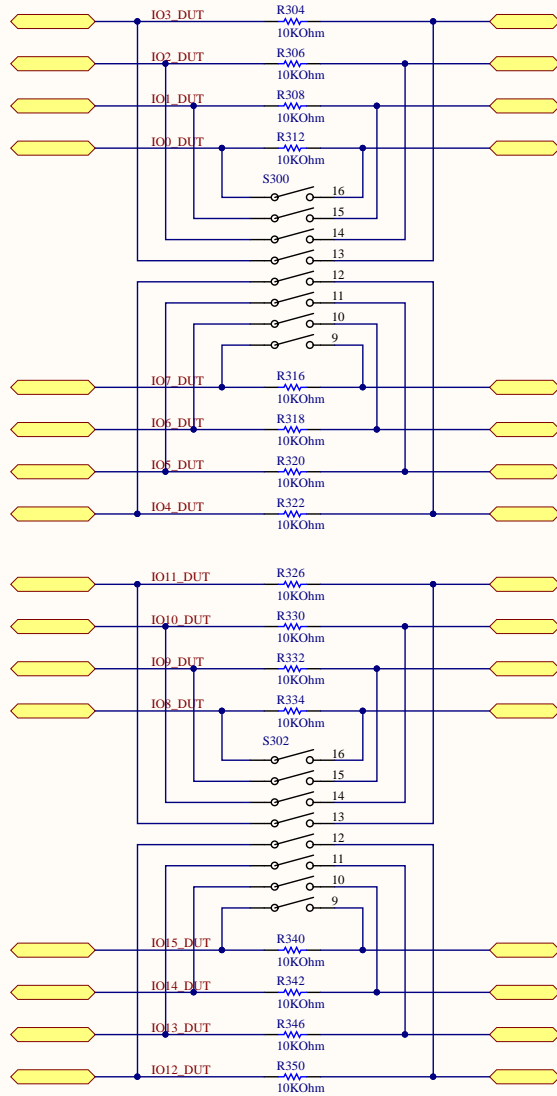


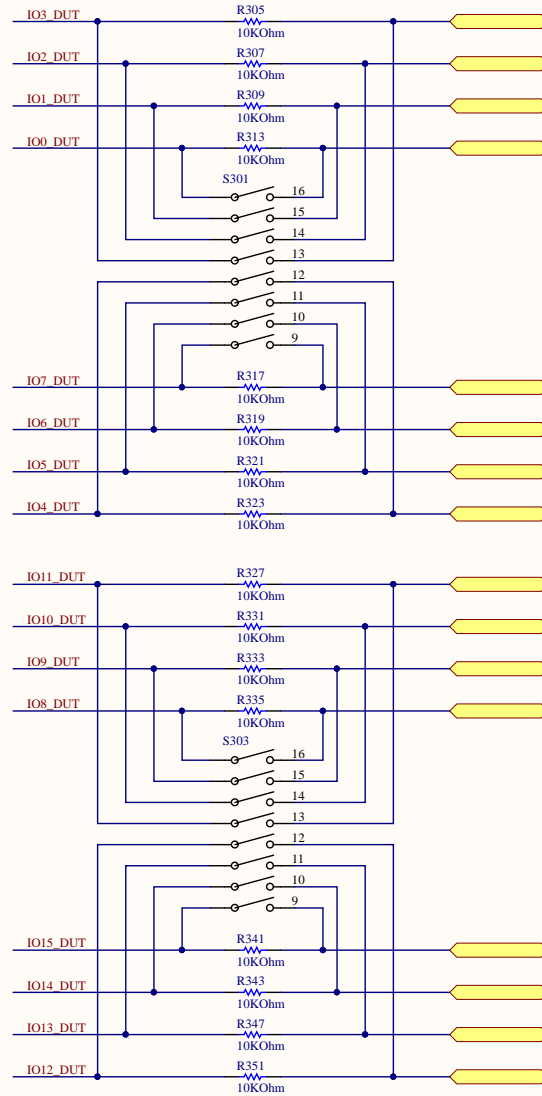
### Tearing SMA connector



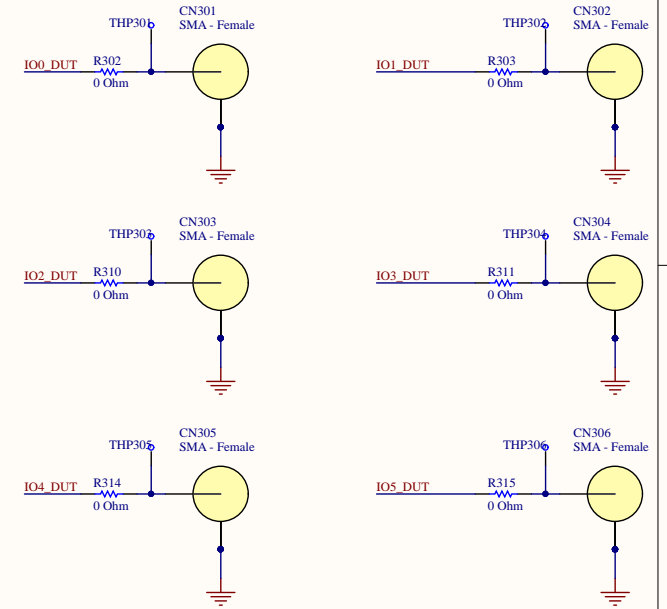
### Glitch protection



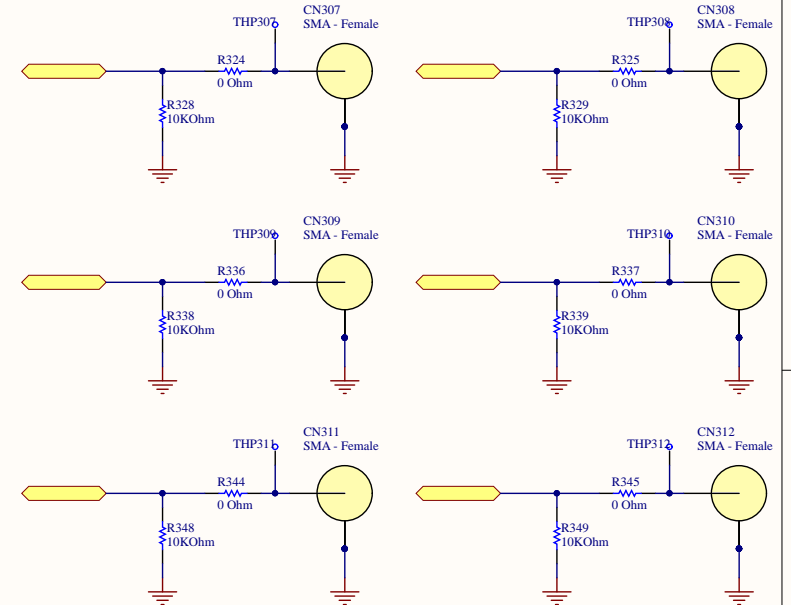
### DUT to platform DIP switch



### DUT IOs SMA connectors

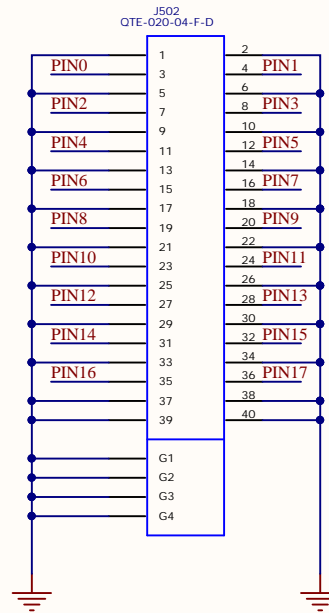
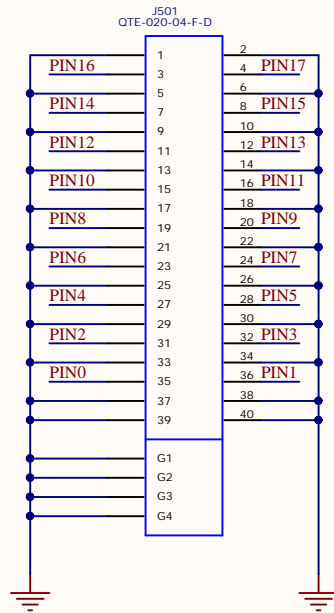
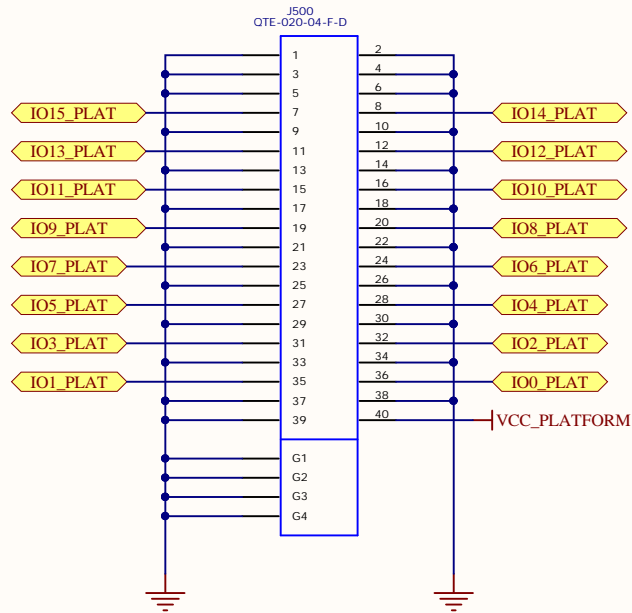


### GPIO SMA connectors

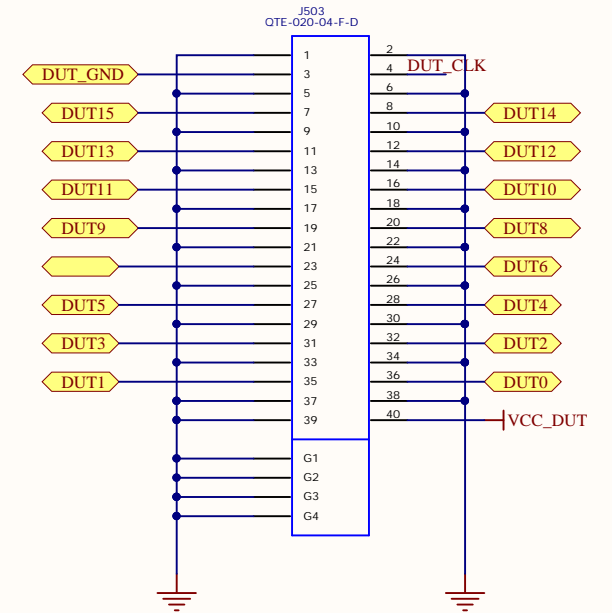


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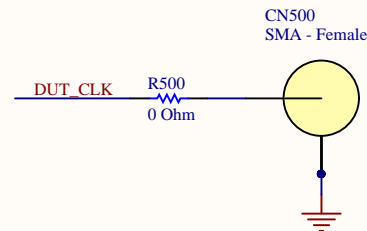
### Platform Connectors



### DUT Connectors

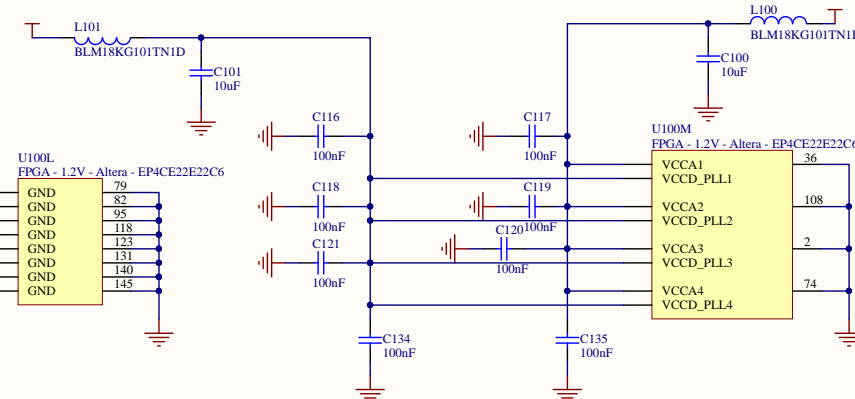
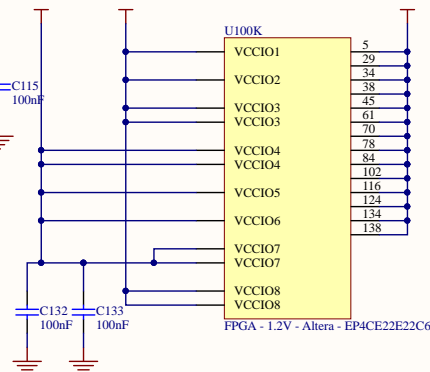
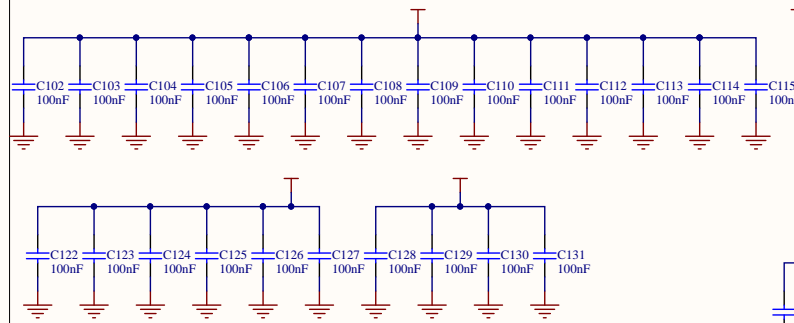


### External Clock Source for DUT

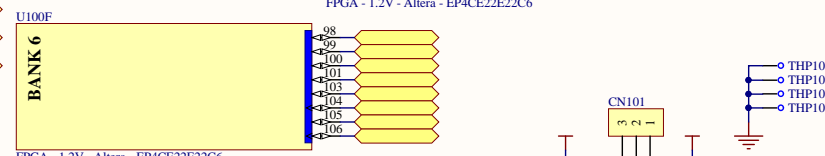
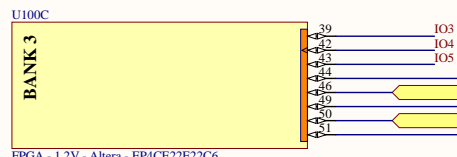
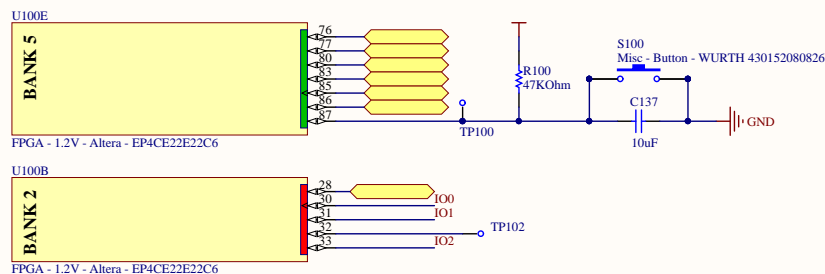
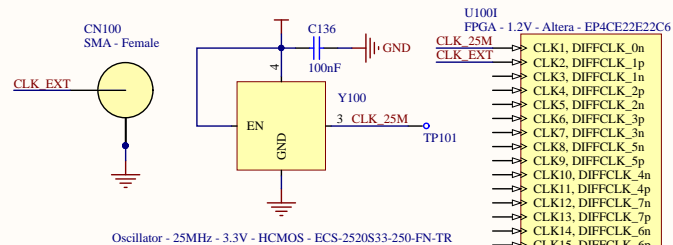


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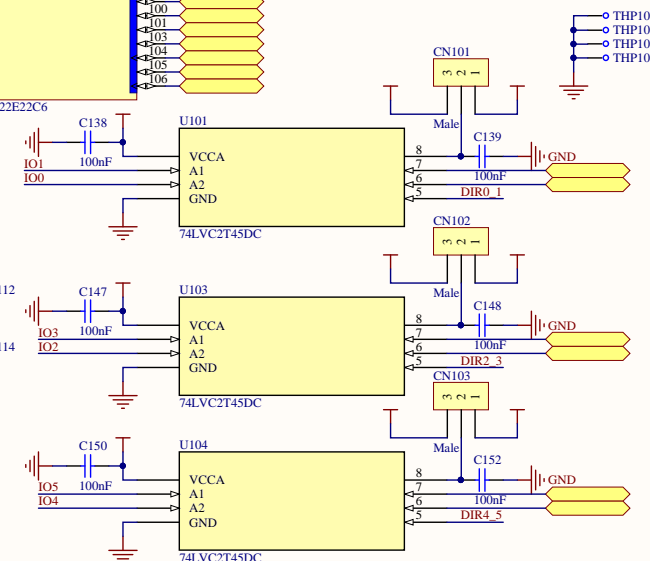
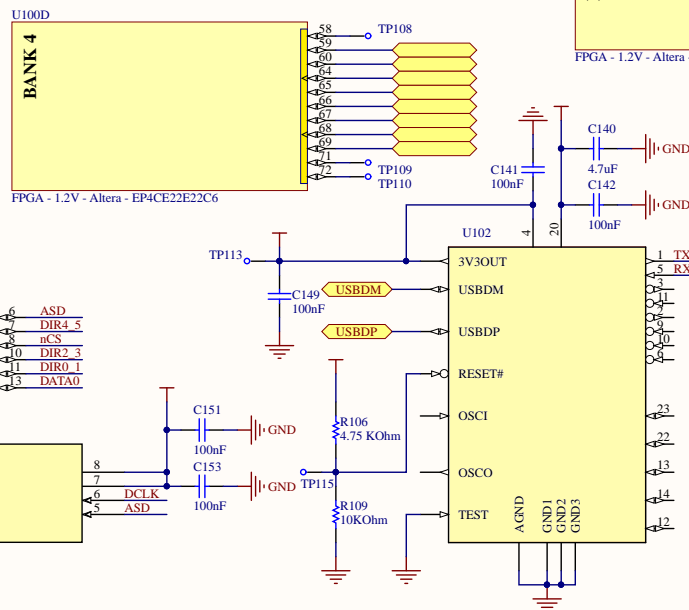
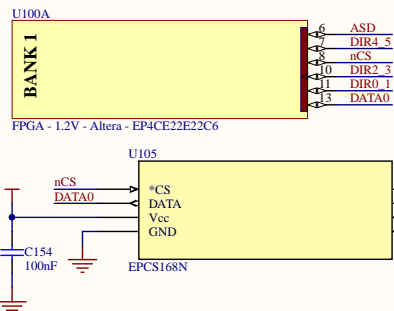
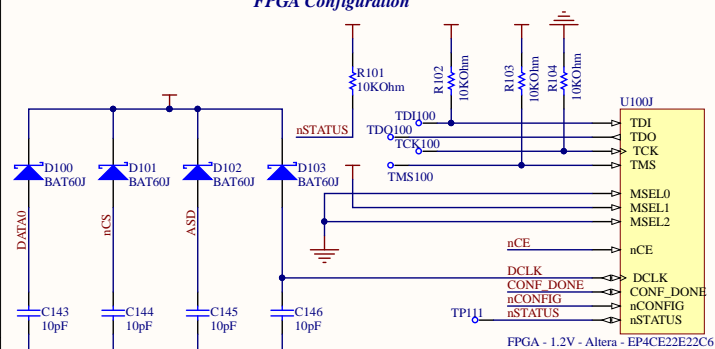




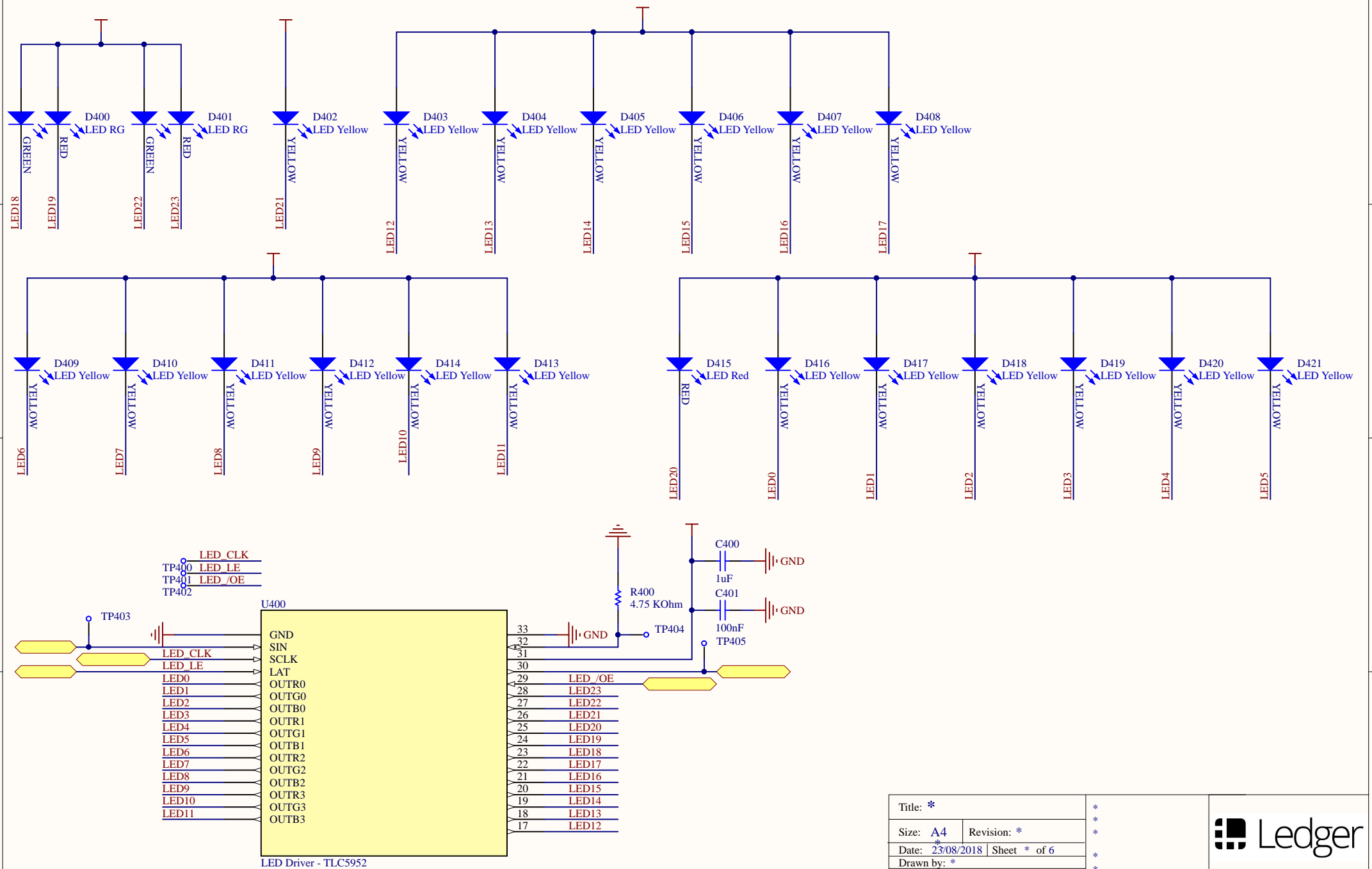
### Clock management



### FPGA Configuration



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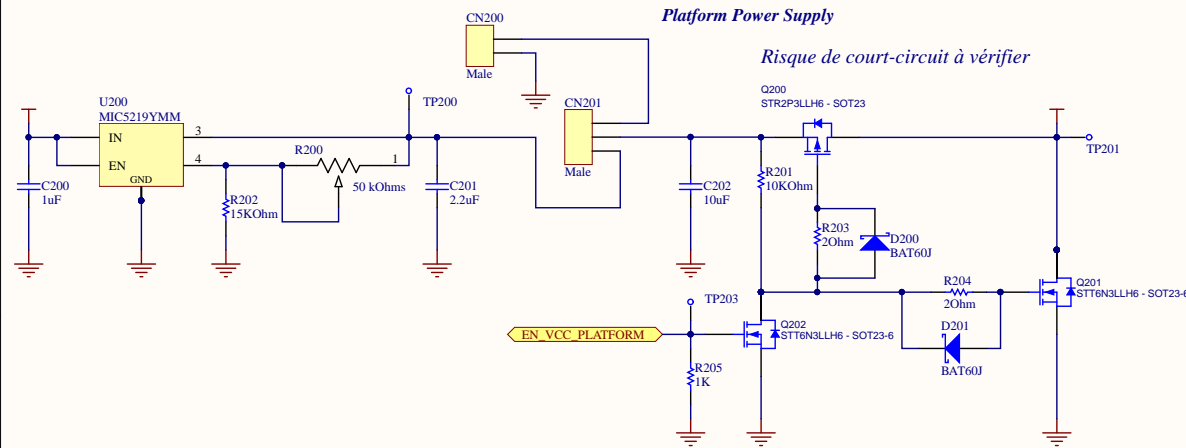


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Approved by: *		*

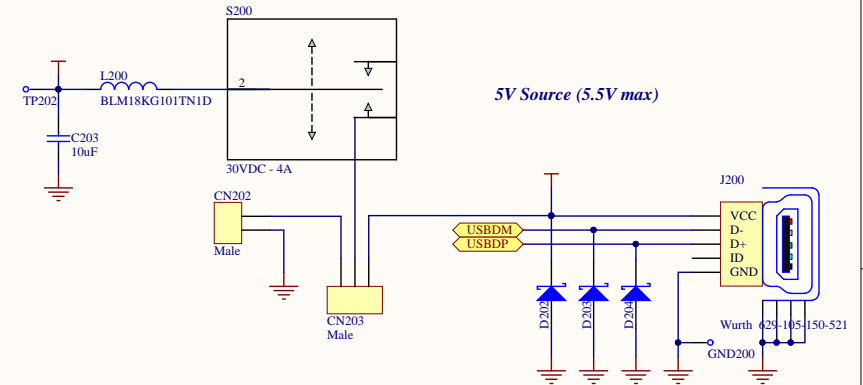


*La tension d'alim de la plateforme peut être ajustée de 1,24V à 5,1V (0,5A max)*

### Platform Power Supply



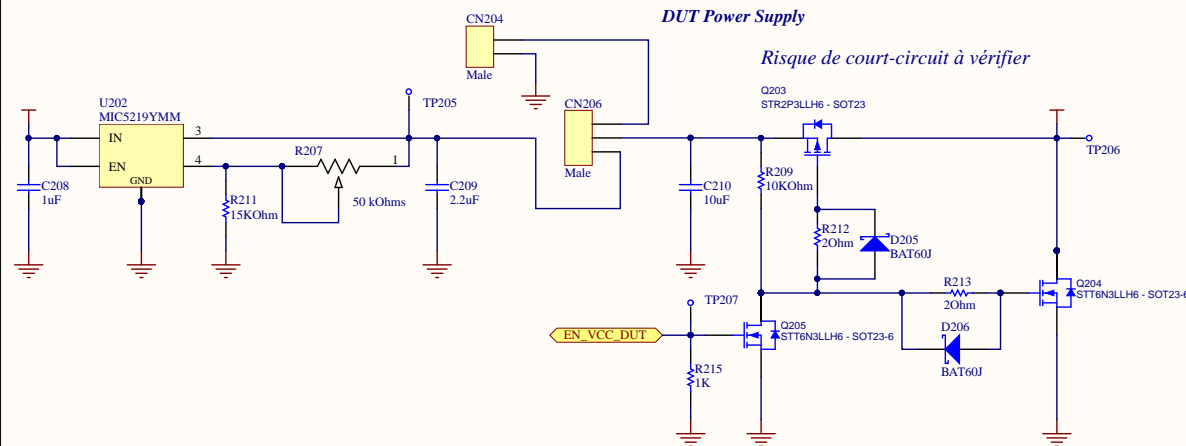
*Risque de court-circuit à vérifier*



**5V Source (5.5V max)**

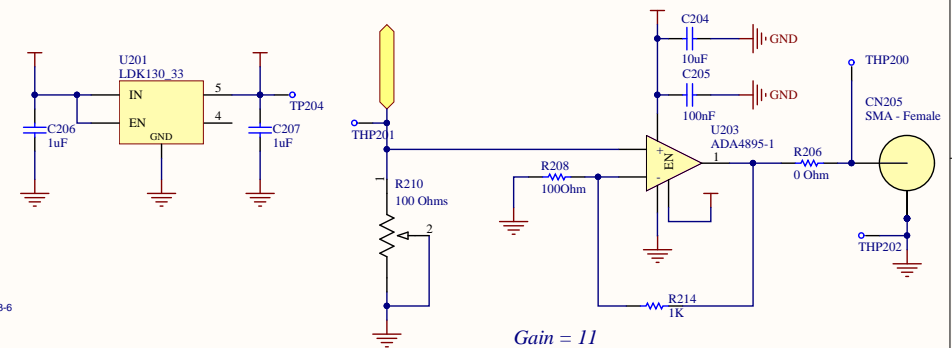
*La tension d'alim du DUT peut être ajustée de 1,24V à 5,1V (0,5A max)*

### DUT Power Supply



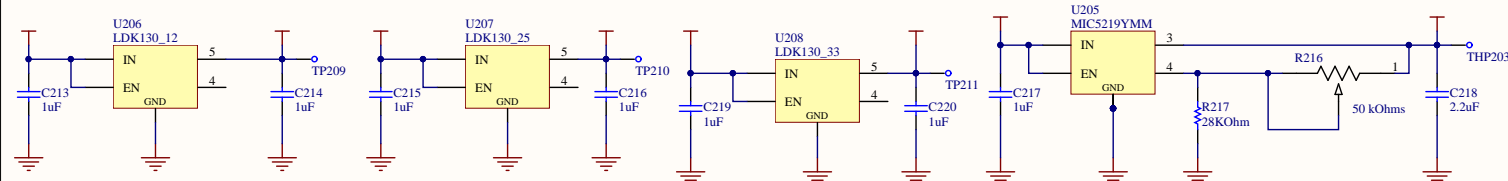
*Risque de court-circuit à vérifier*

### Current measurement

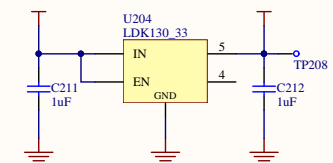

$$Gain = 11$$

### *FPGA Power Supply*

*La tension des IOs peut être ajustée de 1,24V à 3,33V*



### LED Driver Power Supply



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