

(c) Cyberspice 2022

Cyberspice

Sheet: /
File: ElectronUserPortCartridge.sch

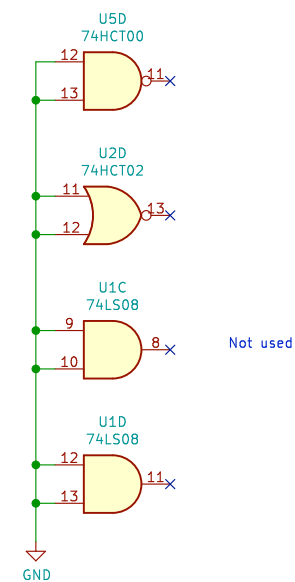
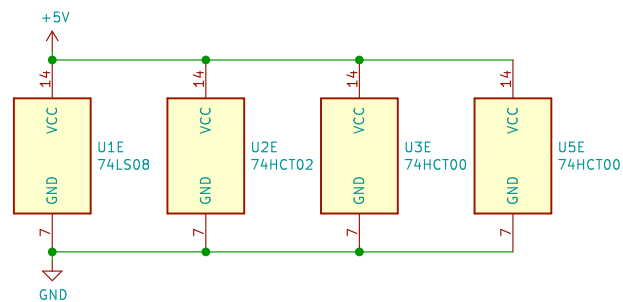
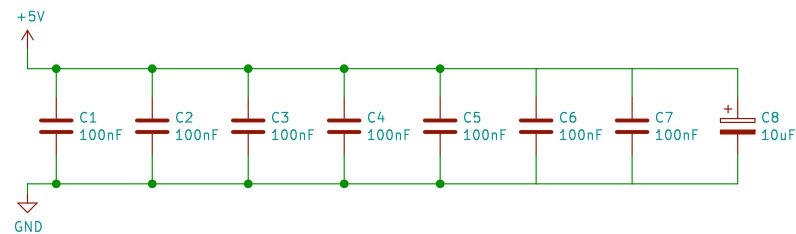
Title: Electron User Port Cartridge

Size: A4 Date: 2022-02-04

KiCad E.D.A. kicad (5.1.9-0-10_14)

Rev: 1.1

Id: 1/4



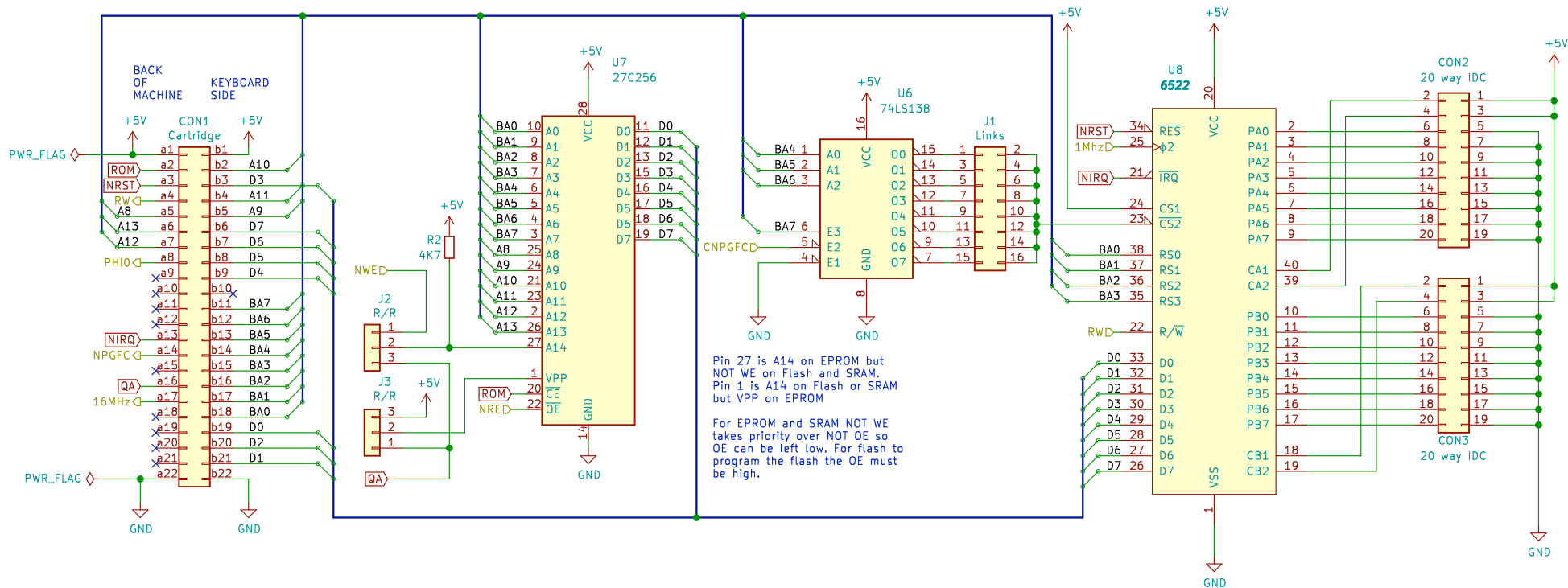
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Sheet: /Power/
 File: Power.sch

Title: Electron User Port Cartridge – Power

Size: A4 Date: 2022-04-08
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Rev: 1.1
 Id: 2/4



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Sheet: /RAMandIO/

File: RAMandIO.sch

Title: Electron User Port Cartridge – Memory and IO

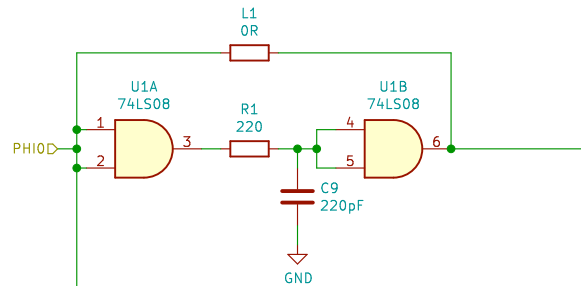
Size: A4 Date: 2022-04-08

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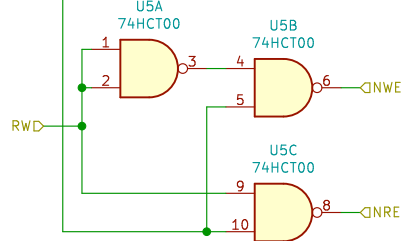
Rev: 1.1

Id: 3/4

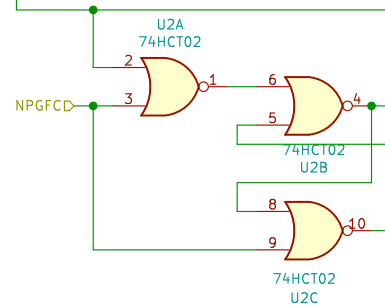
Do not fit IC1, R1 or C9 unless you have clock issues.
This circuit delays the clock by a few nS as the
clock is provided from PHI0 and not PHI2. However
in testing it is not needed. FIT L1.



Derive NOT Read and NOT Write

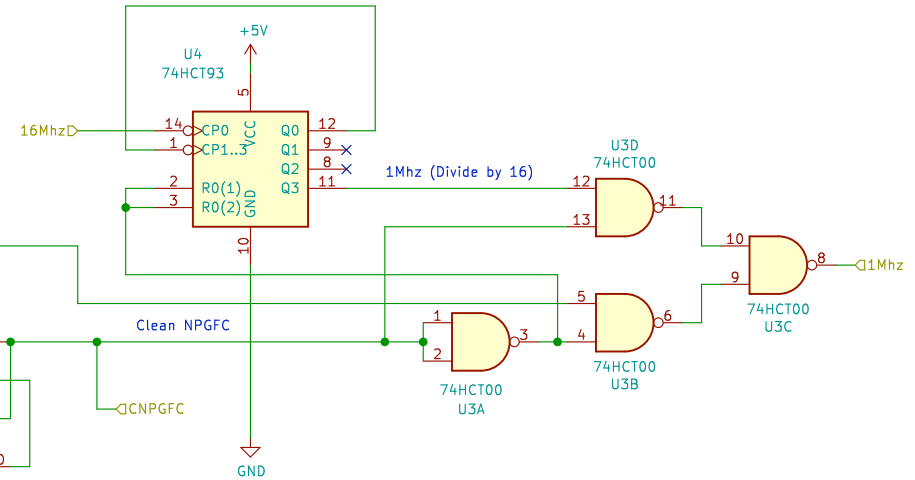


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Clean up the NOT Page FC select line

When IO space is selected the CPU clock
slows to 1Mhz but otherwise the CPU
clock runs at 2Mhz. If we used the clock
directly the timers on the 6522 would be
inaccurate. So derive a 1Mhz clock when
memory rather than IO is selected.



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Sheet: /ClocksAndSignals/
File: ClocksAndSignals.sch

Title: Electron User Port Cartridge – Clock and decode logic

Size: A4 Date: 2022-04-08

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Rev: 1.1

Id: 4/4